Weekly meeting

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IHEP

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Validation task

- 04172015_lowmu

ref1:valid3.119994.Pythia8_A2MSTW2008LO_minbias_inelastic.recon.ESD.e3099_s2578_r6600/

ref2:valid3.119994.Pythia8_A2MSTW2008LO_minbias_inelastic.recon.ESD.e3099_s2601_r6598/

test:valid3.119994.Pythia8_A2MSTW2008LO_minbias_inelastic.recon.ESD.e3099_s2601_r6616/

Ref1: Normal run 2 setup, i.e. narrow beamspot and std tracking (20.1.4.5)

valid3.119994.Pythia8_A2MSTW2008LO_minbias_inelastic.recon.*.e3099_s2578_r6600/

Ref2: Wide beamspot but standard tracking (20.1.4.5)

valid3.119994.Pythia8_A2MSTW2008LO_minbias_inelastic.recon.*.e3099_s2601_r6598/

Test: Wide beamspot with low mu tracking and updated conditions for low mu LCWs, updated dead module maps (SDR-29) (20.1.4.6)

- B-tagging turned off due to crashes

valid3.119994.Pythia8_A2MSTW2008LO_minbias_inelastic.recon.*.e3099_s2601_r6616/

- 04222015-task1

ref:valid3.110401.PowhegPythia_P2012_ttbar_nonallhad.recon.ESD.e3099_s2578_r6540/

test1:valid3.110401.PowhegPythia_P2012_ttbar_nonallhad.recon.ESD.e3099_s2578_r6588/

test3:valid3.110401.PowhegPythia_P2012_ttbar_nonallhad.recon.ESD.e3099_s2578_r6589/

Task 1: Test of MC15-like digi+reco without pileup. The reference is the 20.1.4.3 sample from task 1, test 3 on 10th March.

There are 2 tests: the first is the same as the reference, just updated to 20.1.4.5; the second is the same but with the latest conditions having updated LCWs, dead channel maps and b-tagging calibration.

Ref: s2578_r6540 (19.2.3.3, 20.1.4.3) - no pileup with 25ns calo reco settings (task 1, test 3 from last time)

Test1: s2578_r6588 (19.2.3.3, 20.1.4.5) - as ref but 20.1.4.5

Test2: s2578_r6589 (19.2.3.3, 20.1.4.5) - as test 1 but with SDR-28 conditions containing LCWs, dead channel maps, b-tag calibration.

Others

- wwyy p0 values extraction with 1pb injection

6 root files after submitting jobs

throwing 100k toys at each mass point

- run ISR/FSR samples

produce 20 root files