### Status of HCAL

#### Hongbang Liu (on behalf of the CEPC-HCAL Group)





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- Motivation
- Hadron Calorimeter
  - RPC
  - THGEM
  - GEM
- Electronics
- Simulation & Optimization
- Future plan for CEPC HCAL





Requirements for calorimeter: inside the coil, with thin active medium, with extremely fine segmentation

RPC, GEM, THGEM as sensitive detector for HCAL.



- The HCAL consists of
  - a cylindrical barrel system:
    12 modules
  - > two endcaps: 4 quarters
- > Absorber: Stainless steel



#### Active sensor

- > 6mm thickness
- MIP detection efficiency 95%
- > RPC
  - > 1×1 m<sup>2</sup>
- > Thick GEM or GEM
  - > 0.5×1 m<sup>2</sup>
  - > 1MHz/cm<sup>2</sup>

#### Readout ( 1×1 cm<sup>2</sup> )

- Digital (1 threshold)
- Semi-digital (3 thresholds)



### HCalo detector group meeting

- IHEP, SJTU, UCAS, GXU, XJTU, BISUU, ...
- http://indico.ihep.ac.cn/category/322/
- Calo-Optimization group meeting
  - IHEP、SJTU、UCAS、...
  - http://indico.ihep.ac.cn/category/355/





# Prototypes of DHCAL with RPC



1m<sup>3</sup>, 1 threshold, TB at CERN/Fermilab





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H. Liu @ UCAS, GXU









#### 1. Mechanical drilling or laser drilling





2. Etching: globe etching, mask etching, electrical chemical etching



DHCAL based on THGEM

- Four THGEM options are explored:
  - Double THGEM
  - Single THGEM
  - WELL THGEM
  - Hybrid THGEM
- WELL-THGEM is optimal choice Thinner, lower discharge
- 40 × 40 cm<sup>2</sup> of THGEM (below)

was produced in China (UCAS, GXU, IHEP)







H. Liu , Q. Liu (UCAS , GXU)







#### Gain Uniformity Scan



#### THGEM Microtopography Analysis



Gain Uniformity Testing
 Scan area 60 × 60 cm<sup>2</sup>

- Microtopography Analysis
  - Scan area 30 × 40 cm<sup>2</sup>
  - Find the imperfect holes automatically



- The detection efficiency of well-THGEM was measured with the BEPC pion and proton beams.
- Efficiency:
  - Ar/iso (97/3) ,Gain ~ 2000; Eff (proton) > 93%; Eff(Pion) > 82%
  - Ne/CH4 (95/5) ,Gain ~ 9000; Eff (proton) > 99%; Eff(Pion) > 94%



THGEM Beam Test in Oct., 2015

- 7 THGEMs ware installed, 5 of them were used, and flushed with Ar/isobutane = 97:3.
- 1 threshold, binary readout
- 900 MeV proton/pion beam used
- 5cm x 5cm sensitive region



#### S. Chen , W. Huang(UCAS)







#### GEM assembly using a novel self-stretching technique

#### Jianbei Liu (USTC)



- the main detector R&D focuses at USTC.
- Technology has been developed and matured to produce high-quality GEM detectors as large as  $\sim 1m^2$  for CEPC DHCAL.

→ Resolution uniformity ~11%

4000

T T T

→ Gain uniformity ~16%

3700

3800

→ Can reach gain of 10<sup>4</sup> at 4000V

L L L L

1111

4200

4300

4100

4400

### Conceptual Design for DHCAL GEM

- Detector size: 1m\*0.5m, limited by GEM foil size
- Double-GEM structure (3mm-1mm-1mm) adopted to minimize the thickness of detectors to accommodate the compactness requirement of DHCAL.
- Double-GEM can still produce reasonable gain under safe operation condition according to our measurements and experience.





Mechanical design already finished !



 Starting with 30cm\*30cm. All components have been made ready. Detector to be assembled soon.



Drift PCB and main frame

Readout PCB



- Readout design is underway.
- Using MAROC chips that are suitable to read out GEM/ThGEM.









#### Readout scheme

# Electronics Readout System R&D

ASICs : HARDROC2 64 channels Trigger less mode Memory depth : 127 events 3 thresholds Range: 10 fC-15 pC Gain correction → uniformity



Printed Circuit Boards (PCB) were designed

to reduce the cross-talk with 8-layer structure and buried vias.

Tiny connectors were used to connect the PCB two by two so the 24X2 ASICs are daisy-chained.  $1 \times 1m^2$  has 6 PCBs and 9216 pads.

DAQ board (DIF) was developed to transmit fast commands and data to/from ASICs.



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ASICs : GASTONE



**GASTONE (Gem Amplifier Shaper Tracking ON Events)** 

N channels	64
Chip dimensions	<b>4.5</b> ×
Input impedance	120 🖸
Charge sensitivity	16 m
Peaking time	90 ns
Crosstalk	< 3%
ENC	800 e
Power consumption	~6 m
Readout	Seria

64  $4.5 \times 4.5 \text{ mm}^2$   $120 \Omega$ 16 mV/fC ( $C_{det}$ =100 pF) 90 ns ( $C_{det}$ =100 pF) < 3%800 e<sup>-</sup>+40 e<sup>-</sup>/pF ~6 mW/ch Serial LVDS (100 Mbps)

Q. Liu (UCAS)





## DHCAL Detector Optimization

- DHCAL Simulation and Optimization
  - Standardalone → Full simulation
    (eg. single particle, ZH→qqbb events)
  - Number of layers
  - Thickness of Absorber
  - Readout cell size
  - **Thresholds** (1, 3, ..)



#### D. Hong(GXU, UCAS)



1 threshold-40 layers

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- Detector design and optimization
- Granularity of calorimeters
- Number of layers of calorimeters
- Absorber thickness
- Detector R&D(RPC/THGEM/GEM+Iron)
- Readout electronics (PCB, low power VFE ASIC)
- Gas recirculation system
- High voltage distribution system
- Calibration system
- Mechanical: self-support and compact module



# Many thanks to all members of the CEPC HCalo working group.

### **THANK YOU !**