HGTD t0 calibration

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most of the material from here : <u>TDR</u> and <u>Emma's talk</u>





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<u>HGTD overview</u>





Why to calibration?

- The time of arrival of a hit measured in the HGTD will be different between pixels
 - Need calibration to keep expected time resolution of 30–50 ps
- Static variations ie ToF, flex cable length, etc
- **Dynamic variations** ie day/night variation of the LHC clock
- Corentin's study: plot timing distribution of simulated hits in Zee sample
- Can calibrate pixel t_{hit} using mean of distribution



Gaussian core derived from the time dispersion of the LHC collision => Precision of a few ps can be reached with 10000 hits per pixel



Timing Variation

- LHC per-bunch variation: using ATLAS collision time offset from paper
- Felix jitter: taking 5.19ps jitter from timing performance study
- Three separate random Gaussian 5ps jitter to model **IpGBT, FLEX,** and **ASIC**
 - Temporary numbers to be updated when timing performance studies are available
- Static radially-dependent ToF variation of 0-70 ps
 - No significant impact on these per-sensor hit distributions



Taking measurement as offset for full smear





Timing variation

Can't calibrate away event-by event random fluctuations

- (But are included in study anyway)
- Performance of calibration procedure will depend on how many longer-term variations (heat cycles or other effects) affect the time measurement, but these are largely unknown...
 - Instead parameterize these long-term variations and study how large / fast they could be and still meet our targets
 - Sinusoidally varying 100ps time offset with period of $100\mu s$ 1day



<u>Details</u>

- Code forked from hgtd-simulation gitlab
 - <u>https://gitlab.cern.ch/etolley/hgtd-simulation/tree/master</u>
- Samples: /eos/atlas/atlascerngroupdisk/det-hgtd/samples/aod/ 20.20.10.7/Zee/MU200/
- Assuming event readout rate of 1MHz

actual calibration works with inputs: time and positions of hits all the jitter component are emulated with smearing radius and pad size define by hands

no additional information about the events structure (eg Bunch-crossing)



Calibration procedure

t0 from a 15x15 grid of channels —> corresponding to 1 ASIC









Detailed procedure I



- Calibration procedure tries to set mean of timing distribution to zero
- However, true timing distribution does not necessarily have zero mean
 - Distribution has long tail on LHS
 - Spikes/structures from low min bias stats (should be fixed in new samples)
- Average offset & therefore mean of histogram will be correlated with t_true



Detailed procedure II

Calibration performance depends on timescale of effects being calibrated away....



70000 60000╞ calibration timescale << variation timescale 50000E calibration works 👍 30000₽ 20000 10000E _200 100 150 -150 -100-50 0 50 200 t_{hit} - t_{true} [ps]



Detailed procedure III





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Detailed procedure IV





Detailed procedure V





<u>Summary</u>







ASIC to precision for different clock variations

Hit time resolution $t_{\text{smear}} - t_{\text{reco}}$ after the calibration procedure as a function of the variation period, and for several different choices of calibration window time, shown for r = 150 mm, R = 350 mm, and r = 450 mm.

NB. difference between the 3 plots the statistic collected in the pad considered

Smaller calibration window sizes can reduce t_0 jitter when shorter-term variations affect the hit time. However, longer calibration windows, which can collect more statistics and therefore more precisely determine t_0 , result in a better hit time calibration.

In general, more accurate calibrations can be calculated to correct for longer-term variations, and should result in smaller total clock jitter.

How to improve?

The optimal calibration window size is a function of both the period of the variation to be corrected and the hit rate of the channel. More hits collected in a longer time period result in a better calibration, but the calibration window size needs to be small compared to the scale of the variation.

- When accounting for the expected jitter from components of the readout system and LHC bunch crossing time drift, the clock jitter of approximately 10 ps can be reached. If add unknown sources of jitter the calibration procedure can reduce the total jitter to 20 ps for the time variations studied.
- This calibration procedure uses conservative values of clock jitter contributions.
 Conservative estimates for the expected ALTIROC and FLEX timing jitter were used
 - improving input clock variation smearing
 - improving simulation and get more information
 - rate/hits relation, bunch-crossing, pileup

Online/Offline Calibration:

- The practical constraints of this plan (ie computational cost and data size of calibration, compression scheme) how much data would this generate (numbers * bytes per number *frequency)
- then we compress the data because it will be too much to write out completely. one possible compression scheme: only write out the constant for an ASIC when it changes
- then we apply the calibration at the TierO during the usual calibration loop, NN hours after data-taking (edited) The \$t_0\$ correction may be calculated online in the FELIX, or offline. The calibration will be applied offline.



backup

Time resolution

$$\sigma_{\text{total}}^2 = \sigma_{\text{L}}^2 + \sigma_{\text{elec}}^2 + \sigma_{\text{clock}}^2$$

1) Sensor : 20-25 ps non irradiated, 40-50 ps after irradiation

- Electronics jitter : < 25 ps
 TDC 20 ps lsb → < 5 ps
 Time walk after TOT correction < 10 ps
- 3) Clock and calibration :
 - local jitter < 10 ps
 - channel to channel intercalibration < 10 ps





Any jitter or long term stability on the 40 MHz clock will induce a time resolution degradation

All clocks inside ASIC ALTIROC built from the 40 MHz encoded in the fast command of the IpGBT

IpGBT clock jitter : < 5 ps ?



ALTIROC and CLOCK

Input 40 MHz decoded from IpGBT

PLL to build 320 and 640 MHz clocks

Phase shifter common to 40/320/640 MHz (similar to phase shifter in 65 nm for IpGBT), accuracy ~100 ps 40 MHz phase adjusted as TDC active only over +1.25 ns around BC Luminosity window (3.125 ns) centered on BC

Another PLL after phase shifter is jitter too large

Clock distribution to each channel as synchronous as possible

Internal pulser



T0 calibration

Static contribution :

- Clock distribution in ASIC (similar in each ASIC)
- Different path length of the flex (e-link) as a function of ASIC position
 - \rightarrow can be measured with internal pulser, should stable with time
- Average Time of Flight of particles as a function of radius
 - \rightarrow can be computed, and measured with data.
 - (partially compensated by flex length)

Dynamic contribution :

- Any dependence of the clock from USA15 to peripheral electronics (@-30 °c)
 - Very fast variation, seen as jitter on individual channel : cleaner ?
 - Phase variation (\rightarrow T0) :
 - Long/medium term (as day/night variation of 200 ps in ATLAS.)
 - → use data to correct/monitor
 - Send back to USA15 the 40 MHz to compare with master clock ?



T0 calibration with data



Inclusive hit time distribution in a pixel : rms 240 ps

→ Can measured T0 of pixel to better than 5 ps with 10000 hits

Taken into account occupancy, it corresponds to 10⁵ events at inner radius and 10⁶ events at higher radius

Assuming trigger rate of 10 kHz

→ Will take 10s at inner and 100 s at higher radius

→ As static contribution in ASIC can average t0 over ASIC (225 channels) → every 50 ms if needed

Any variation larger than 20 kHz not corrected with data calibration



- Can also count hits from neighboring sensors
- Read by same ASIC => subject to the same time jitter and offsets





- Can get a precision of ~10ps with ~1500 hits, ~3ps with 10k hits
- However, hit rate is geometry dependent => need over 500k
 events to get ps level precision at higher radii





