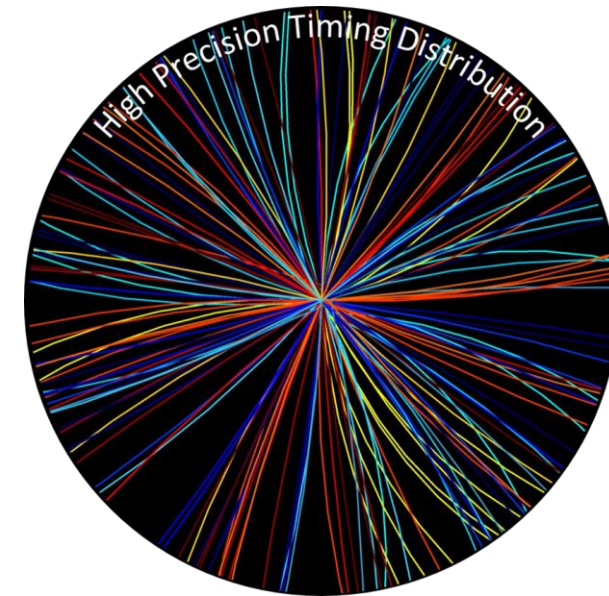


High-Precision Timing Distribution Systems for LHC experiments

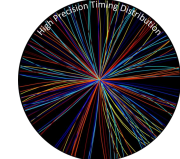


Eduardo Mendes
CERN EP-ESE

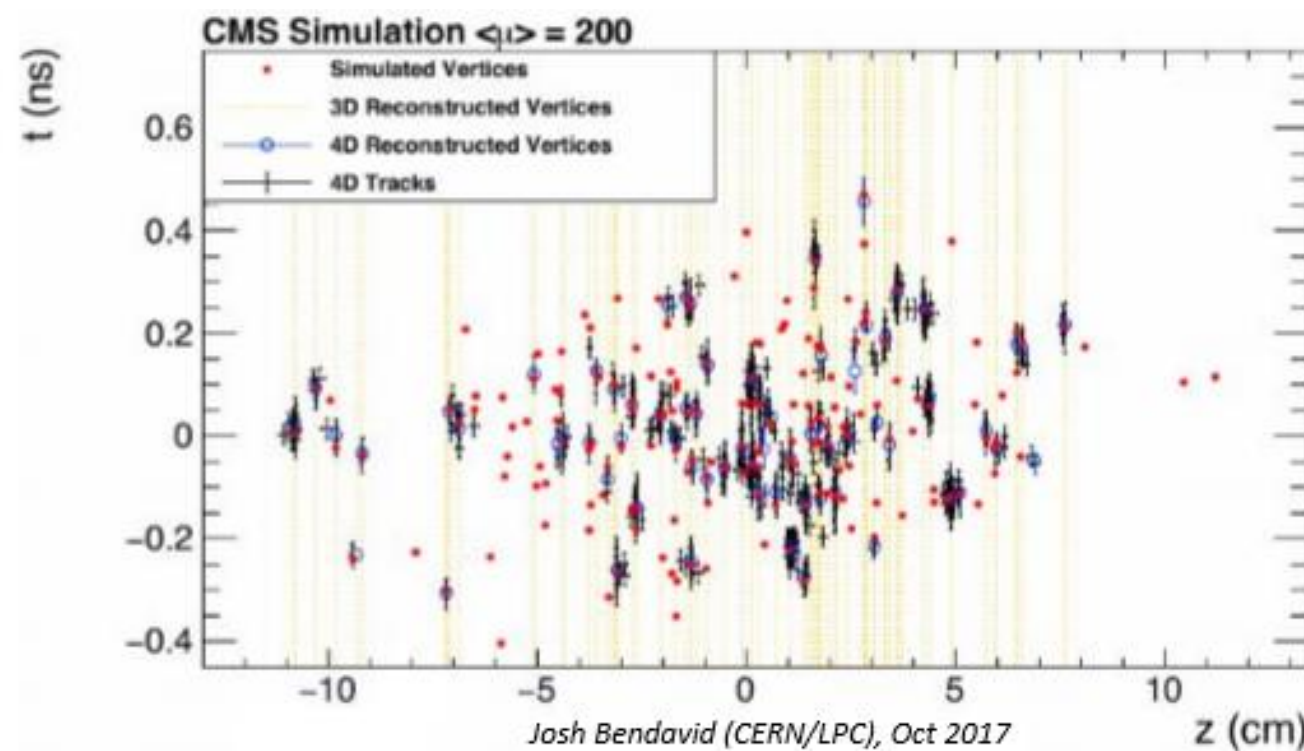
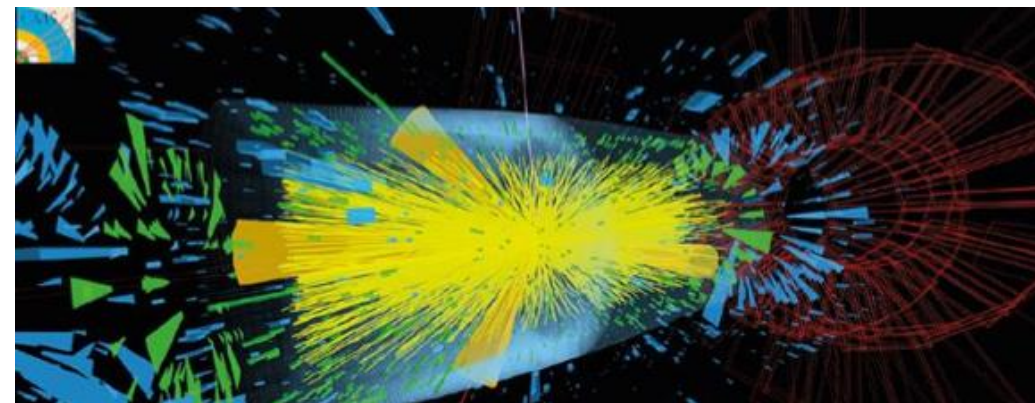


- Thanks to the many colleagues who contributed directly or indirectly to this talk

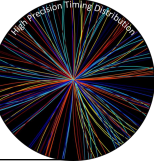
New playing rules of the HL-LHC



- High Pile-up expected for HL-LHC
 - From 20 (LHC nominal) to ~ 200 (HL-LHC)
- Adding a timing-layer to the event reconstruction can reduce the effective pile-up
- CMS and ATLAS experiments will adopt such strategy



Agenda

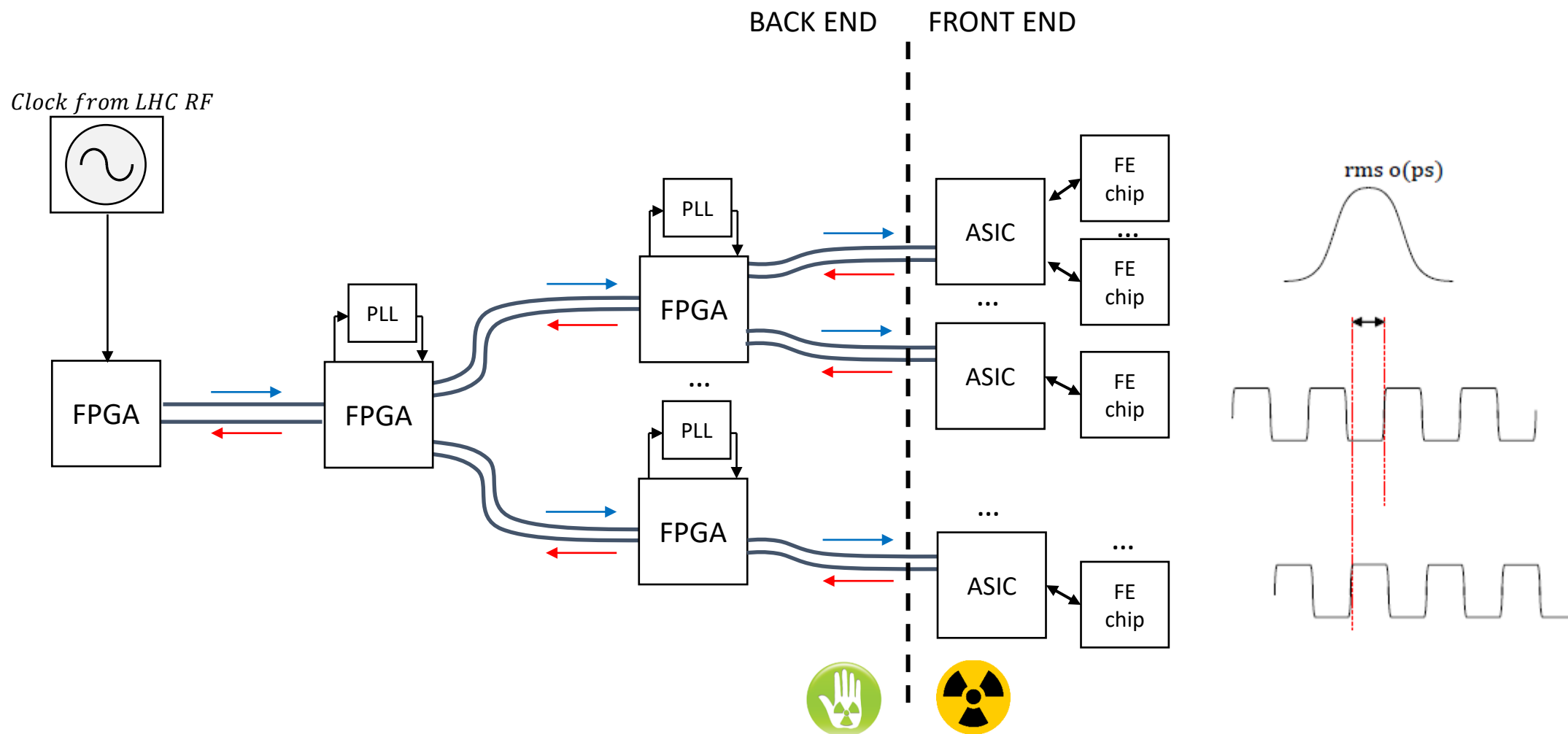


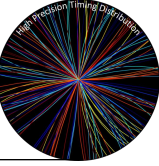
- Introduction
- Timing distribution systems
 - Front-end
 - Back-end
- Conclusions

Timing distribution for LHC experiments



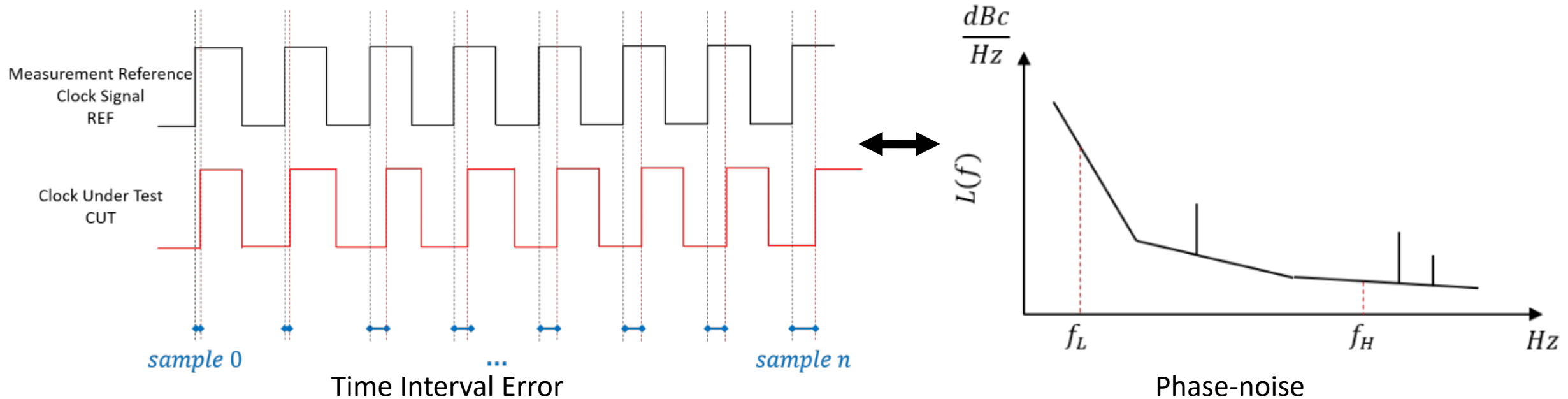
- Baseline **Timing**, trigger and control system based on high-speed optical links



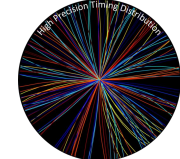


Measuring clock stability

- Jitter - fast phase variations



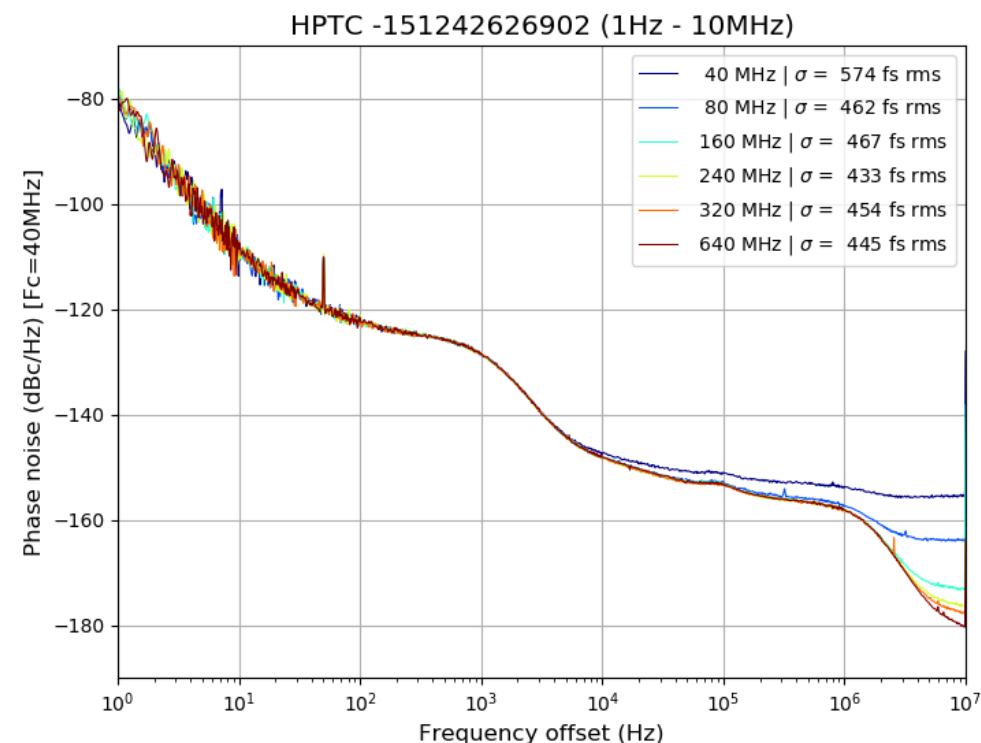
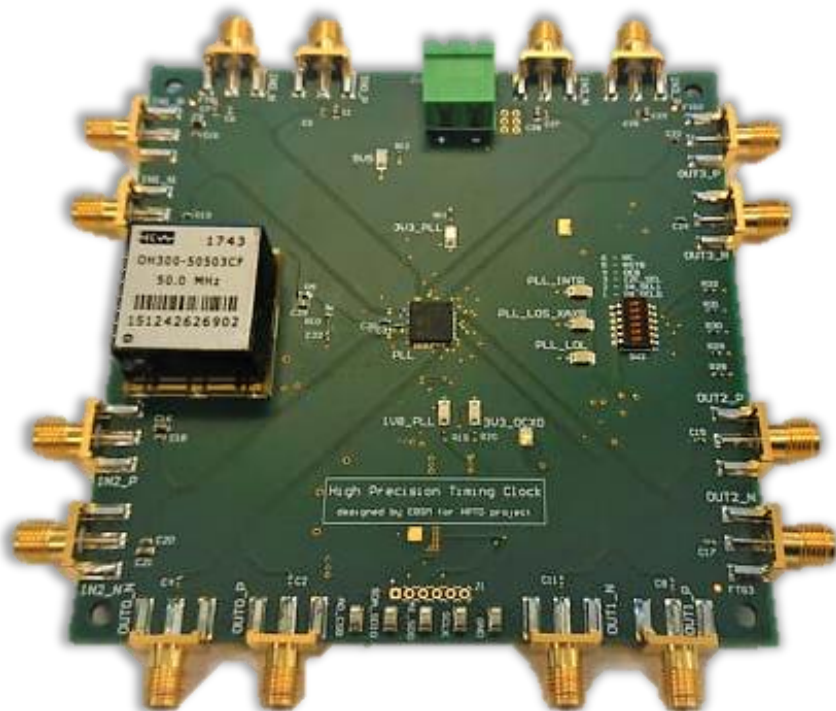
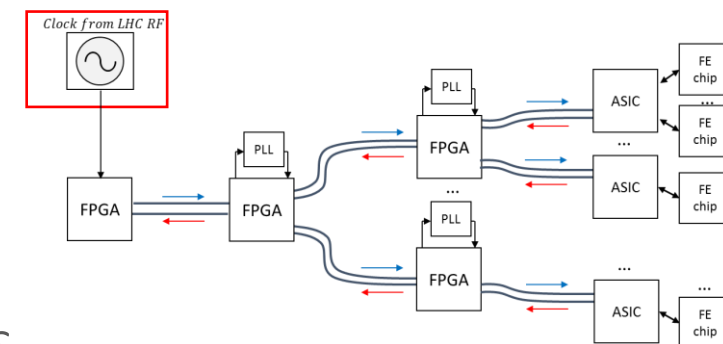
- Wander – slow phase variations
 - Heavily depends on environmental conditions (temperature, power supply stability, ...)
 - Typically measured in time-domain
- Phase-determinism with resets



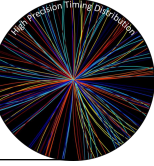
Clock source: emulating LHC RF

- High-Precision Timing Clock

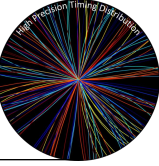
- Low-cost high-quality generator designed at CERN
- Based on a commercial PLL and OCXO
- Used by several members of the CERN High Precision timing community - comparable reference for timing measurements



Agenda

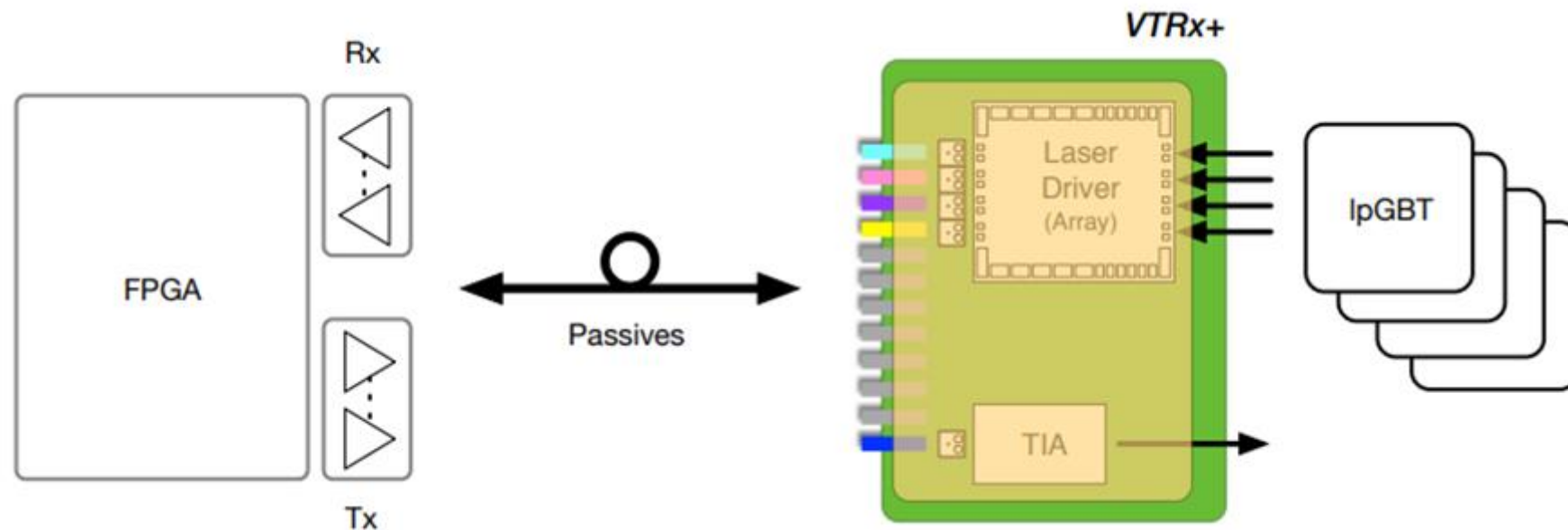


- Introduction
- **Timing distribution systems**
 - Front-end
 - Back-end
- Conclusions

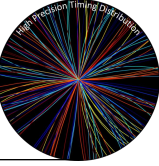


Front-end: Versatile Link Plus and IpGBT

- Radiation Hard Optical Link and SerDes
- Fixed-latency and low-jitter (<5ps)
- Common project developed by CERN and partner institutes

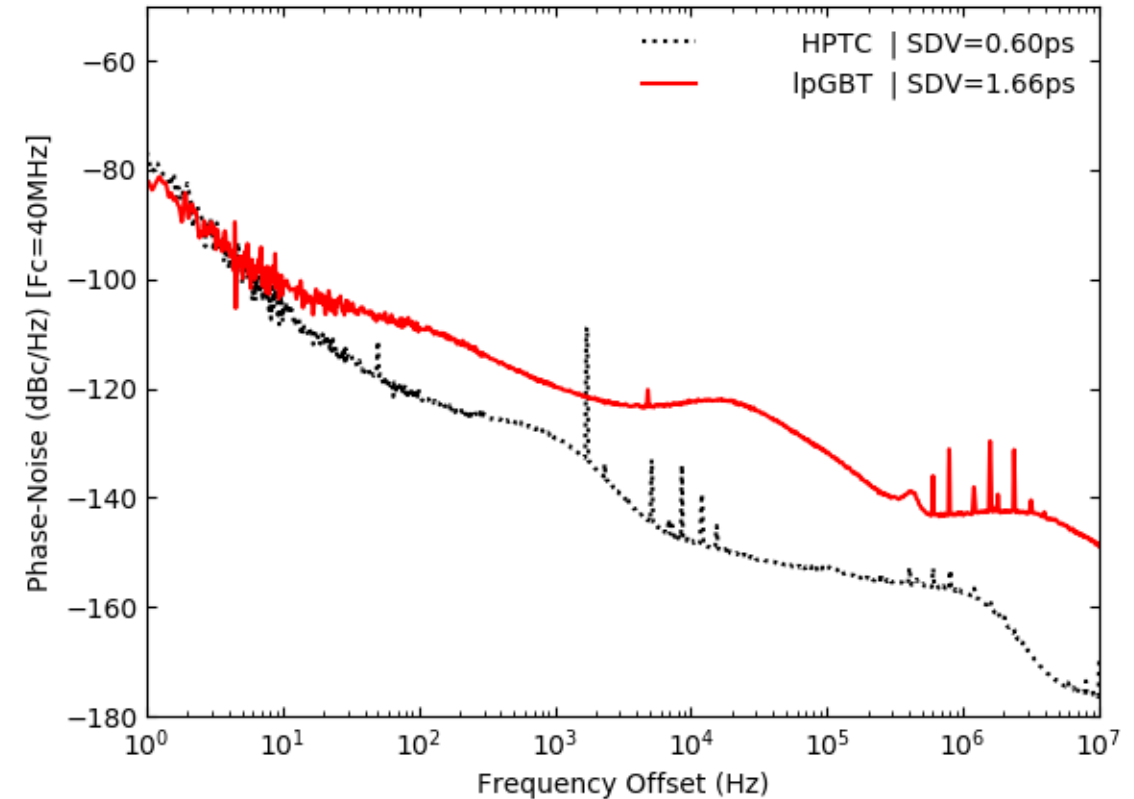
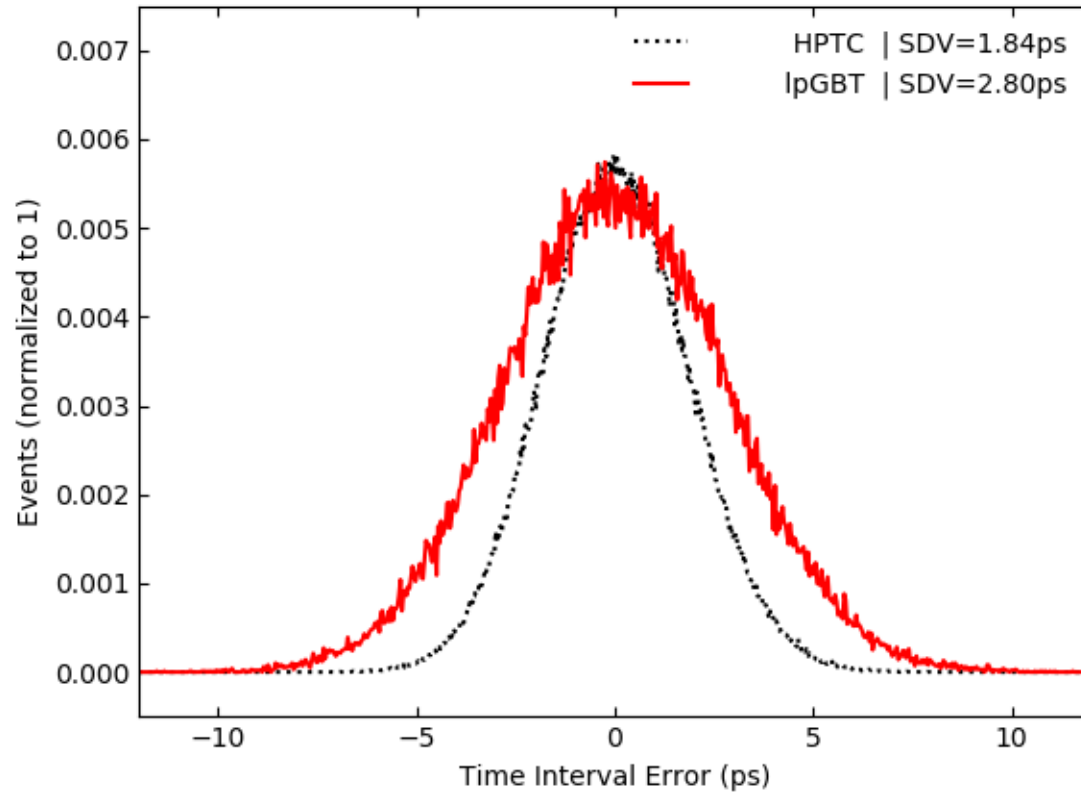
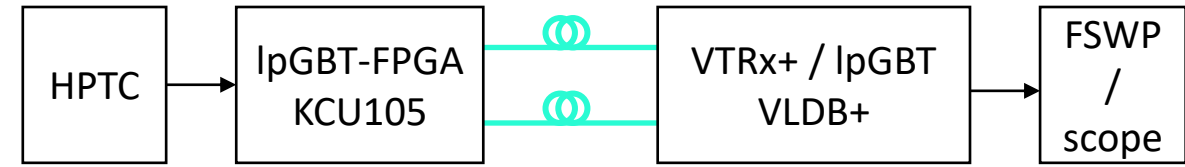


- For more information: <https://pos.sissa.it/313/048/pdf>

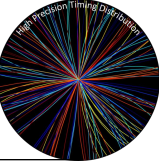


Front-end: Versatile Link Plus and IpGBT

- Link timing performance
 - TIE and phase-noise

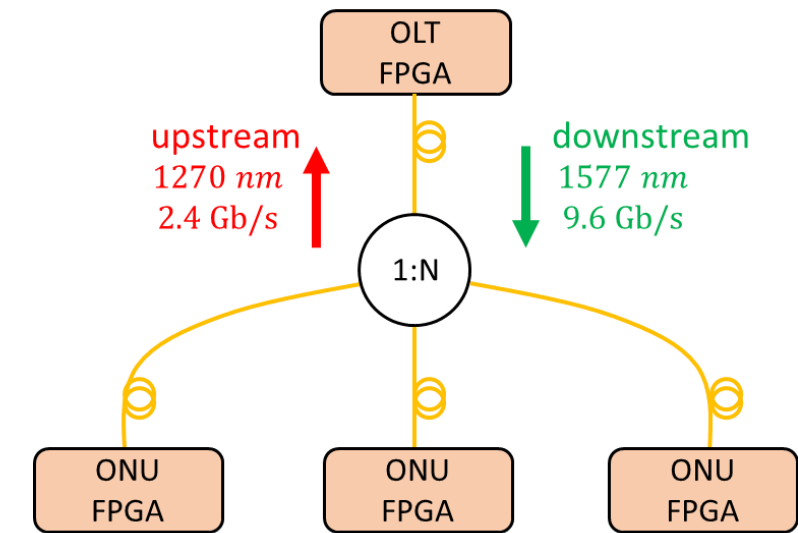
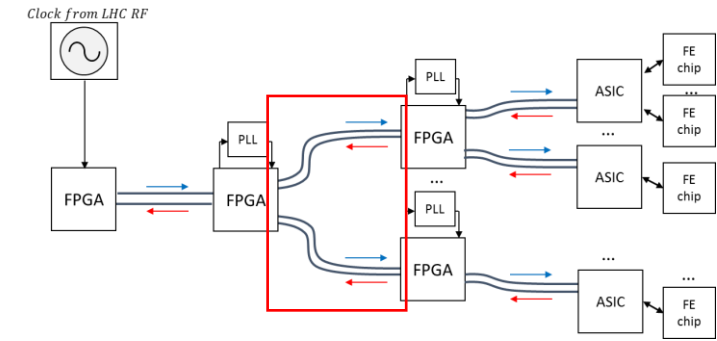


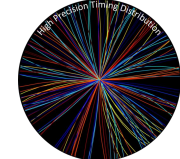
- Excellent performance achieved within the specifications of the LHC experiments



Back-end: TTC-PON

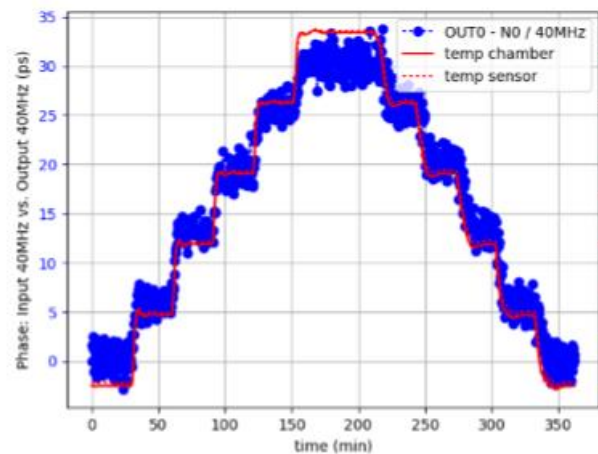
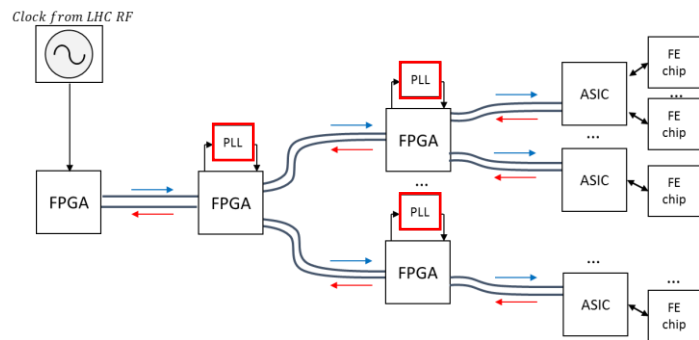
- Back-end timing distribution system adopted by ALICE and LHCb experiments
- Point-to-multipoint optical network inspired on current TTC
 - Based on Passive Optical Networks
 - Single-fiber using WDM and TDM
 - Low-cost for a high split-ratio (1:64)
- Phase-monitoring based on system bidirectionality
- Can achieve TIE jitter < 5ps with a careful system design
 - More details in [NSS 2020](#)



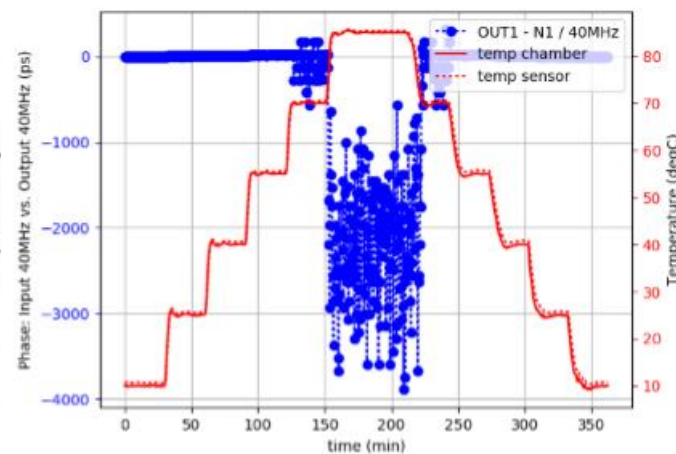


Back-end: COTS

- Heavily based on commercial components and custom links
- CERN started a R&D program to study the timing performance of the different components in a timing distribution system (FPGA, PLLs, LDO, ...)
 - Steered by the LHC experiments designers



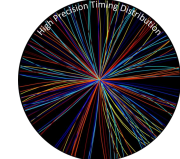
(a) N0 / OUT 40MHz



(b) N1 / OUT 40MHz

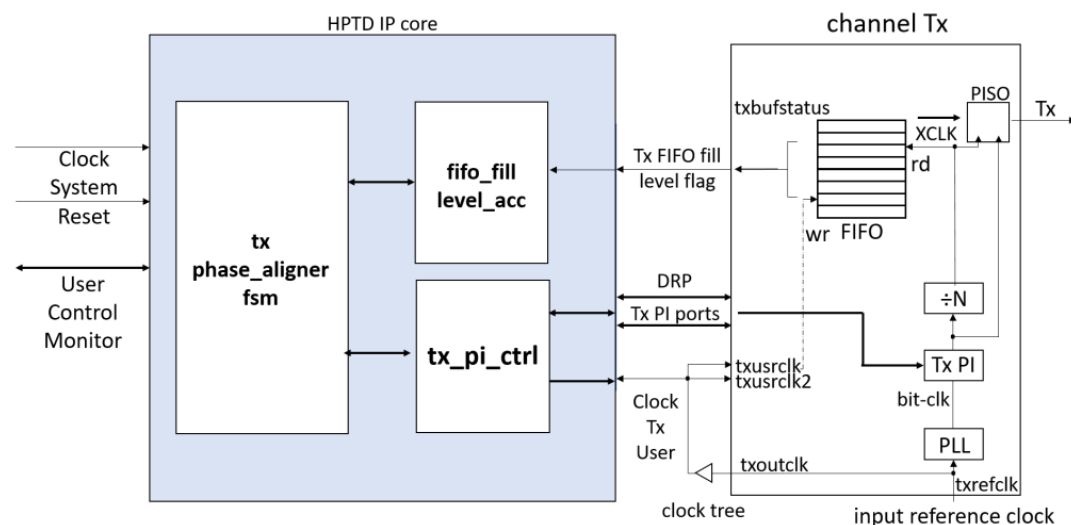
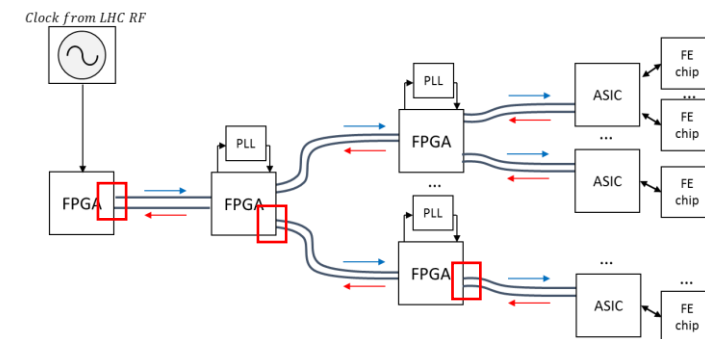
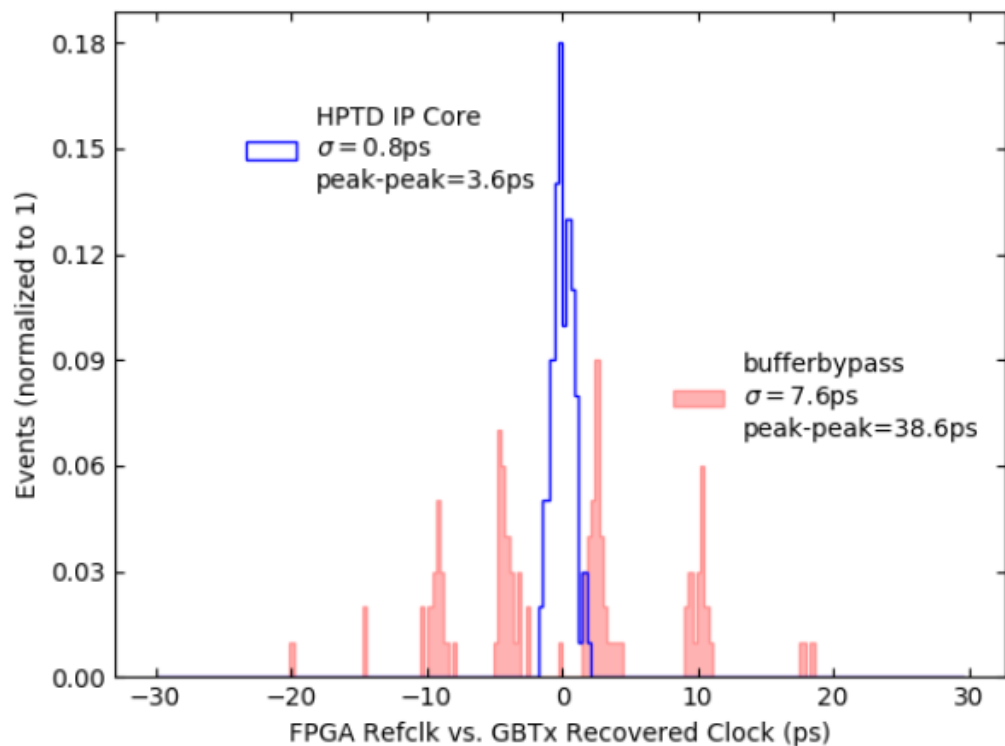
- Some results can be found [here](#)

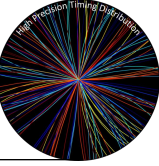
Example: recommendation on how to use Si5344 PLL to avoid phase-jumps



Back-end: HPTD IP

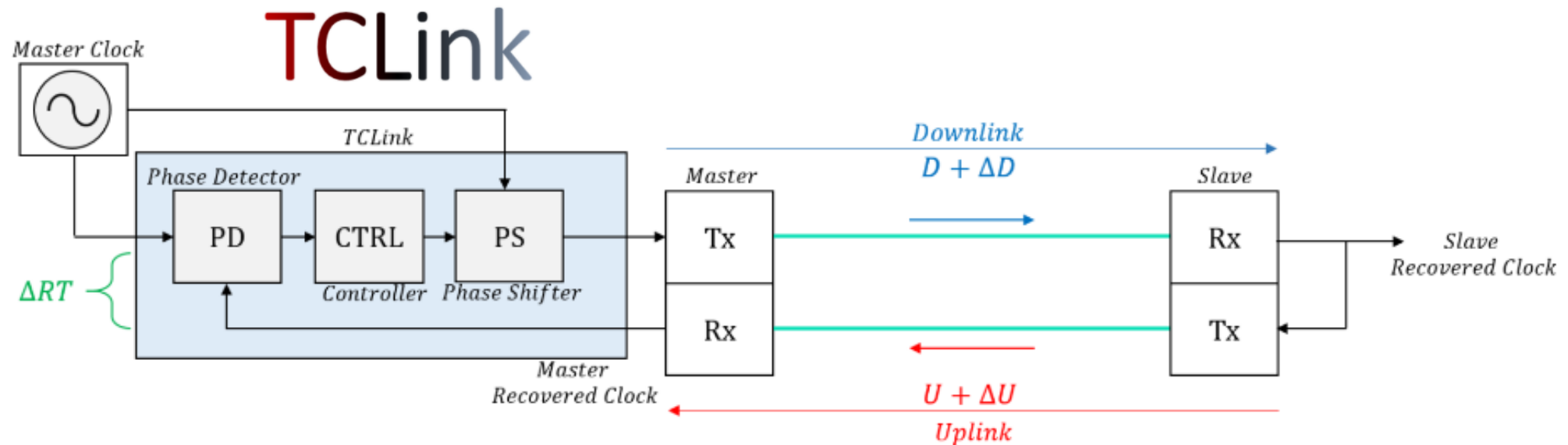
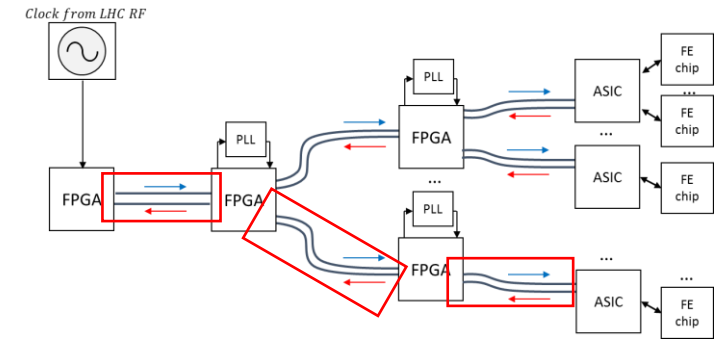
- Higher Phase-determinism for Ultrascale transmitters
- Results presented in [IEEE TNS](#)



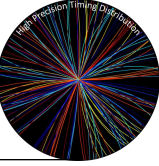


Back-end: Timing Compensated link (TCLink)

- CERN developed an FPGA-IP to implement timing monitoring and compensation in FPGA-based links
 - Protocol-agnostic
 - Presented in [TWEPP-19](#)

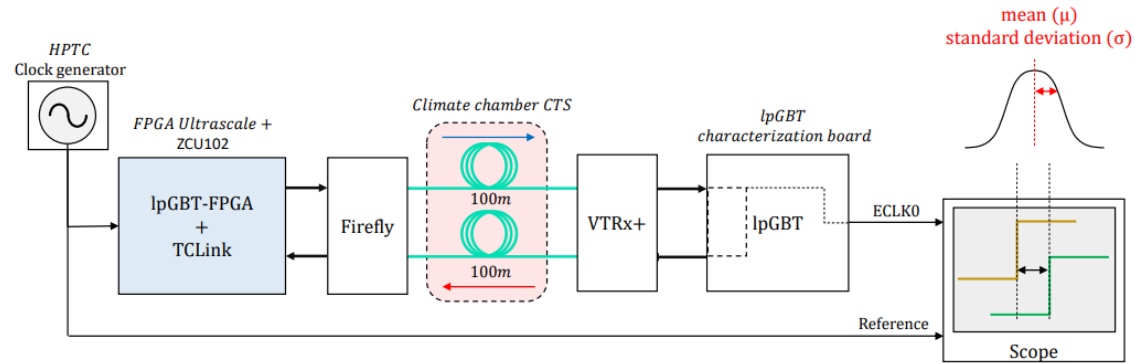


- Unique feature: fully integrated (no need of external components)

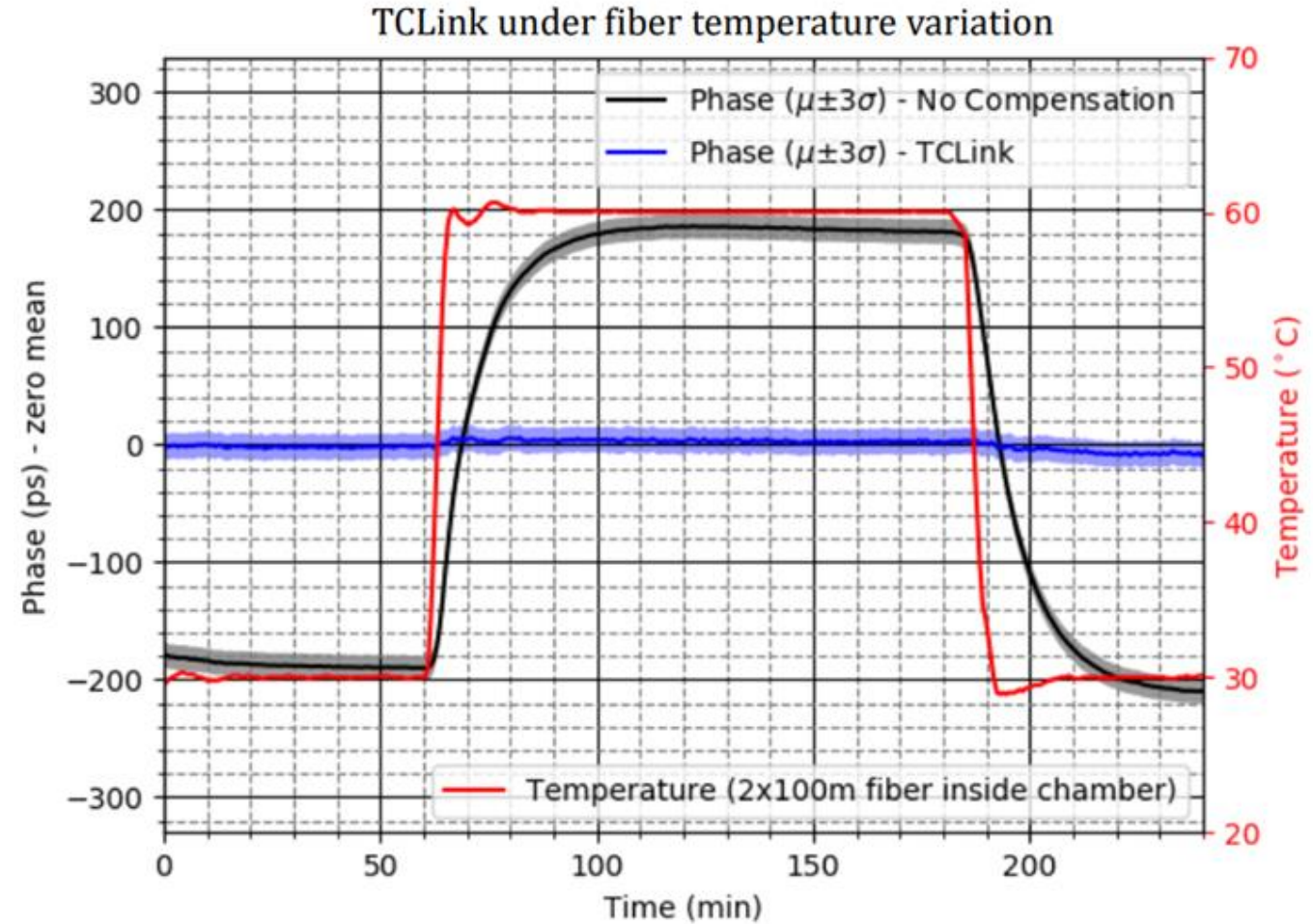


Back-end: Timing Compensated link (TCLink)

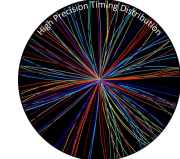
- Tracks and correct slow-phase variations with sub-ps resolution



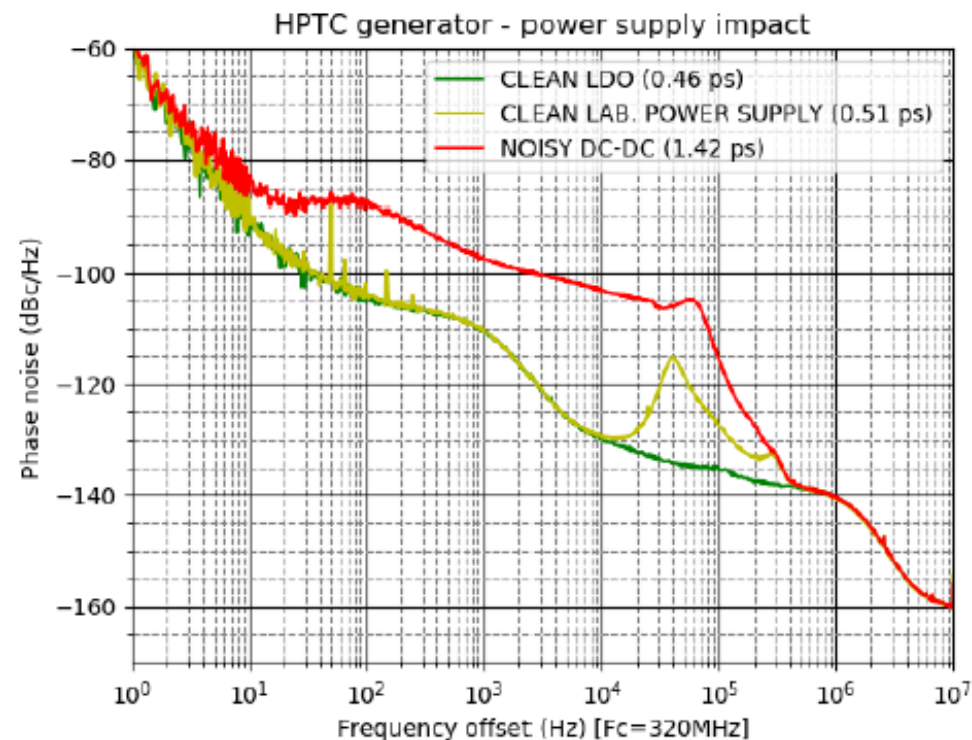
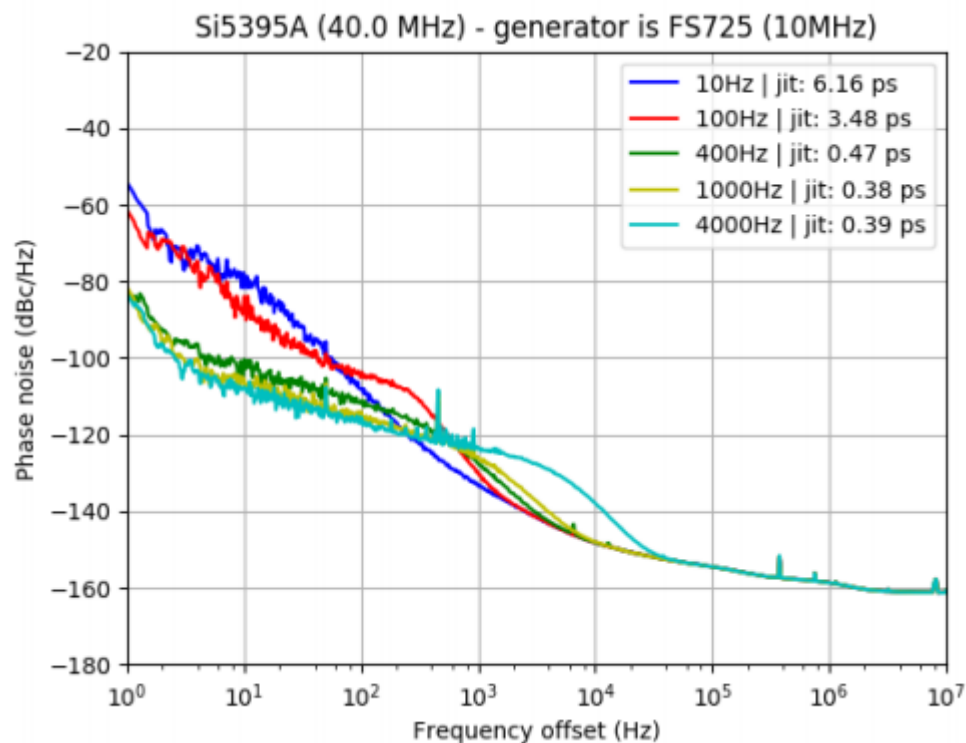
- Adopted by CMS designers



Timing distribution

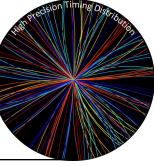


- It is possible to achieve a high timing performance with our current systems but...



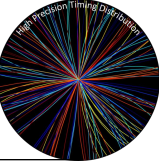
... any design mistake can greatly jeopardize the performance of a timing distribution network

Conclusions

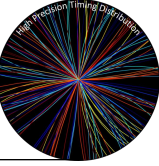


- To achieve the high-performance required for HL-LHC...
 - CERN and external institutes are undergoing an extensive R&D program on the different components of a timing distribution system
 - Some weak-points were identified and solutions proposed
- Early system prototypes helped in gaining confidence that such a system can meet the specifications
 - See [this talk](#) of Jeroen Hegeman from CMS-DTH
- It is possible to achieve a high timing performance for our needs with our current systems but a **careful system design** has to be carried out

THANK YOU

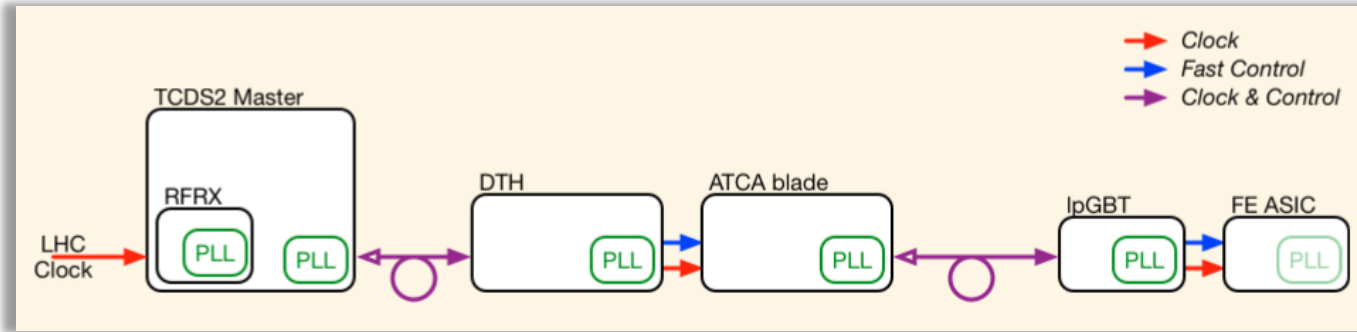


BACK-UP

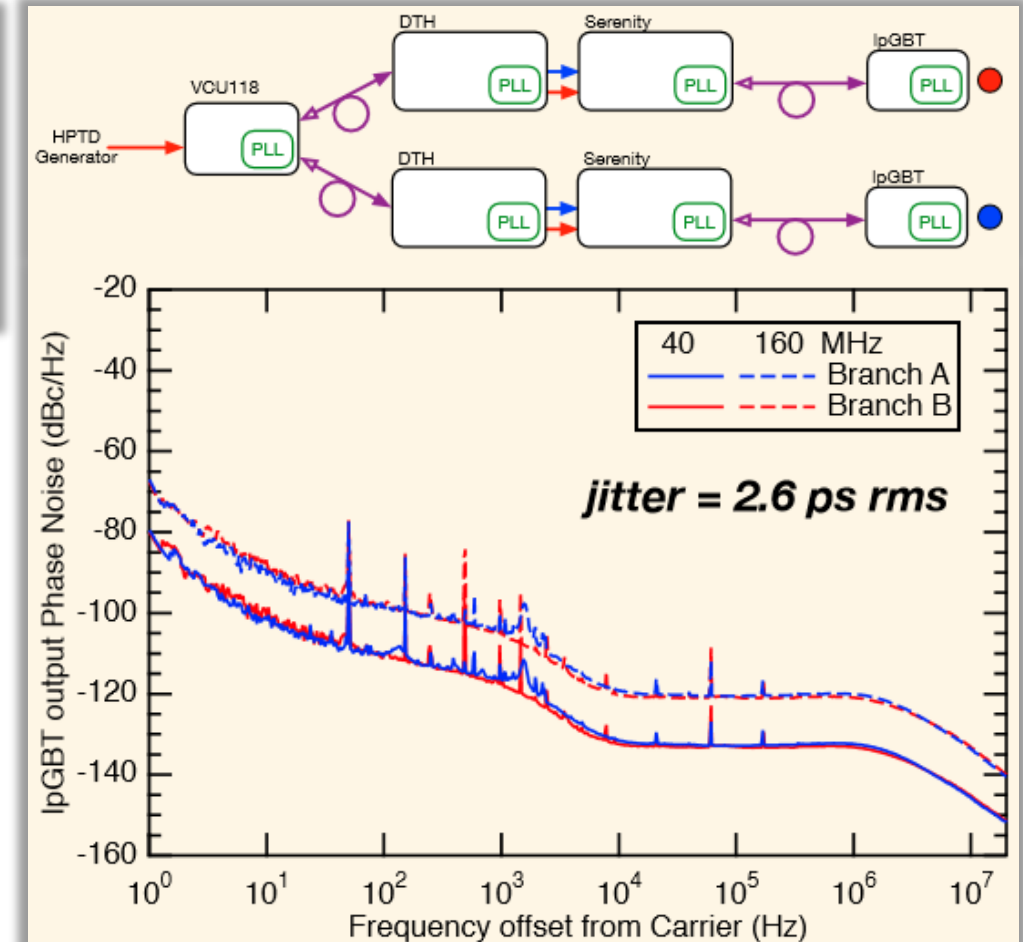


CMS DTH

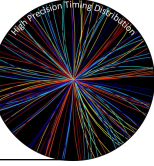
- Baseline system distributes clock and fast control



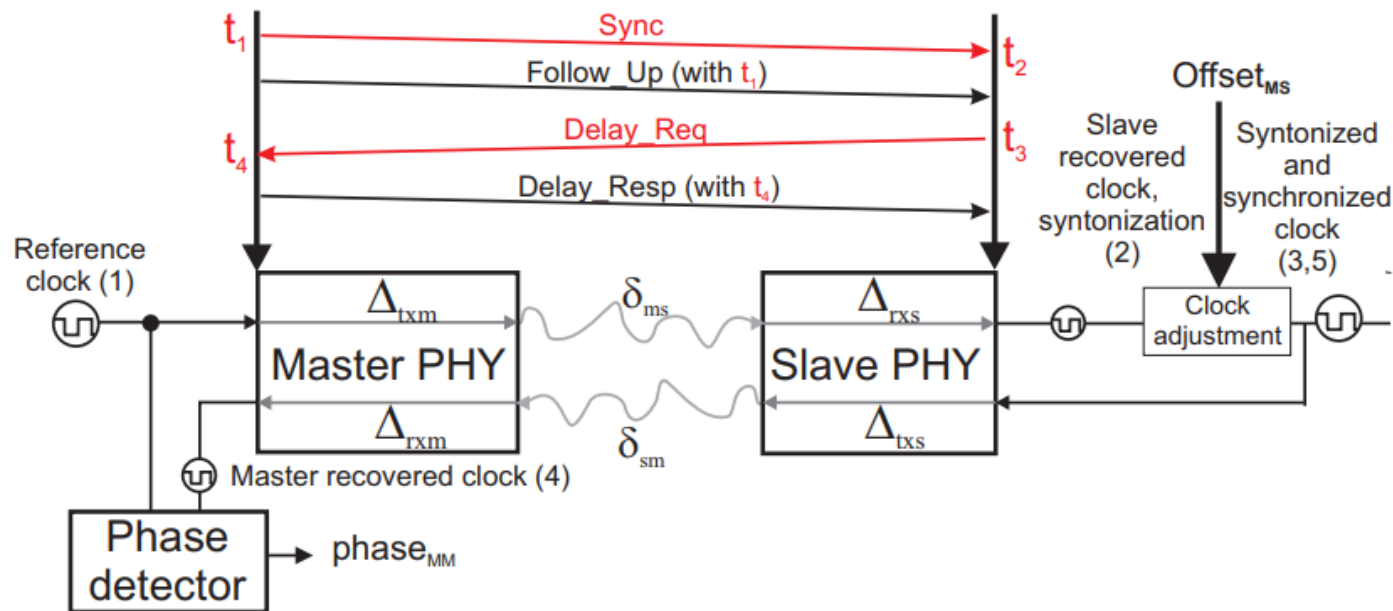
- Excellent performance achieved in the first full-chain prototype
- For more information see [this talk](#) from Jeroen



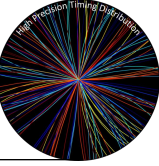
White-Rabbit



- High-accuracy (sub-ns) time transfer using an enhanced version of the Precision Time Protocol (PTP)

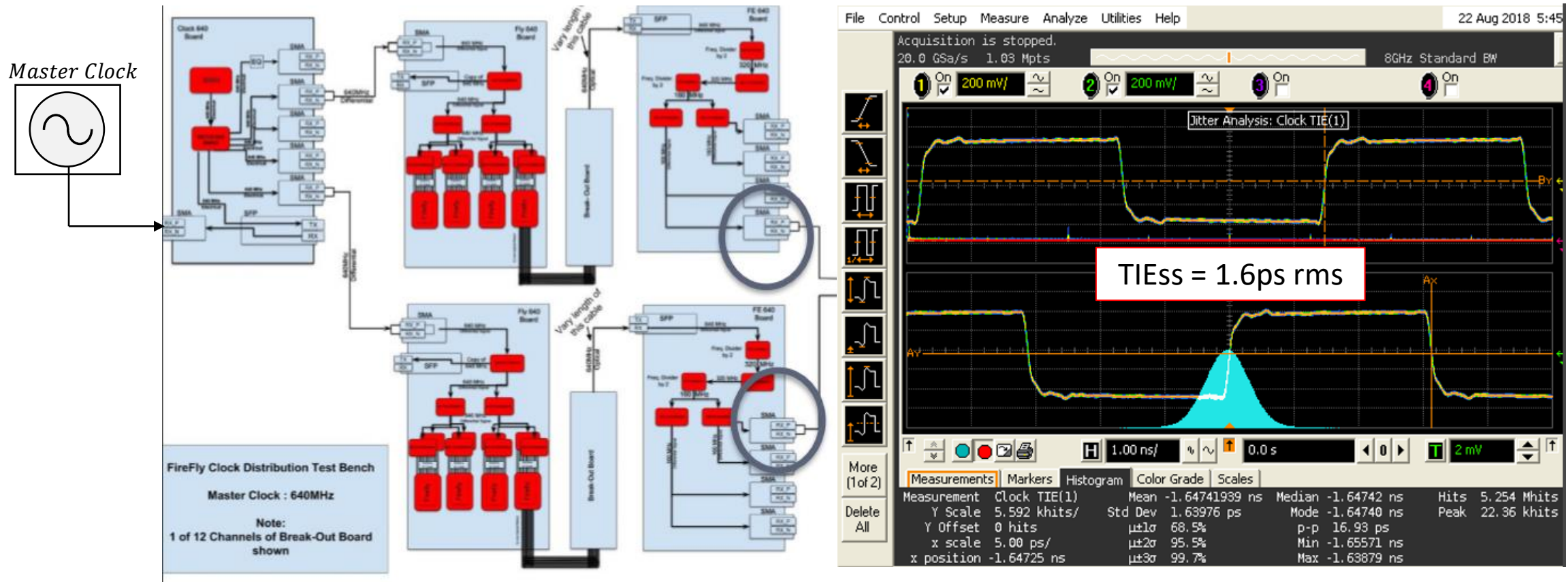


- The high accuracy of synchronization achieved in White Rabbit was [standardized](#).
- Can be used to distribute RF signals: currently being implemented at CERN SPS-RF
 - Prototype shows better than 100fs jitter and phase-determinism over resets below 10ps

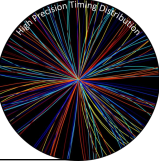


Pure clock distribution

- An alternative to a serial self-synchronous based timing distribution system would be a pure clock distribution link - no data-encoding
- Presented in [TWEPP19](#) by University of Minnesota



Time transfer on free-space



- Work developed at NIST for time-frequency transfer in free-space
 - Based on frequency-comb
 - Not directly applicable for a detector-wide timing distribution but some ideas can be inspiring...
- High-performance achieved with accuracy on femto-second level
- Work published in [Nature Photonics](#)

