

## Development of the µRWELL detector for large area application

#### You Lv , Yi Zhou , Jianbei Liu

State Key Laboratory of Particle Detection and Electronics University of Science and Technology of China

The 2020 International Workshop on the High Energy Circular Electron Positron Collider 26/10/2020---28/10/2020





- Motivation
- Novel solutions to large area µRWELL
- Characterization of the detectors with X-rays
- Summary

#### µRWELL detector



**µRWELL:** A Novel MPGD with resistive electrode and a single stage of well-type gas amplification.

**Architecture:** Compact, No gluing, No stretching, Easy handling.





#### **DLC resistive electrode:**

1. A drop of the amplifying voltage on the DLC resistive layer caused by radiation, reducing the capability to stand high particle fluxes.

2. Higher the radiation rate, larger current drawn through the resistive layer, larger the voltage and gain drop.

For larger area detector, the gain drop caused by the radiation is more seriously.

Large area and high-rate: key performance to the application

The fast-grounding technique is presented to solve the problem of gain drop in  $\mu$ RWELL with large area.

## Fast-grounding µRWELL (SG2++ & SBU )



**1. SG2++ type** (Cu grid): The copper clad on the DLC is etched to conductive grounding lines by photolithography.

**2. SBU type** (Sequential Build Up): Current evacuation achieved by two stacked DLC layers. Matrix of conductive vias manufactured with SBU technology are used to connect DLC layer to grounding.



#### **Problems to large area:**

- For SG2++, after gluing the APICAL substrate onto the readout PCB, it is impossible to see the fast grounding lines, so it is impossible to align the mask for APICAL etching.
- For SBU, still have same alignment problems when making the conductive vias.



- Motivation
- Novel solutions to large area µRWELL
- Characterization of the detectors with X-rays
- Summary

## Novel idea to large area



#### PEDF: Patterning , Etching , Drilling & Filling



Step1: Copper & APICAL etching, to make a big hole, with DLC on bottom.



Step3: Use silver glue to connect the DLC to readout pad.

Cu Apical DLC Prepreg PCB



Step2: Drill a small hole, the copper of the readout pad expose to air.



Step4: Make  $\mu$ RWELL structure and remove the copper around silver glue.

#### Advantages:

- 1. No alignment problems even goes to large area;
- 2. No copper-coated DLC needed, better resistivity control;
- 3. Larger contact area between DLC and silver glue, improving the connection.

### PEDP, DEF, DEP µRWELL



Different options of large area  $\mu$ RWELL close to PEDF from Rui.



PEDP: Patterning , Etching , Drilling & Plating

DEP: Drilling , Etching & Plating

DEF: Drilling , Etching & Filling

Four different types (PEDF , DEF , PDEF , DEP) of  $\mu RWELL$  PCB with sensitive area of 5cm  $\times$  5cm have been produced at CERN and transferred to USTC .

Thanks to Rui De Oliveira and CERN PCB workshop for supplying the technique support on µRWELL PCB.

#### µRWELL prototypes



- Small-pad readout applied to the μRWELL prototypes.
- The parameters of readout pads almost same as small-pad readout MM.

C. Di Donato., et al. Small-pads resistive Micromegas prototype. NIM A. Volume 958, 2020.







Small-pad readout

µRWELL PCB

Fast-grounding holes

Fast-grounding hole: 1mm diameter, total 80 holes with a geometry acceptance of 97.5 %.

- These μRWELL PCBs (PEDF,PEDP,DEF,DEP) are assembled to μRWELL prototypes.
- The performance of these detectors are tested with X-rays.

Pad parameters Size: 0.85mm @X & 2.85mm @Y Pitch: 1mm @X @ 3mm @Y Channel: 48(X) by 16(Y) = 768



- Motivation
- Novel solutions to high rate µRWELL
- Characterization of the detectors with X-rays
- Summary

#### Test setup



Work gas:  $Ar/iC_4H_{10} = 95/5$ Hirose adapter: 128 readout pad  $\rightarrow$  1 channel  $\rightarrow$  grounded by 50 $\Omega$  terminator or Keithley 6482 Picoammeter.



**DLC resistive electrode**: grounded via a 50  $\Omega$  terminator or 6482 Picoammeter. **Induced signal:** picked up from the top-copper or readout-pad. **Current signal:** picked up from readout-pad monitored by Keithley 6482.



## Gain vs drift field

#### Normalized gain, fixed the avalanche electric field.



- The thickness of copper cladded on the APICAL is  $5 \mu m$  (PEDF&DEF).
- The plating process make the thickness of copper up to 15 μm (PEDP&DEP). This is the reason to the different behavior between PEDF and PEDP.

## Simulation of the gain vs. drift field

A simulation was carried out to understand the influence on the gain caused by the thickness of copper electrode.

Detector geometry: ANSYS Electron avalanche and the efficient gain: Garfield++ Gain vs. Drift field Gain vs. Drift field Gain Gain Cu: 5 µm Cu: 15 µm 2 5 2 5 1 3 4 1 3 4 Drift field (kV/cm) Drift field (kV/cm)

The simulation result is qualitative consistent with the test result.

The work gas set to  $Ar/CO_2$  (70/30) in this simulation, the  $Ar/iC_4H_{10}$  (95/5) gas mixture will be simulated in the next step. 12

For DAQ system, it always receive induced signal, the induced signal gain is more accurate than current gain.

The induced signal gain is the effective gain, it is influenced by the weighting field.

13

#### Two different definition of gas gain presented: **Current gain** and **Induced signal gain**

**1.** Current gain: absolute gain, current signal exported from readout pad and recorded by Keithley 6482 Picoammeter.

$$G = \frac{I}{R \bullet \frac{8 \text{keV}}{E} \bullet q_{e}}$$

Definition of gas gain

I: current R: rate E: average ionization energy

2. Induced signal gain: induced signal exported from readout pad or copper electrode, it was amplified by a pre-amplifier followed by a main amplifier, the signal finally recorded by MCA.

A pulse generate a signal, and it test system (pre-amplifier, main amplifier)

$$G = \frac{C \cdot U}{\frac{8 \text{keV}}{E} \cdot q_{e}}$$
 C: capacitance of pre-AMP U: effective voltage

Drift  
Top-copper  

$$GREV$$
  
 $glue$   
 $50\Omega$   
 $GREV$   
 $GREC$   
 $GREV$   
 $GR$ 





## Gain vs avalanche electric field





Current gain: ~30000 @420V

Induced signal gain: ~10000 @420V







The current gain is about 3 times bigger than induced signal gain.

It is due to that the effective weighting field smaller than 1.



## Weighting field in µRWELL



1. Ramo Theorem: induced signal :

$$I_n^{ind}(t) = -\frac{Q_n^{ind}(\mathbf{x}(t))}{dt} = \frac{q}{V_w} \nabla \psi_n(\mathbf{x}(t)) \dot{\mathbf{x}}(t) = -\frac{q}{V_w} \mathbf{E}_n(\mathbf{x}(t)) \dot{\mathbf{x}}(t)$$

Induced charge: proportional to weighting potential.

2. Weighting field  $\mathbf{E}_{n}(\mathbf{x})$ : setting the voltage of interested electrode to 1, and other electrode to 0.

$$\mathbf{E}_n(\mathbf{x}) = -\nabla \psi_n(\mathbf{x})$$

µRWELL detector:

- 1. The second electron only drift in the well-hole.
- 70 µm insulating material (50um Prepreg+12um Kapton+10um epoxy glue) between readout pad and DLC electrode, reduce the effective weighting field in well-hole.
- 3. Weighting potential: about 0.42 (0.48 to 0.9) in the well-hole.
- 4. Induced signal gain is smaller than current gain.





## Rate capability (PEDP)



Rate capability: assessed by detector gain as a function of counting rate per unit area.

Different collimator diameters: 8 mm, 6 mm, 3 mm, 1 mm are used.

Rate: (1MHz/cm<sup>2</sup>), Gain (6000): 1 @1mm, 1 @ 3mm, 0.9 @6mm, 0.85 @ 8mm. Gain (14000): 0.97 @1mm, 0.95 @ 3mm, 0.85 @6mm, 0.68 @8mm.

For a m.i.p. the primary ionization is 10 times smaller than 8 keV X-rays with a drift gas gap of 3mm.

Gain vs. current: Study the ohm effect of DLC resistive electrode in  $\mu$ RWELL.



The log of the relative gain drops linearly with current

- Same current with different gain, same gain drop. The ohm effect result in the gain drop.
- Same current with different collimator diameters, different gain drop. The radiation area changes, the effective resistivity changes, the gain drop changes. 16

## Rate capability (PEDF & DEF)





Rate: (1MHz/cm2), PEDF gain: 1 @1mm, 0.95 @ 3mm, 0.9 @6mm, 0.8 @ 8mm.

DEF gain: 0.95 @1mm, 0.85 @ 3mm, 0.8 @6mm, 0.7 @8mm.

The DEF detector don't have pattering during the etching process. It may make the hole irregularly, and a poor performance.

For PEDF and DEF: The filling process is not a standard process. The fast-grounding hole would be damaged when removing the excess silver glue. The rate capability of PEDP is better than PEDF.

## Problems of DEP $\mu$ RWELL

 When voltage applied to 425V, discharge occurs (~ 1 μA), then short circuit happened between the DLC and copper.
 The PCB was flushed by high pressure water gun, then baked in oven with 70 degree.

3. The DEP detector work well in low rate.

4. A ripple signal occurs in high rate. The current exported from readout-pad shows larger fluctuation that make it impossible to have a rate capability result.







When test the highest gain, the PEDF and PEDP also occurs discharge (~ 1µA), but PEDF and PEDP can back to the good work condition.



#### **Resistivity measurement**



#### Measuring the resistivity between DLC and fast-grounding holes.



Measurement

PEDP: 6 holes out of range. Resistivity: 90 to 110  $M\Omega$ 

PEDF: 32 holes connected. Resistivity: 350 to 450  $M\Omega$ 

DEP: All holes disconnected.

DEF: 1 holes connected. Resistivity:  $380 \text{ M}\Omega$ 



For DEP and DEF, it is uncontrollable when etching the grounding holes due to that no pattering during etching process. Bad connection between DLC and grounding holes.

For PEDF, some of the grounding holes would be damaged when removing the excess silver glue. The PEDP shows the best connection between DLC and grounding holes. 19

## Large area high-rate µRWELL detector



A 50cmX50cm  $\mu RWELL$  have been designed, the PEDP technique will be used for the large area high-rate  $\mu RWELL.$ 

1: MeRt 1 2: MeR2 1 3: MeR3 2 4: MeR4 1 5: MeR6 2 5: MeR6 2 6: MeR6 2 7: NeR6 2 6: NeR6 2 6: NeR6 2 7: NeR7 1 8: NeR8 1 9: NeR8 1 9: NeR8 1 10: NeR10_1 11: NeR11_2 12: NeR12_1 13: Ne2																						
1 : NeR1_1 2 : NeR2_1 3 : NeR3_2 4 : NeR4_1 5 : NeR5_2 6 : NeR5_2 6 : NeR5_2 7 : NeR7_1 8 : NeR1_1 9 : NER3_1 9 : NER3_1 11 : NER11_2 12 : NER10_1 11 : NER11_2 12 : NER11_2 13 : NER11_2 13 : NER11_2 14 : NER11_2 13 : NER11_2 13 : NER11_2 14 : NER11_2 13 : NER11_2 14 : NER11_2 13 : NER11_2 13 : NER11_2 14 : NER11_2 13 : NER11_2 13 : NER11_2 14 : NER11_2 13 : NER11_2 14 : NER11_2 13 : NER11_2 14 : NER11_2 13 : NER11_2 13 : NER11_2 14 : NER11_2 13 : NER11_2 14 : NER11_2 14 : NER11_2 13 : NER11_2 14 : NER11_2 13 : NER11_2 14 : NER11_2 13 : NER11_2 14 : NER11_2 13 : NER11_2 13 : NER11_2 14 : NER11_2 14 : NER11_2 15 : NER11_2 16 : NER11_2 17 : NER11_2 17 : NER11_2 18 : NER11_2 18 : NER11_2 19 : NER11_2 19 : NER11_2 19 : NER11_2 19 : NER11_2 10 : NER11_2 10 : NER11_2 10 : NER11_2 11 : NER11_2 11 : NER11_2 13 : NER11_2 14 : NER11_2 13 : NER11_2 14 : NER11_2 14 : NER11_2 15 : NER11_2 16 : NER11_2 17 : NER11_2 17 : NER11_2 18 : NER11_2 18 : NER11_2 18 : NER11_2 19 : NER11_2 19 : NER11_2 19 : NER11_2 10 : NER11_2 11 : NER11_2 10 : NER11		I				•				•										Nu		•
1 : NeR1_1 2 : NeR2_1 3 : NeR2_1 3 : NeR2_1 3 : NeR6_2 4 : NeR1_1 5 : NeR5_2 8 : NeR6_2 7 : NeR7_1 8 : NeR7_1 9 : NeR7_1 1 : NeR71_2 10 : NeR71_2 11 : NeR71_2 13 : NeR71_2 13 : NeR71_2 13 : NeR71_2 14 : NeR71_2 15 : NeR71_2 16 : NeR71_2 18 : NeR71_2 19 : NeR71_2 19 : NeR71_2 10 : NeR71_2 10 : NeR71_2 11 : NeR71_2 13 : NeR71_2 14 : NeR71_2 13 : NeR71_2 13 : NeR71_2 13 : NeR71_2 14 : NeR71_2 13 : NeR71_2 13 : NeR71_2 13 : NeR71_2 14 : NeR71_2 13 : NeR71_2 13 : NeR71_2 13 : NeR71_2 13 : NeR71_2 14 : NeR71_2 13 : NeR71_2 13 : NeR71_2 14 : NeR71_2 13 : NeR71_2 14 : NeR71_2 13 : NeR71_2 13 : NeR71_2 14 : NeR71_2 14 : NeR71_2 15 : NeR71_2 16 : NeR71_2 17 : NeR71_2 17 : NeR71_2 18 : NeR71_2 19 : NER71_2 19 : NER71_2 10 : NER71_2 10 : NER71_2 10 : NER71_2 10 : NER71_2 11 : NER71_2 11 : NER71_2 12 : NER71_2 13 : NER71_2 14 : NER71_2 15 : NER71_2 16 : NER71_2 17 : NER71_2 17 : NER71_2 18 : NER71_										ero enos												1
1: NeR1_1 2: NeR2_1 2: NeR2_1 3: NeR3_2 4: NER4_1 5: NER5_2 6: NER5_2 6: NER5_2 6: NER5_2 7: NER7_1 7: NER7_1 8: NER7_1 9: NER7_1 9: NER7_1 10: NER71_2 10: NER71_2 11: NER71_2 13: NER71_2 14: NER71_2 15: NER71_2 16: NER71_2 17: NER71_2 16: NE771_2 16: NE7																					狎	ľ,
1: NeR1_1 2: NeR1_1 2: NeR2_1 3: NeR4_1 3: NeR4_1 5: NeR6_2 4: NeR7_1 5: NeR7_1 5: NeR7_1 7: NeR7_1 9: NeR7_1 9: NeR7_1 11: NeR71_1 11: NeR71_2 12: NeR71_2 13: NeR71_2 14: NeR71_2 15: NeR71_2 15: NeR71_2 16: NeR71_2 16: NeR71_2 16: NeR71_2 17: NeR71_2 19: NER71_2 19: NER71_2 10: NER71_2 10: NER71_2 10: NER71_2 11: NER71_2 11: NER71_2 11: NER71_2 12: NER71_2 13: NER71_2 14: NER71_2 15: NER71_2 16: NER71_2 16: NER71_2 17: NER71_2 17: NER71_2 19: NER71_2 10: NER71_2 10: NER71_2 10: NER71_2 10: NER71_2 11: NER71_2 11: NER71_2 11: NER71_2 12: NER71_2 13: NER71_2 14: NER71_2 14: NER71_2 15: NER71_2 16: NER71_2 17: NER71_2 17: NER71_2 17: NER71_2 19: NER71_2 19: NER71_2 19: NER71_2 19: NER71_2 10: NE771_2 10: NE771_2 10: NE771_2 10:																						
1: NeR1_1 2: NeR2_1 2: NeR2_1 3: NeR4_1 5: NeR6_2 4: NeR6_2 6: NeR6_2 6: NeR6_2 7: NeR7_1 7: NeR7_1 9: NeR7_1 9: NeR7_1 11: NeR7_1 12: NeR71_2 13: NeR71_2 13: NeR71_2 13: NeR71_2 14: NER71_2 15: NER71_2 15: NER71_2 15: NER71_2 16: NER71_2 19: NER																						
1. NeRT_1 2. NeRT_1 2. NeRT_1 3. NeRT_1 3. NeRT_2 4. NeRT_2 5. NeRT_2 5. NeRT_2 6. NeRT_2 7. NeRT_1 1. NeRT_1 9. NeRT_2 11. NeRT_2 12. NeRT_2 13. NeRT_2 13. NeRT_2 13. NeRT_2 13. NeRT_2 14. NERT_2 15. NERT_2 15. NERT_2 19. NERT_2 20. NE																						•
1: NeRY_1 2: NeRY_1 2: NeRY_1 3: NeRY_2 3: NeRY_2 4: NeRY_1 5: NeRY_2 6: NeRY_2 7: NeRY_1 7: NeRY_1 9: NeRY_2 11: NeRY_1 12: NeRY_2 13: NERY_2 13: NERY_2 13: NERY_2 13: NERY_2 14: NERY_2 15: NERY_2 15: NERY_2 19: NERY_2																						
1: NeRY_1 2: NeRZ_1 2: NeRZ_1 3: NeRZ_2 3: NeRZ_2 4: NeRZ_2 5: NeRZ_2 5: NeRZ_2 6: NeRR_1 7: NeRT_2 9: NeRT_2 10: NeRT_2 11: NeRT_2 13: NeRT_2 13: NeRT_2 13: NeRT_2 13: NeRT_2 13: NeRT_2 13: NeRT_2 14: NERT_2 14: NERT_2 15: NERT_2 13: NERT_2 14: NERT_2 14: NERT_2 14: NERT_2 14: NERT_2 14: NERT_2 14: NERT_2 14: NERT_2 14: NERT_2 15: NERT_2 14: NERT_2																						•
10. MeRV 1 2. MeRV 1 2. MeRV 2 3. MeRV 2 4. MeRV 1 5. MeRV 2 6. MeRV 2 7. MeRV 2 1. MeRV 2																					T.	
1: Nerk 2: Nark 2: Nark 3: Nark 3: Nark 5: Nark 6: Nark 1:	15		2_2	Ð	5_2	2_2	5	-	3_2	10_1			13_2	14_2				18_1	19_2			•
	NetR	<b>NetR</b>	VetR	NetR <sup>2</sup>	VetR5	VetR6	NetR.	NetR(	<b>VetR</b>	NetR	VetR <sup>-</sup>	NetR	VetR <sup>-</sup>	VetR <sup>-</sup>	NetR	NetR <sup>-</sup>	VetR <sup>-</sup>	NetR	VetR <sup>-</sup>	<b>NetR</b>	C	
																						•
																						•
																						•
																						•
																						ľ,

The layout of the  $\mu RWELL\text{-}NT\text{-}50 cm X50 cm$ 



The active area is divided into 20 sectors

#### Summary



1. Four different technique of high-rate  $\mu$ RWELL was presented. The current gain can be larger than 30000, with a induced signal gain larger than 10000. The rate capability of these detectors was tested, and the best performance is achieved with PEDP technique.

2. The performance (rate capability with larger irradiation area, efficiency, position resolution, dead area) will be tested in next step. A larger area  $\mu$ RWELL with PEDP technique have been designed.

- Thank Lanzhou Institute of Chemical Physics for support on DLC coating.
- Thank Rui De Oliveira and CERN PCB workshop for supplying the technique support on µRWELL PCB.
- Thanks to Giovanni Bencivenni, Eraldo Oliveri and CERN GDD for helpful suggestions and discussions.



# Thank You



#### BACKUP

#### **ATLAS geometry**

#### 1. ATLAS geometry





- 2. High-η muon tagger
- Materials: 2mm PCB + 3mm gas + 2mm PCB
- Total ten detectors with a distance of 10mm each.

Pythia8 generate p-p collide events.

Physical process: SoftQCD:nonDiffractive (minimum-bias component)











**Figure 7.** Relative charge collection efficiency as a function of the drift field.

#### ORTEC 142AH刻度



 flipted in the state state state state state state

 flipted in the state state state state state state

 flipted in the state state state state state state

 flipted in the state state state state state state

 flipted in the state state state state state

 flipted in the state state state state

 flipted in the state state state

 flipted in the state state

 flipted in the sta

Fig. 4.1. Simplified Block Diagram of the ORTEC 142AH Preamplifier.

- Test input: A fast rise time (less than 40 ns) followed by a slow exponential decay (200 to 400 μs).
- Energy Output: The charge-sensitive loop is essentially an operational amplifier with a 1-pF capacitive feedback.
- While test pulses are being furnished to the Test input, connect either the detector (with bias applied).

