

Jadepix3 FW and SW Status

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Sheng Dong, CCNU

s.dong@mails.ccnu.edu.cn

Updates

- More control interfaces and the constrains have been fixed.

- CLK_SEL
- SN_OEn
- POR
- EN_diff
- 1G_f
- SERIALIZER_RST

- PDB, why this pin is connected to MGT bank?

- Configure logic update, for saving time.

- Move the configuration FIFO to IPbus Jadepix Slave
- Write configuration **one by one** to FIFO -> IPbus **block write**.

