

# Jadepix3 DAQ Status

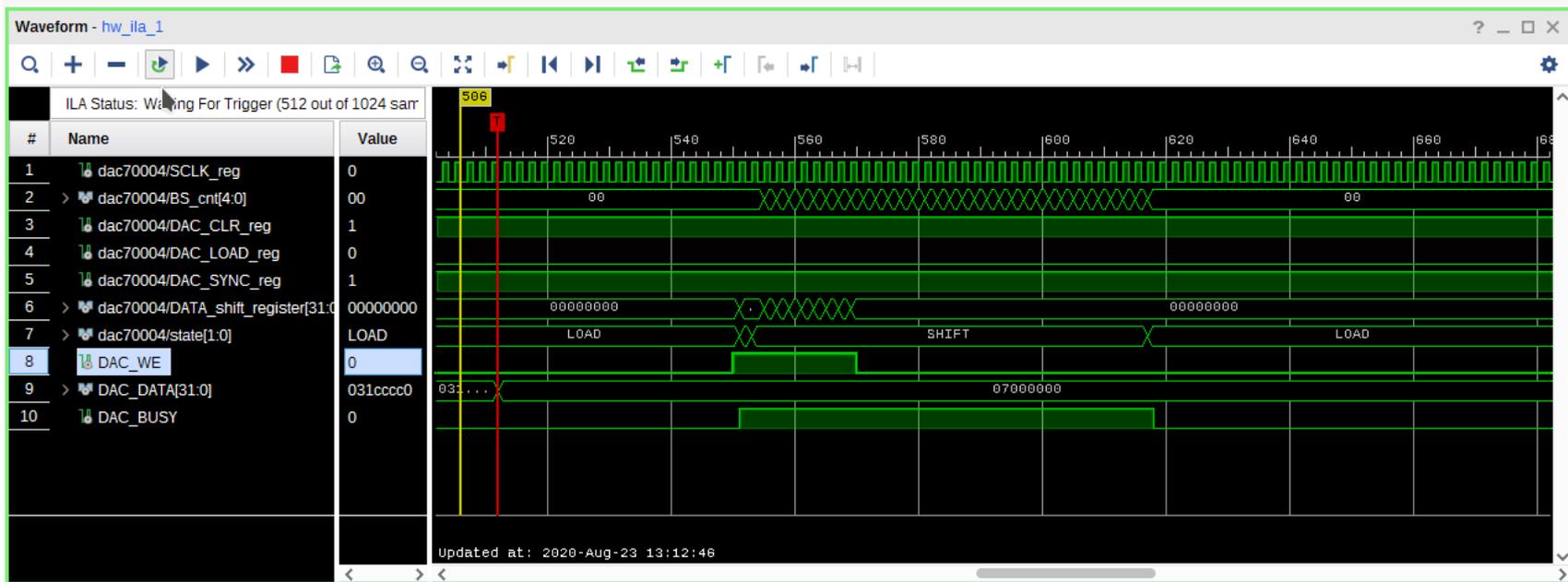
[s.dong@mails.ccnu.edu.cn](mailto:s.dong@mails.ccnu.edu.cn)

2020/08/24

# Status

- Some bug fixed on firmware and software (View code repository)
- DAC and SPI parts have been tested via ILA
- Control logic is under debugging

# DAC debug



Settings - hw\_ila\_1    Status - hw\_ila\_1 x

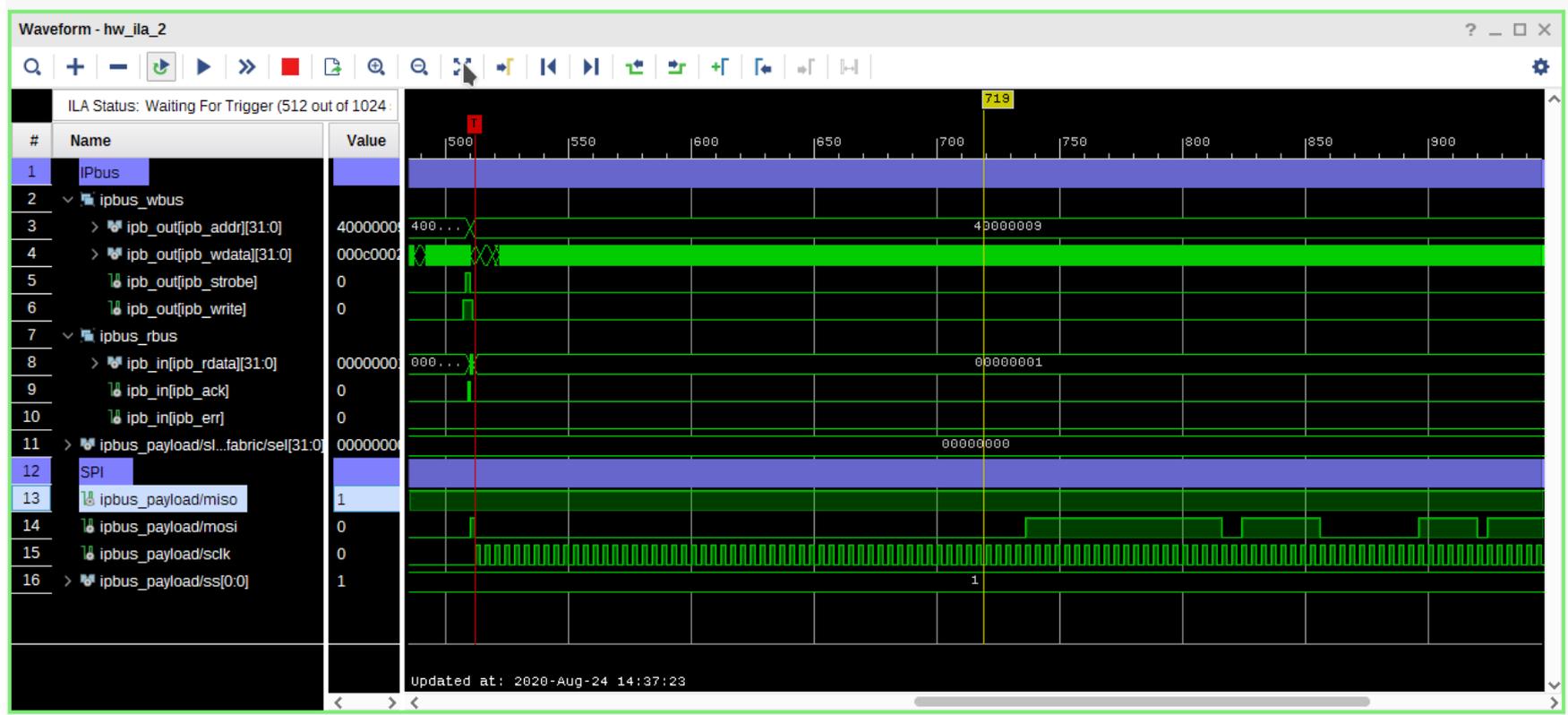
Core status      Waiting for Trigger

Capture status - Window 1 of 1

Trigger Setup - hw\_ila\_1 x    Capture Setup - hw\_ila\_1

Name	Operator	Radix	Value	Port
DAC_WE	==	[B]	0	probe8[0]
DAC_DATA[31:0]	==	[H]	0700_0000	probe2[31:0]

# SPI Debug



# Reference

- [https://gitee.com/habrade/JadePix3\\_Software](https://gitee.com/habrade/JadePix3_Software)
- [https://gitee.com/habrade/JadePix3\\_Firmware](https://gitee.com/habrade/JadePix3_Firmware)