

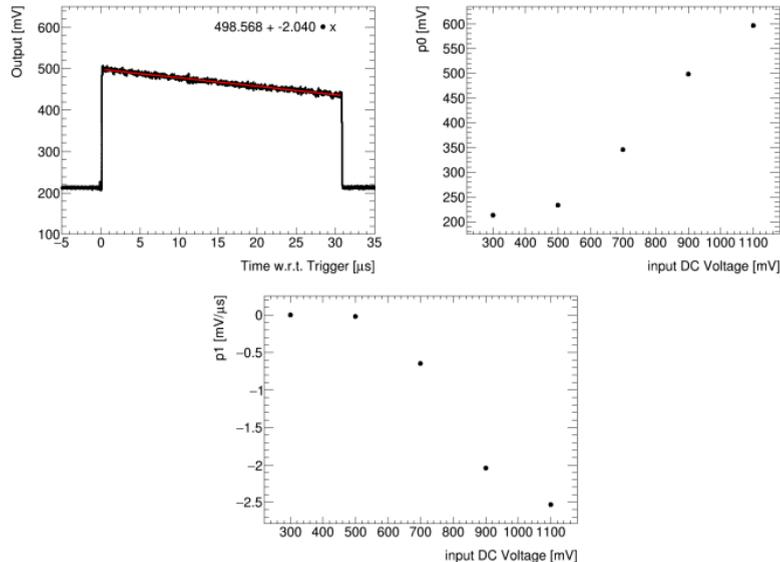
Status of SCA Testing

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- Take-away message from last week's meeting:



- Fed with DC input, fit the output VS time (cell number) to linear function, the p0 is roughly linear in the working input-voltage range, say ~ 600 - 900 mV, while p1 seems not linear.
- Put it in another words, the output decline along cell number depends on the input voltage.

- Current strategy:
 - Instead of correct for the output decline with cell number first, just fit (not necessarily restricted to the linear function) the output VS input for each individual cell in the working input-voltage range. The effect of decline along cell number will be included in this approach.
 - Apply the correction from the fit for each individual cell, test it with sin and pulse input.
 - Derive the time resolution with pulse input with few ns rise time.
- Pending issue: set up the firmware developed by Dongsheng, as well as the PC-controlled data-taking system. Need few full-days working time...