# Design and Fabrication of IHEP-IME ultra-fast timing sensors (ATLAS)



中國科學院為能物現為完施 Institute of High Energy Physics Chinese Academy of Sciences



INSTITUTE OF MICROELECTRONICS OF CHINESE ACADEMY OF SCIENCES

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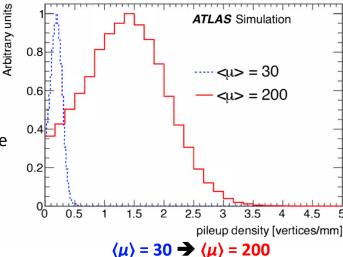
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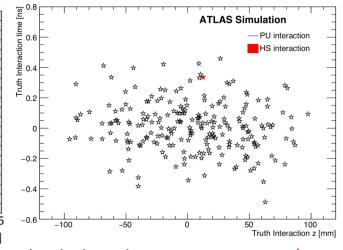


### HL-LHC & ATLAS HGTD

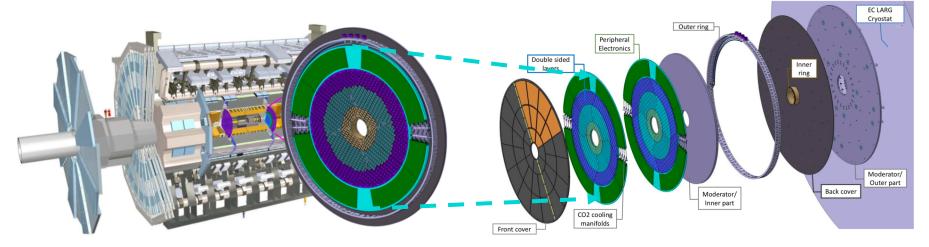
The high-luminosity (HL) phase of the Large Hadron Collider (LHC) at CERN aims to deliver to deliver an integrated luminosity of up to 4000fb<sup>-1</sup>.

The instantaneous luminosity of the HL-LHC will reach up to  $7.5 \times 10^{34}$  cm<sup>-2</sup> s<sup>-1</sup>, a large increase from the  $2.1 \times 10^{34}$  cm<sup>-2</sup> s<sup>-1</sup> obtained during Run 2 of the LHC





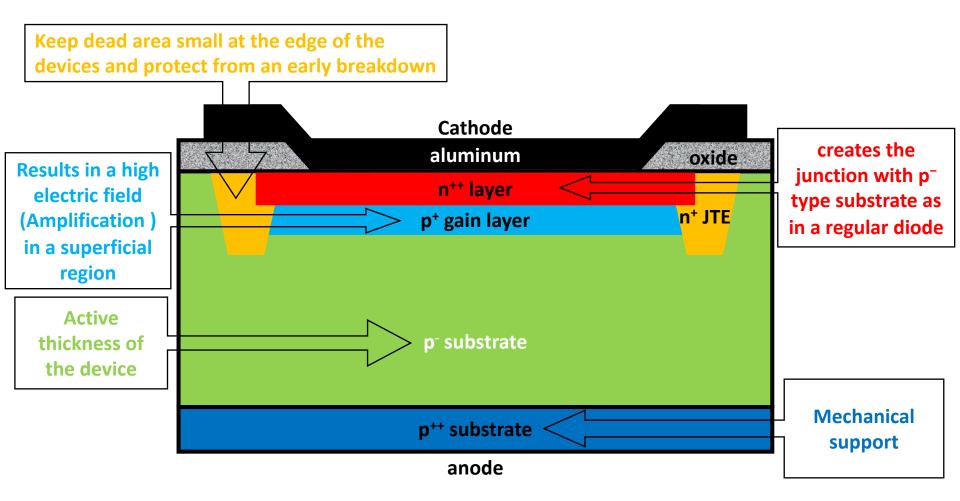
local pileup density is 1.44 vertices/mm



The ATLAS Collaboration, Technical Design Report: A High-Granularity Timing Detector for the ATLAS Phase-II Upgrade, CERN-LHCC-2020-007 ATLAS-TDR-031

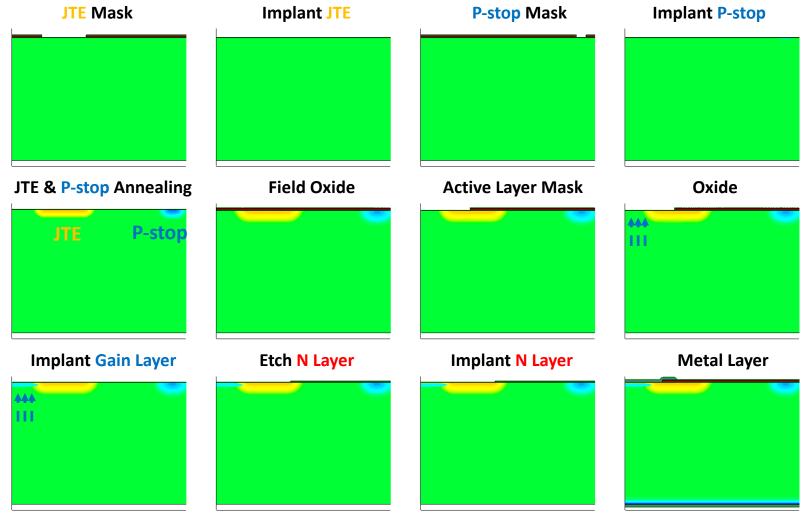


### LGAD Sensor Structure



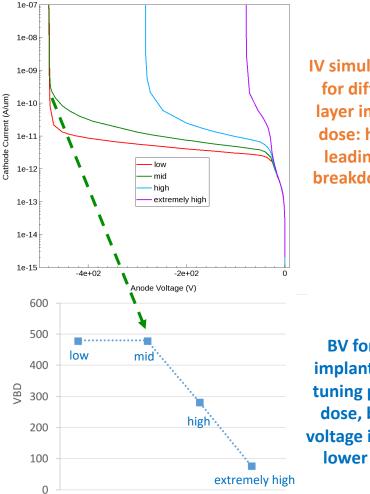


### **Fabrication Process Simulation**



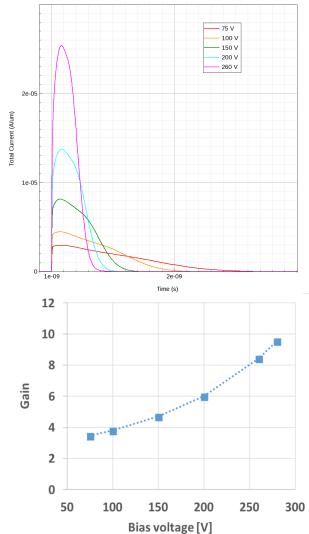


### IV and Gain Simulation



IV simulation curves for different gain layer implantation dose: higher dose leading to lower breakdown voltage

BV for different implantation dose: tuning point at mid dose, breakdown voltage in simulation lower than 479V



Charge collection curve with different bias voltage: higher bias voltage leading to more collected charge

Gain versus bias voltage curve for high dose LGAD sensors

P dose 2020/11/8



### Mask and Cutting

		L1-15-70	L1-15-75	L1-15-85	L1-15-90	L1-15-10	0 L1-80	L1-30	L1-50
		L1-20-90	L1-20-90-1	1 L1-20-90-2	L1-20-90-3	L1-20-90-	-4 L1-20-90-6	H1-15-80	H2-15-80
		L1-15-80	G1-15-80	G2-15-80	G3-15-80	G4-15-8(	0 G5-15-80	G6-15-80	PIN-15-100
		L4-15-1	00	L4-15-90	L4-15-8	30	L4-20-90	L4-20-90	S1 S2 S3 S4
					L4-20-90	)-4	L3-20-90-6	L1-10-60	L1-15-60
	QQ	L5-20-90		L4-20-90-2 L3-20-90-3		G6-15-80	L1-60-60		
						L3-20-90-3	TS	IME-L	



#### Nuclear Inst. and Methods in Physics Research, A 984 (2020) 164558



#### Check for

Design of Low Gain Avalanche Detectors (LGAD) with 400 keV ion implantation energy for multiplication layer fabrication

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ARTICLE INFO ABSTRACT

Keywords: LGAD Silicon sensors TCAD simulation High breakdown voltage Gain factor Implantation energy Low Gain Avalanche Derectors (IGAD) are silicon sensors that can achieve a time resolution of better than 20 ps. The ATLAS and CMS experiments are designal IGAD detectors to address the pile-up challenge at the High Luminosity Large Hadron Collider (HL-LHC). The Institute of High Energy Physics (IHEP) has recently developed two versions of IGAD sensors. The IGAD sensors were designed using Technology Computer-Mded Design (TCAD) simulations and optimized to obtain high breakdown voltage and a suitable gain. The n-type Junction Termination Extension (N-JTE) and p-type gain layer are two critical structures for IGAD sensors that were investigated. IHEP has tuned the fabrication process of two foundries to obtain the most promising design. The first version of the HIEP ICAD sensor, with a gain higher than six and breakdown voltage higher than 400 V, was submitted to Tianjia Zhonghuan Semiconductor Company for fabrication. The second version of the IGAD sensor benefits from the higher implantation energy available at the Institute of Microelectronics (IME) to reach a gain higher than te and breakdown voltage higher than 420 V.

= 1250 µm × 1250

from the n++ cathode at ground potential.

of the p<sup>-</sup> substrate is 50 µm

Fig. 1. Sketch of the LGAD structure with the active area shown. Height and width

are not at the same scale. The sensor total area is 1250 µm × 1250 µm. The thickness

the anode. High voltage is applied on the anode and charge is collected

The  $p^-$  substrate is the active volume for charge drifting. A thinner sensor contributes to better time resolution due to fewer Landau

fluctuation. The proper gain factor can reduce iitter from electronic

noise and time walk from amplitude variations allowing for high time

#### 1. Introduction

CERN will start the High Luminosity (HL) Phase-II of the Large Hadron Collider (LHC) in 2027 [1]. The HL-LHC will deliver an integrated luminosity of up to 4000 fb-1 over the subsequent decade. The instantaneous luminosity of the HL-LHC will reach up to  $7.5 \times 10^{34}$ cm<sup>-2</sup>s<sup>-1</sup>, a substantial increase from the 2.1 × 10<sup>34</sup> cm<sup>-2</sup>s<sup>-1</sup> obtained during LHC Run 2 [2,3]. The increase of instantaneous luminosity will increase the number of collisions in each bunch crossing. Improvement of the spatial and timing resolution is needed for particle tracks to be distinguished and primary vertices to be properly identified. One possible approach to satisfy these requirements is to use a tracking detector in conjunction with independent precise timing measurements. LGAD are silicon sensors that can achieve a time resolution of better than 20 ps. They have so far been developed by several silicon foundries [4-9]. The Institute of High Energy Physics (IHEP) High-Granularity Timing Detector group has recently developed its first two versions of LGAD sensors. In this paper, we report on the design, Technology Computer-Aided Design (TCAD) simulation and proposed fabrication technology for these IGAD sensors

#### 2. Structure

The LGAD structure drawn in Fig. 1 is based on the standard PIN diode architecture with an  $n^{++}$  layer as the cathode and a  $p^{++}$  layer as

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### **Publications**

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#### Design and fabrication of Low Gain Avalanche Detectors (LGAD): a TCAD simulation study

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ABSTRACT: Low Gain Avalanche Detectors (LGAD) are silicon sensors with a time resolution better than 20 ps. The ATLAS and CMS experiments are designing LGAD detectors to address the pile-up challenge at the High Luminosity-Large Hadron Collider (HL-LHC). The Institute of High Energy Physics (IHEP) High-Granularity Timing Detector group has recently developed its first version of LGAD sensors. The LGAD structure was designed using Technology Computer-Aided Design (TCAD) simulations and optimized to obtain a high breakdown voltage and ideal gain. The n-type Junction Termination Extension (N-JTE) zone is a critical structure to guarantee a high breakdown voltage. The gain layer is optimized for an ideal gain factor and hence good time resolution. The optimized LGAD sensor has a gain higher than six and a breakdown voltage higher than 400 V.

KEYWORDS: Solid state detectors; Timing detectors; Charge transport and multiplication in solid media; Photon detectors for UV, visible and IR photons (solid-state) (PIN diodes, APDs, Si-PMTs, G-APDs, CCDs, EBCCDs, EMCCDs, CMOS imagers, etc)

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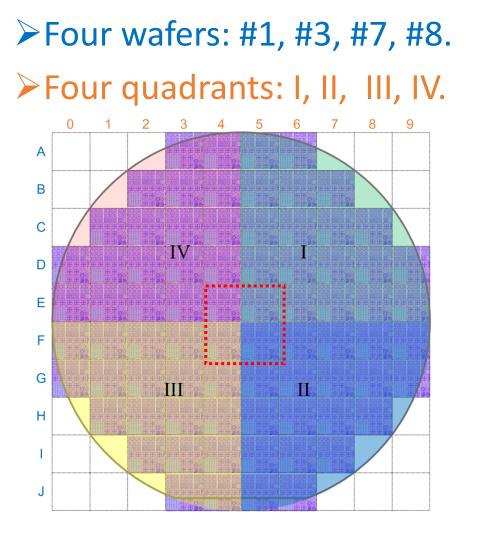
https://doi.org/10.1088/1748-0221/15/03/C03008

#### https://iopscience.iop.org/article/10.1088/1748-0221/15/03/C03008

#### https://linkinghub.elsevier.com/retrieve/pii/S0168900220309554



### IME Production Summary

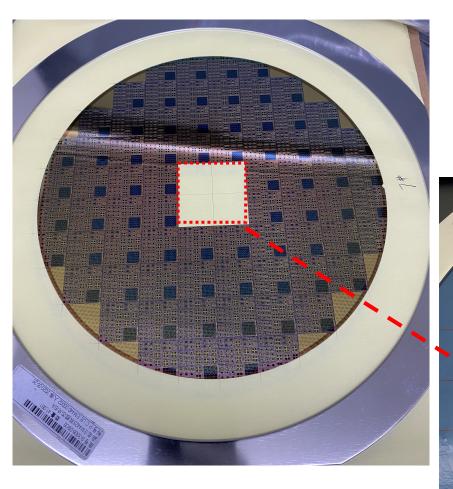


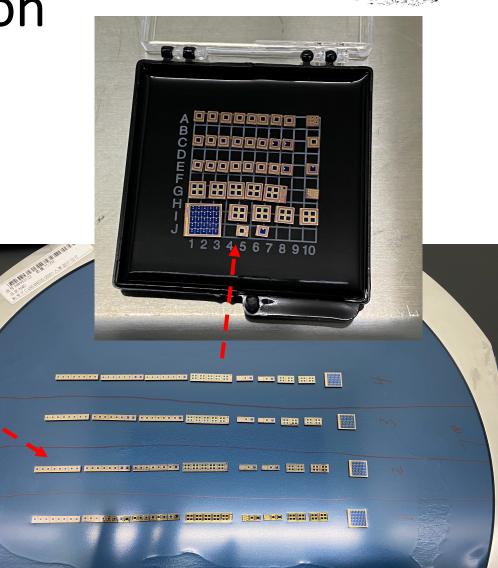
Wafer	N Energy	Carbon
#1	mid	+
#3	mid	-
#7	mid	-
#8	high	-

Quadrant	P dose		
1	low		
11	mid		
	high		
IV	extremely high		



### **Sensor Separation**

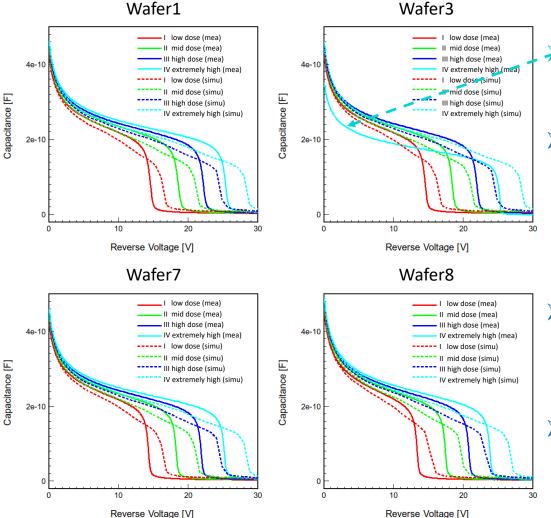




Kewei IHEP



### **CV** Test and Simulation

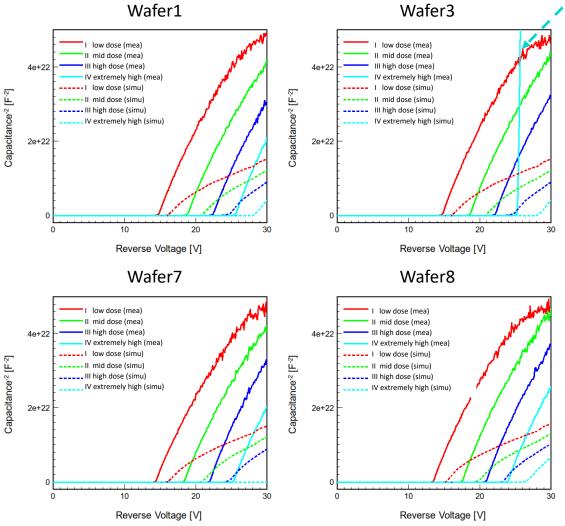


#### Wafer3 Quadrant IV problem

- In simulation (dash line), higher gain layer depletion voltages show up from low dose to extremely high dose. Because the gain layer doping concentration is increasing.
- In measurement (solid line), higher gain layer depletion voltages show up from Quadrant I to Quadrant IV.
- The change of CV measurement fit with CV simulation.



### C<sup>-2</sup>V and Simulation

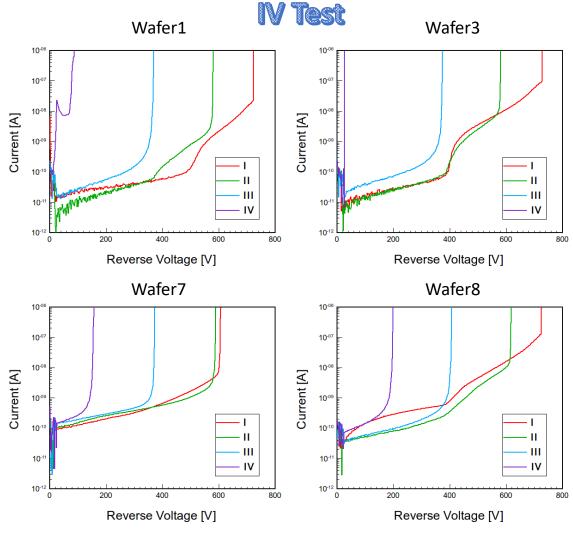


#### Wafer3 Quadrant IV problem

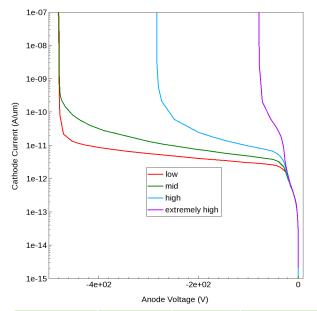
Vafer	Quadrant	V <sub>gain layer</sub> difference
1	1	16 V →14.5 V (-9.4%)
	11	21 V →18.5 V (- 11.9%)
	111	24.5 V →22.5 V (-8.2%)
	IV	28 V →25.5 V (-8.9%)
3	1	16V → 14.5V (-9.4%)
	11	20.5V → 18.5V (-9.8%)
		23.5V → 21.5V (-4.3%)
	IV	28V → 25V (-10.7%)
7	1	16V → 14V (-12.5%)
	11	20.5V → 18.5V (-9.8%)
		24V → 21.5V (-10.4%)
	IV	31V <del>→</del> 25V (-19.4%)
8	1	15V → 13V (-13.3%)
	11	19.5V → 17.5V(-10.3%)
		23V → 20.5V (-10.9%)
	IV	26.5V → 24V (-9.4%)



### IV Test and Simulation



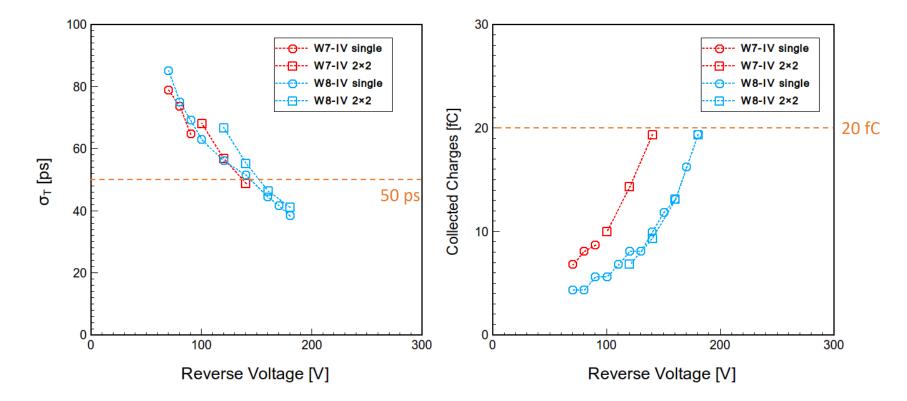
#### W7 IV Simulation



Quadrant	Simu BV	Test BV	Difference
I	76 V	148 V	+94.7%
11	283 V	370 V	+30.7%
<i>III</i>	479 V	584 V	+21.9%
IV	479 V	602 V	+25.7%

BV in measurement fit with simulation.
Baseline design BV has the least difference.

# Time Resolution & Collected Charges W7-IV & W8-IV



#### > 50ps and 20 fC in room temperature achieved, better results expected in low temperature.



### Summary & Plan

- ✓ CV and IV measurements show high uniformity for first production IHEP-IME LGAD. Low I<sub>leakage</sub> (<100pA) for medium and high boron dose implantation.
- ✓ Carbon implantation achieved low leakage current (<100pA, U<200V).
- ✓ Pretty charge collection and time resolution before irradiation (<50ps,</li>
   >20fC).
- ✓ TCAD process simulated results are close with measurements but process calibration is necessary in next run.

Proton (CIAE) and neutron (JSI) radiation for IMEv1 sensors.
 Large size production in IMEv2 production.

## Thanks for your listening!



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