

new2

Monday, 26 October 2020

Silicon Detector (08:30 - 10:00)

-Conveners: Meng Wang; Massimo Caccia; Sasha Rozanov; Harald Fox

time	[id]	title	presenter
08:30	[133]	Overview of CEPC silicon detectors	Dr ZHU, Hongbo
08:50	[134]	Development of CMOS pixel sensors with high resolution and low power for the CEPC vertex detector (MOST1)	Ms ZHANG, ying
09:05	[135]	Status of the TaichuPix chip for the high-rate CEPC Vertex Detector (MOST2)	Mr WEI, Wei
09:20	[136]	TowerJazz 65nm process for a high resolution pixel detector	GONELLA, Laura
09:40	[137]	ARCADIA (Advanced Readout CMOS Architectures with Depleted Integrated sensor Arrays)	GIUBILATO, Piero

Silicon Detector (14:00 - 16:50)

-Conveners: Meng Wang; Massimo Caccia; Sasha Rozanov; Harald Fox

time	[id]	title	presenter
14:00	[126]	Current and future development on CMOS pixel sensors	Dr HU-GUO, Christine Prof. BAUDOT, Jerome
14:20	[127]	From ATLASPix3 to CEPCPix1	ZHANG, Hui PERIC, Ivan
14:40	[128]	status on the silicon tracker demonstrator	Prof. ANDREAZZA, Attilio
14:50	[129]	R&D on low mass mechanics and highly integrated cooling for future inner tracking devices	
15:05	[130]	Recent development in LGAD detectors	SADROZINSKI, Hartmut
15:25	[131]	Lessons learned from the ATLAS upgrade tracker design	ELSING, Markus
15:45	[132]	ALICE ITS3: ultra-thin, wafer-scale, flexible MAPS for truly-cylindrical, minimum-material-budget inner tracking layers	MAGER, Magnus