

TaichuPix-2 test results

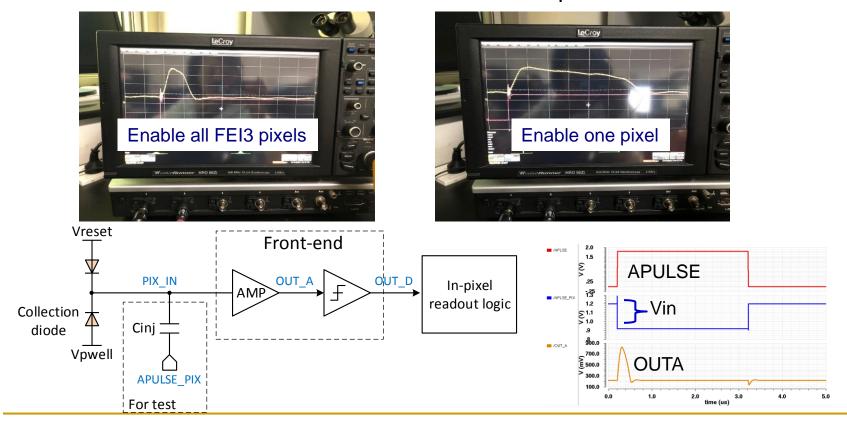
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Pixel analog output



- General test setup
 - VDD=1.8 V (AVDD=DVDD), VHIGH/VLOW provided by DAC on test board
- Number of enabled pixels affect the amplitude and duration of analog output

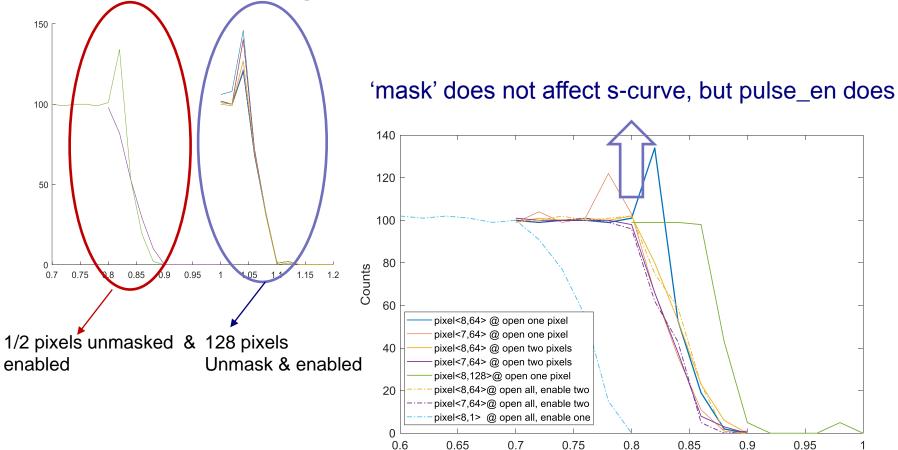
OUTA0 vs. different number of pixels enabled



S-curve



VHIGH=1.2 V, scanning with different VLOW



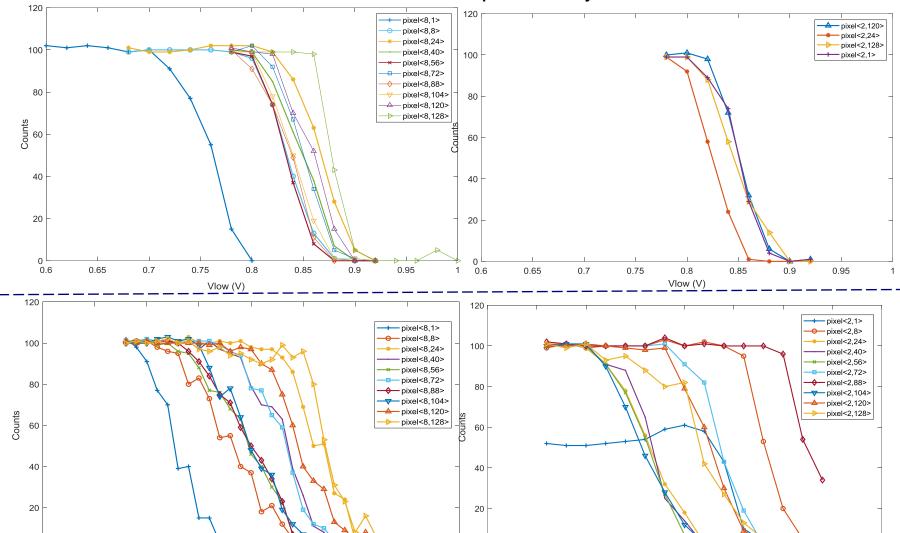
Vlow (V)

In the following test, all FEI3 pixels are unmasked



S-curve

Enable one pixel every time



Enable 10 pixels simultaneously

0.75

8.0

Vlow (V)

0.7

0.65

0.6

0.95

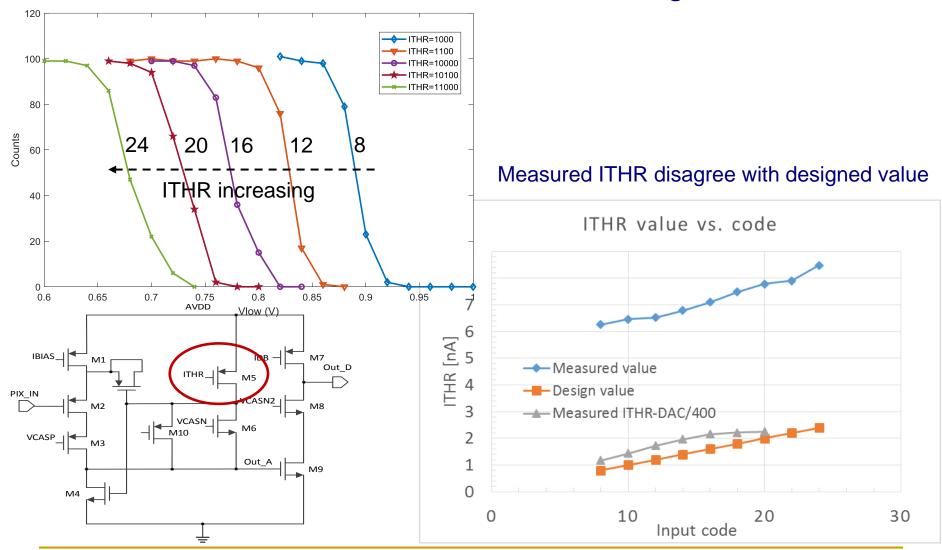
0.85

Vlow (V)

Effect of ITHR



Threshold increases with ITHR, coincides with design



Summary and plan



- With one pixel enabled, the amplitude and duration of analog output disagree with design
 - Effect of crosstalk/resonance
 - BIAS condition different with design?
 - Fine adjustment of bias V&I has done, no bias set found same analog output with design
 - Measured min. noise ~13 mV, min. threshold ~300 mV
- S-curve measurement need to be done with one pixel enable every time
 - Rough max. threshold dispersion ~200 mV (pk-to-pk), measured with one column

Next step:

- S-curve for each sector, evaluate different FE design
- Find the reason of crosstalk/resonance problem