Update of TPC prototype with the lower power consumption ASIC

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TPC prototype with new ASIC chips

2 Design the pixelated TPC module

3 Contribution for Snowmass and Summary



• Testing of TPC prototype with new ASIC chips



Low power ASIC chip- WASA_V0 testing board



Testing parameters:

- GEMs detector: 280V-310 V
- $E_{drift}: \leq 280 \text{ V/cm}$
- Operation gases: Ar/CF4/iC4H10 95/3/2 (T2K)
- Radioactive source: 55Fe@ 1mCi
- Channels: 128channels ($2 \times 4 \times 16 = 128$ channels available)
- External power supply: $\pm 5V$, $\pm 12V$, $\pm 24V$

New electronics testing with the module

⁵⁵Fe testing

Testing parameters:

- GEMs detector: 280V-310 V
- E_{drift}: ≤280 V/cm
- Operation gases: Ar/CF₄/iC₄H₁₀ 95/3/2 (T2K)
- Radioactive source: ⁵⁵Fe@ 1mCi
- Successfully commissioned and collected signals using DAQ







Time resolution and the different charge



Time resolution of ASIC chip @100 fC, 10 mV/fC, 30 MS/s Time resolution of the different charge input

New electronics testing with the prototype

- Successfully realized the joint test of low-power ASIC chip and TPC prototype
- ASIC+TPC parameters
 - TPC:
 - GEM: 280 V
 - Driftlength: 500mm
 - <u>E drift</u>: 180 V/cm
 - Gas: <u>At/CF4/iC4H1095/3/2(T2K)</u>
 - UV laser: 7.2 mJ @20 Hz
 - Laser tracks: 3 layers along drift length
 - Electronics:
 - Trigger by UV laseer
 - Gain: 20 mV/fC
 - Sample frequency: 30 MS/s



UV laser track reconstruction and position resolution



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Transvers diffusion and δ_v VS UV laser power

$$\sigma_y^2 = \frac{D_T^2}{N_{eff}}(z - z_0) + \frac{h^2}{12N_{eff}} + \frac{w^2}{12N_{eff}},$$

Transvers diffusion of Laser TPC prototype: $D_T = (310.7 \pm 7.6) \, \mu m / \sqrt{cm}$

- Simulation results using Garfield++ compared the experimental results with the different UV laser power
- The experimental data fit is close to the simulation result
- The optimization of the UV laser power will be set at 1-1.8uJ/mm^2

Next steps:

- Analyze the electric field with the D_T
- Analyzed the electric field with the spatial resolution



- Design the pixelated TPC module
 - Good electromagnetic shielding and low noise
- Simulation and discussion of pixel TPC for CEPC at Z are ongoing with LCTPC collaboration
 - Chang Yue, Yu Liwen, Yuan Zhiyang, Huirong Qi
 - LCTPC collaboration group

Design the pixelated TPC module



Low noise TPC module for the pixelated readout

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Design the pixelated TPC module

• The design has been completed, and the production has begun (2 weeks)



Contribution for TPC Snowmass white paper

MPGDs for TPCs at future lepton colliders

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4 TPC for CEPC

The Circular Electron Positron Collider (CEPC) has been proposed as a Higgs/Z factory in China [11]. The baseline design of a CEPC detector consists of a tracking system composed of a vertex detector with three concentric double-sided pixel layers, a high precision (about 100 µm) large volume TPC and a silicon tracker in both barrel and end-cap regions. The tracking system has similar performance requirements as for the ILD detector, but without power-pulsing, which leads to additional constraints on detector specifications, especially for the case of the machine operating at Z-pole energy with high luminosity. Until a decision on a tracker for a future circular collider in China can be reached, a number of tasks are still remaining regarding the TPC research. Such tasks include the full simulations of the TPC performance in the CEPC environment, further design of the low power consumption readout electronics, UV laser calibration methods and cooling options [12]. Some of the key challenges to be addressed in the near future are the physics requirements for the TPC performance towards the inclusive CEPC physics program. MPGD technology, though quite far advanced in some aspects, still needs a significant effort from key partners. Nonetheless, the CEPC TPC requirements and challenges for the detector are similar than the ones de-March, 2022 cribed for the ILD, and thus achievable with existing MPGD technologies. R&D activities Warch, 2022 are actively orgoing in China and could potentially lead to partnership with the USA.

This submission will focus on advancements and advantages of Micro Pattern Gas Detector (MPGD) technologies together with their applications for the construction of a dedicated Time Projection Chamber (TPC) that can serve as an excellent main tracker for any multipurpose detector that can be foreseen to operate at a future lepton collider. The first portion of the report will be the executive summary. It will be followed by sections detailing on applications of MPGDs specifically for the construction of the LCTPC for the ILD at ILC, for a possible upgrade of the Belle II detector and for the design of a TPC for a detector at CEPC. MPGD technologies offer synergy with other detector R&D's The destination of the second state of the se

- Successfully testing and collected signals using the new electronics with the lower power consumption chips
- UV laser tracks were reconstructed and simulation results using Garfield++ compared the experimental results with the different UV laser power
- The design of the pixelated TPC module with the low noise has been completed, and the production will be done in two weeks
- Some contributions has been done for MPGD Snowmass whitepaper from IHEP

Thanks for your attention.