



Status of high spatial resolution pad and pixelated TPC technology R&D for CEPC

Huirong Qi

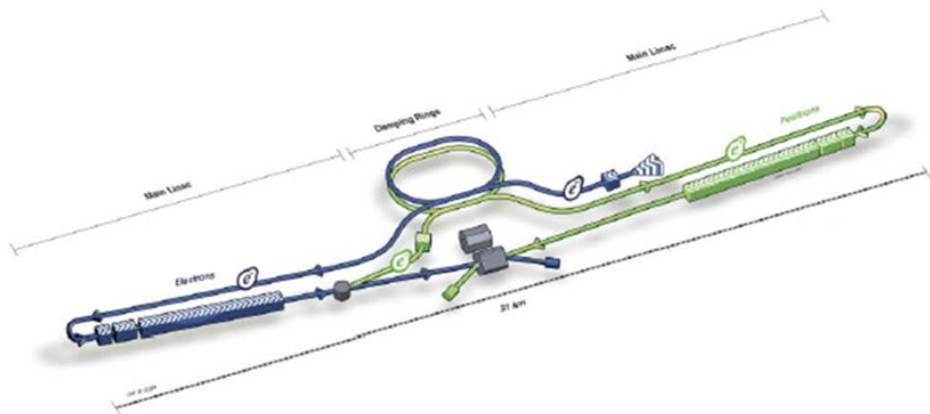
Xin She, Yue Chang, Liwen Yu, Jian Zhang, Hongliang Dai, Jinxian Zhang, Zhiyang Yuan
Linghui Wu, Gang Li, Shengsen Sun, Manqi Ruan and on behalf of CEPC TPC study group

**2022 international workshop on the high energy Circular Electron Positron Collider
October 24-28, 2022, Beijing**

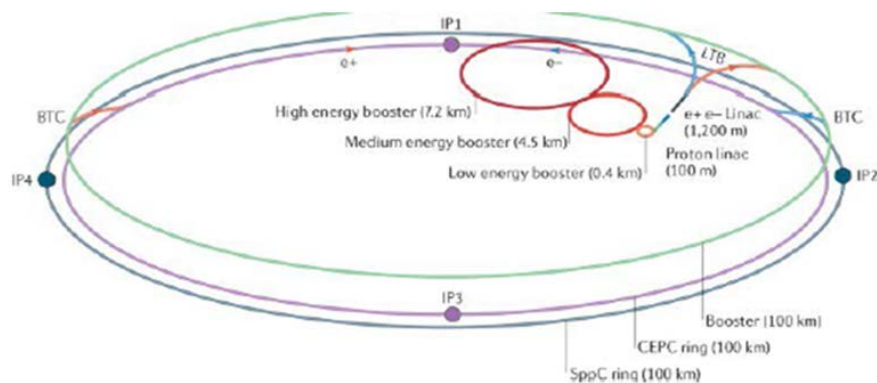
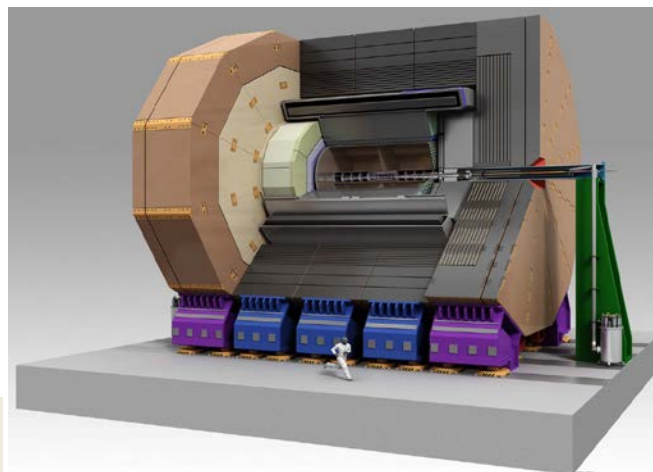
- **TPC detector for e⁺e⁻ colliders**
- **High spatial resolution pad readout TPC**
- **Pixelated readout TPC R&D**
- **Summary**

TPC technology for the future e+e- colliders

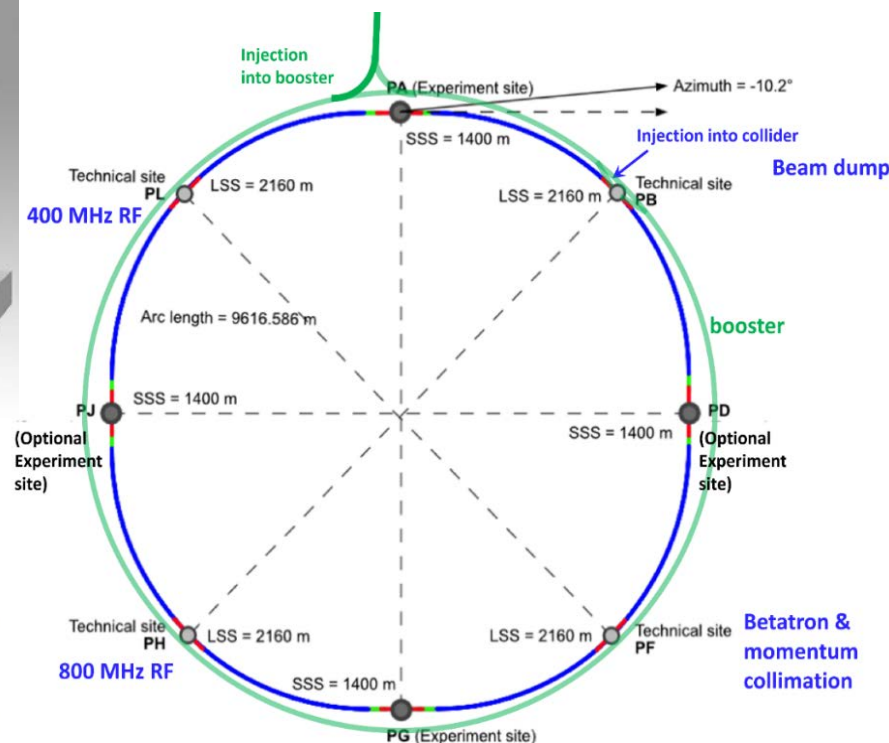
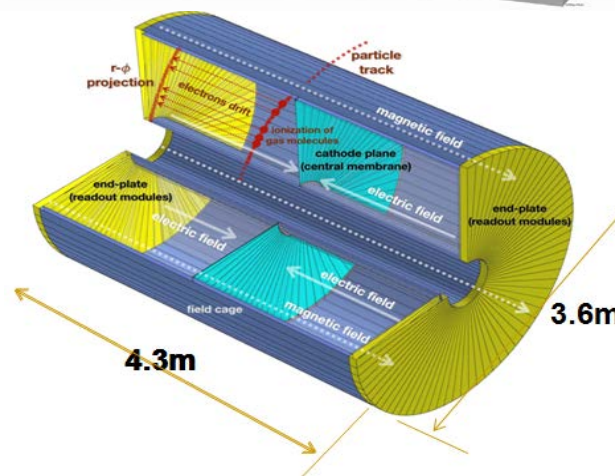
- A TPC is the main tracking detector for **some candidate experiments at future e+e- colliders**
 - ILD at ILC and the baseline detector concept of CEPC
- TPC technology can be of interest for other future e+e- colliders



International Linear Collider (ILC)



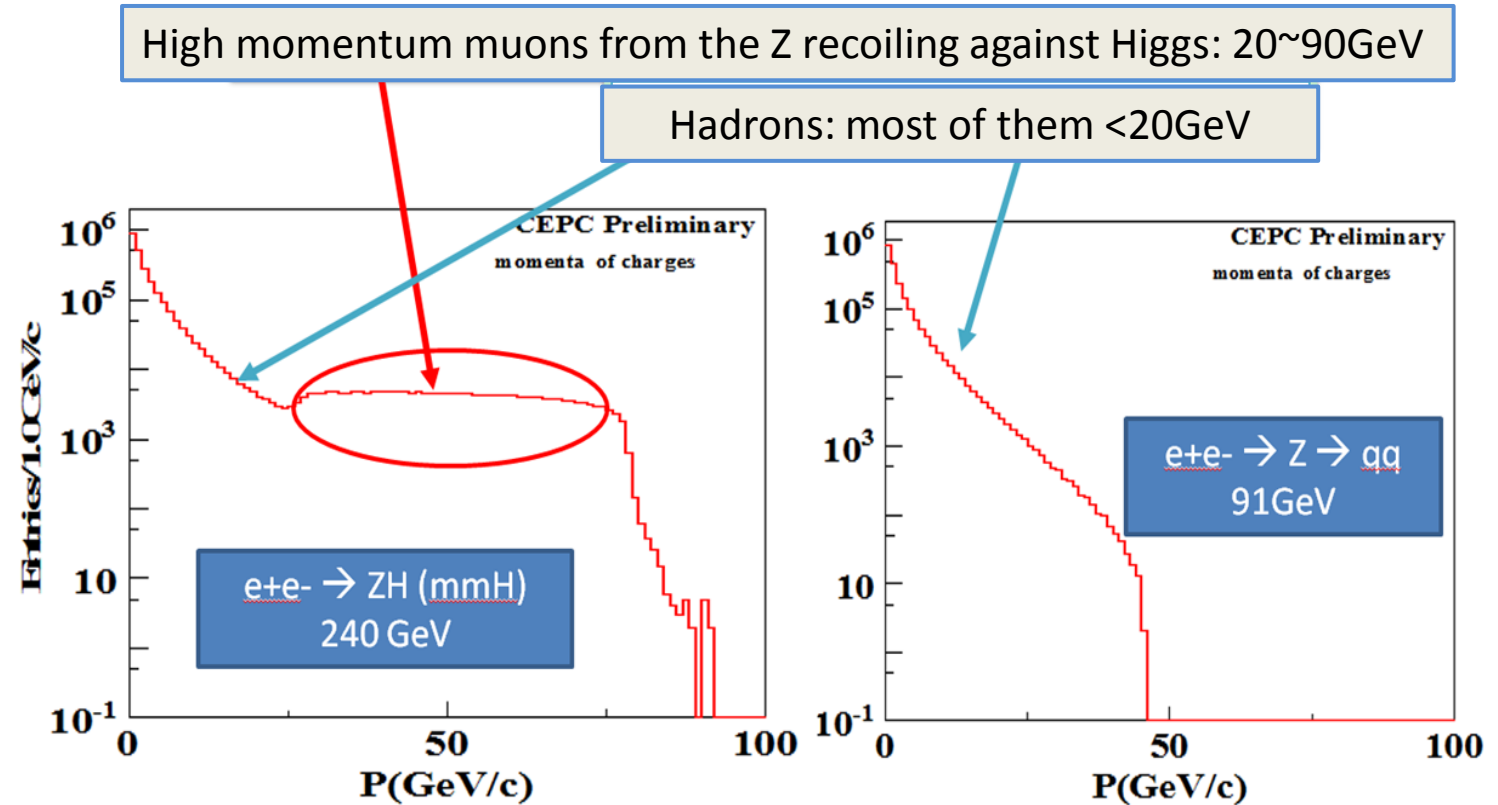
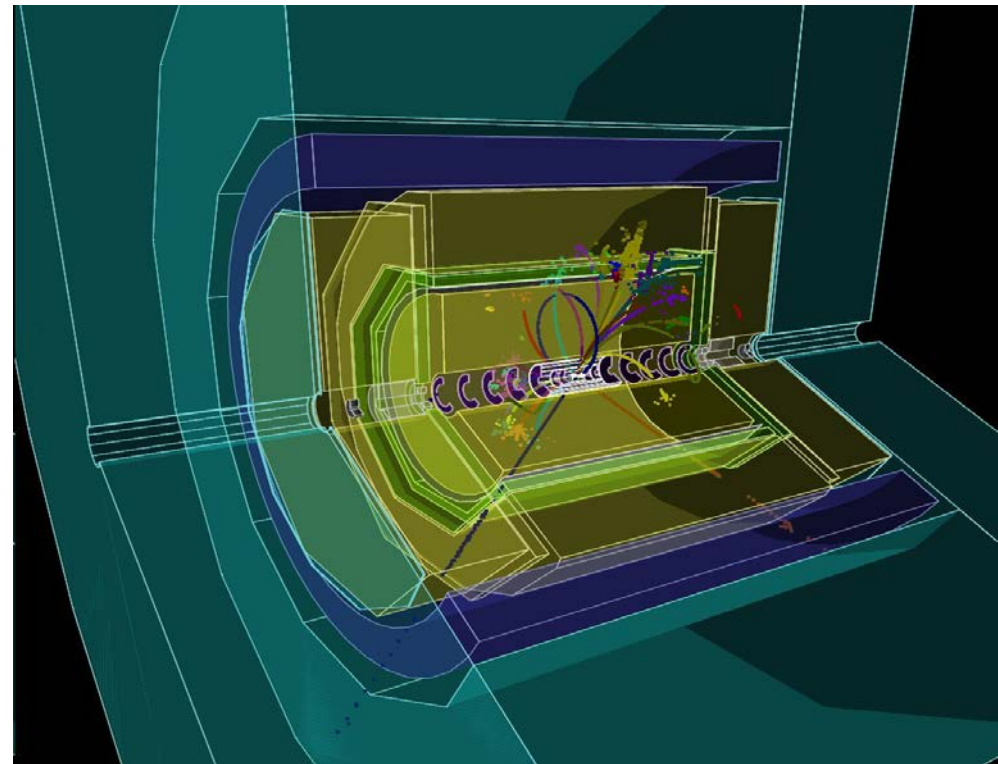
Circular Electron Positron Collider (CEPC)



Future Circular Collider (FCC-ee)

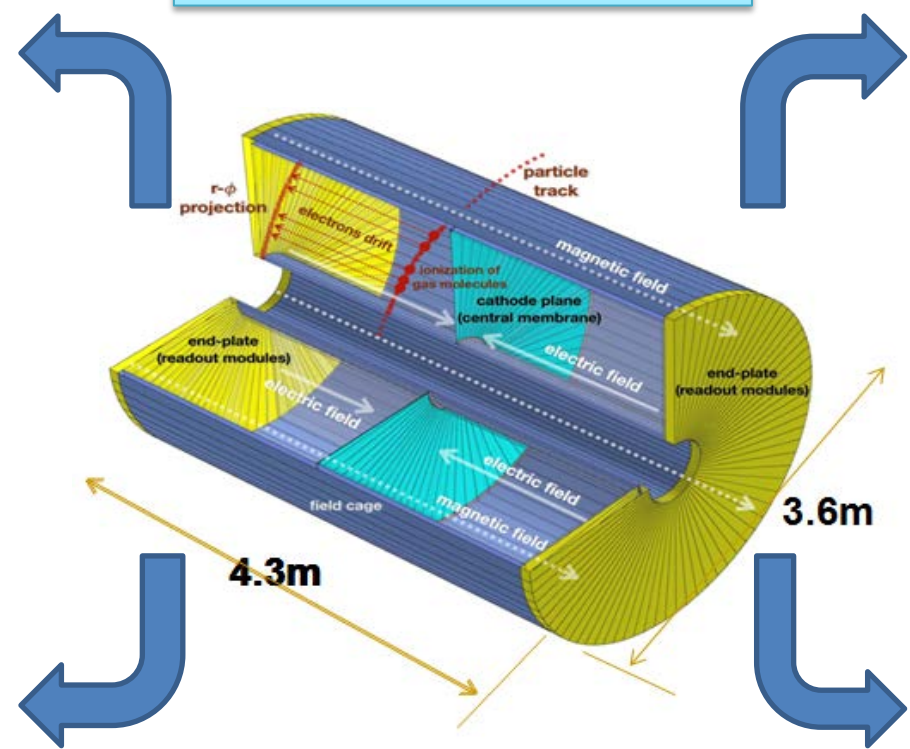
TPC requirements from e⁺e⁻ Higgs/EW/Top factories

- TPC can provide hundreds of hits (for track finding) with high spatial resolution compatible with PFA design (**very low material** in chamber)
 - $\sigma_{1/pt} \sim 10^{-4} (\text{GeV}/c)^{-1}$ with TPC alone and $\sigma_{\text{point}} < 100 \mu\text{m}$ in $r\phi$
- **Provide dE/dx and dN/dx with a resolution** <4%
 - Essential for Flavor physics @ Z run
 - Beneficial for jet at higher energy



Key issues of TPC technology for e+e- collider

TPC track detector for e+e- collider



- Pad readout TPC**
- To meet Higgs physics
- 1mm × 6mm of Pad
- TPC module
- TPC prototype with UV laser

- Pixelated readout TPC**
- To meet Z physics
- ~500μm of Pad
- TPC prototype with UV laser track
- dN/dx+dE/dx study

- Ion back flow study**
- Simulation of Ion Backflow
- Test the UV light created the ions by photoelectric effect
- Experimental study

- PID performance Study**
- Simulation of the ionization cluster in space
- PID studies of the different readout TPC prototype
- Experimental study

Pad and pixelated readout TPC technology for CEPC

- TPC as the main tracker detector to satisfy the physics requirements :
 - For Higgs, W and top running, **no problem** for all TPC readout technologies.
- For high luminosity (2×10^{36}) Z running
 - Pixelated readout TPC is a good option at **high luminosity** on the circular e+e- collider
 - Pixelated readout TPC is a realistic option to provide
 - High spatial resolution **under 2T or 3T magnetic field**
 - Better momentum resolution
 - High-rate operation (MHz/cm²)
 - dE/dx and Cluster counting (**in space**)
 - Excellent two tracks separation

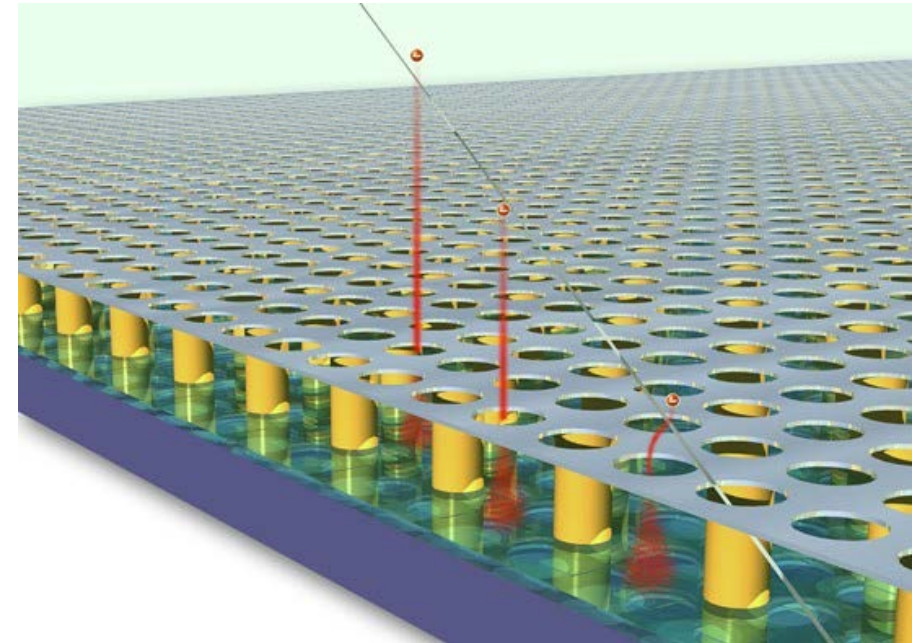
Standard charge collection:

Pads (1 mm × 6 mm)/ long strips

Pixelated readout:

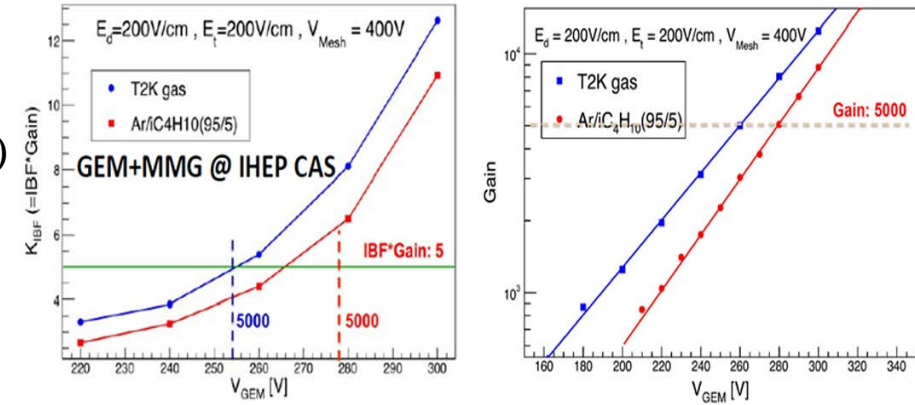
Bump bond pads are used as charge collection pads.

55μm × 55 μm or larger

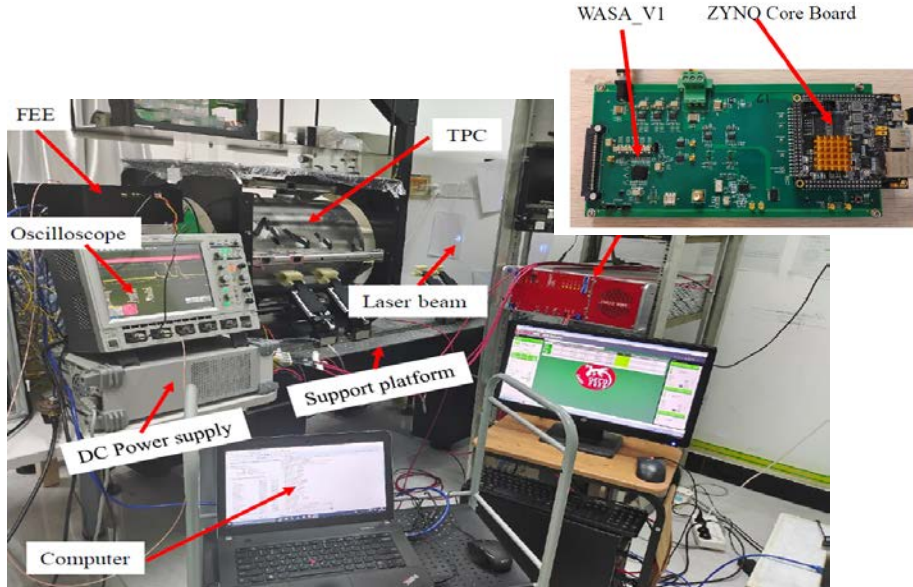


CEPC TPC detector R&D

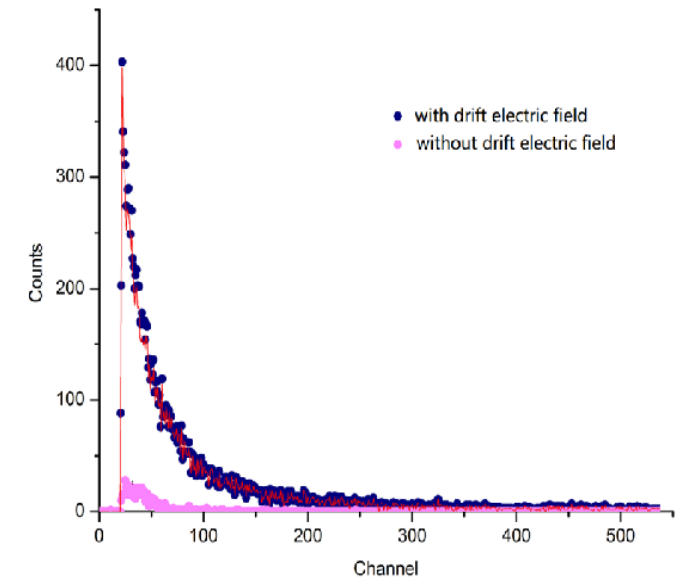
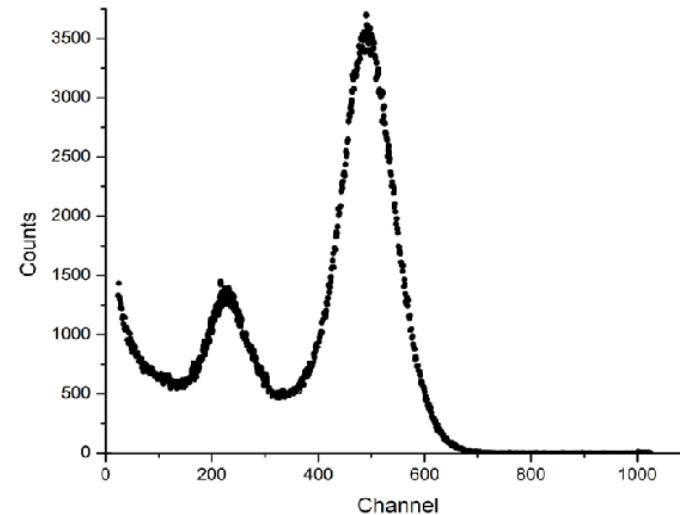
- CEPC TPC detector prototyping roadmap:
 - From TPC module to TPC prototype R&D for beam test
 - Low power consumption FEE ASIC (**reach <5mW/ch** including ADC)
- Achievement by far:
 - Supression ions hybrid GEM+Micromegas module
 - $IBF \times Gain \sim 1$ at **Gain=2000** validation with GEM/MM readout
 - Spatial resolution of $\sigma_{r\phi} \leq 100 \mu m$ by TPC prototype
 - dE/dx for PID: <4% (as expected for CEPC baseline detector concept)



GEM+Micromegas module R&D



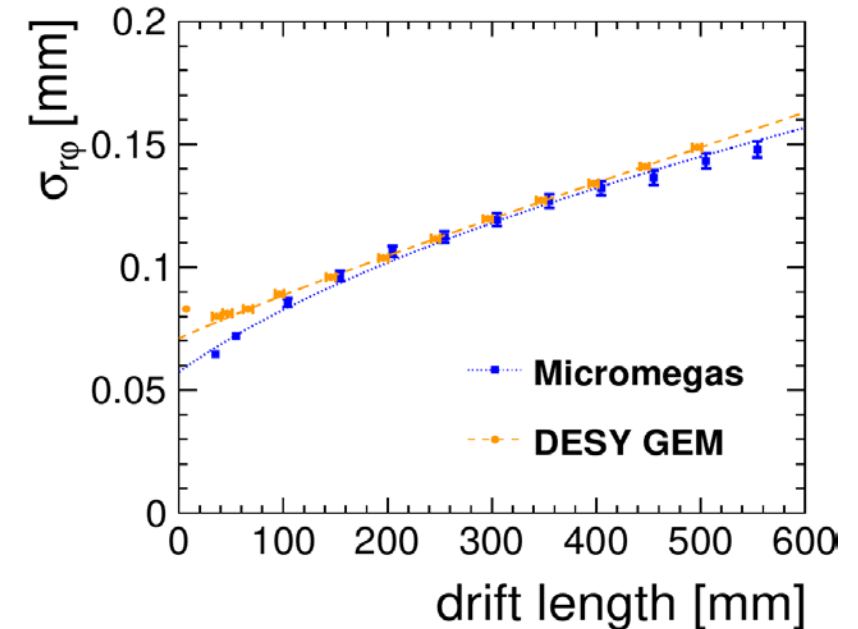
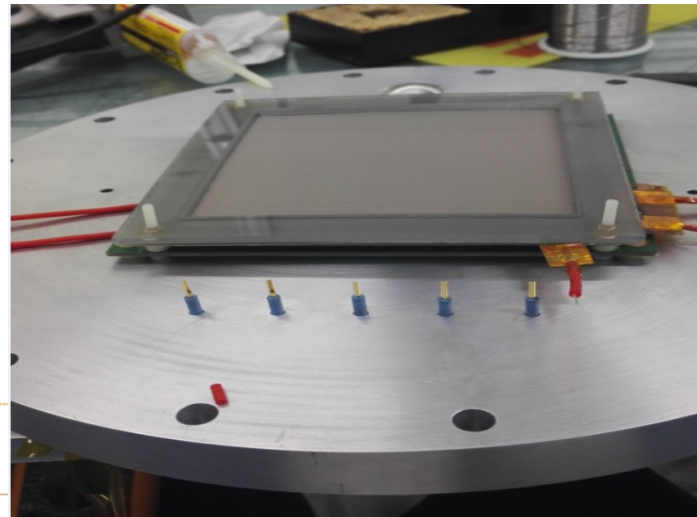
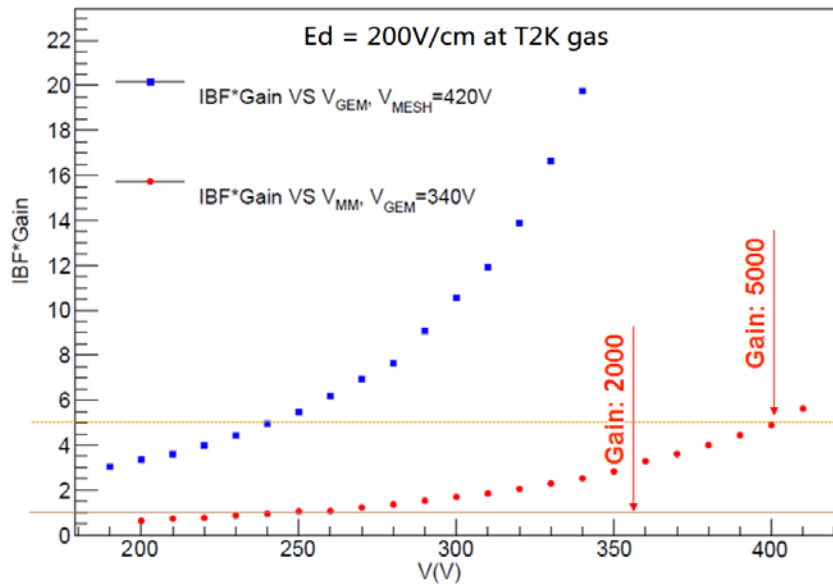
Low power consumption readout



⁵⁵Fe X-ray spectrum profile(Left) and cosmic ray spectrum(Right)

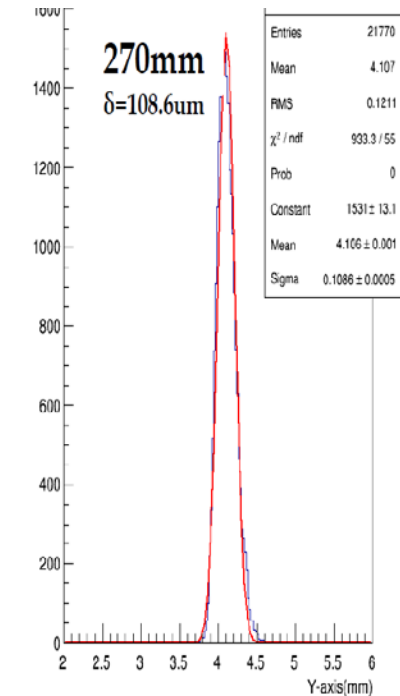
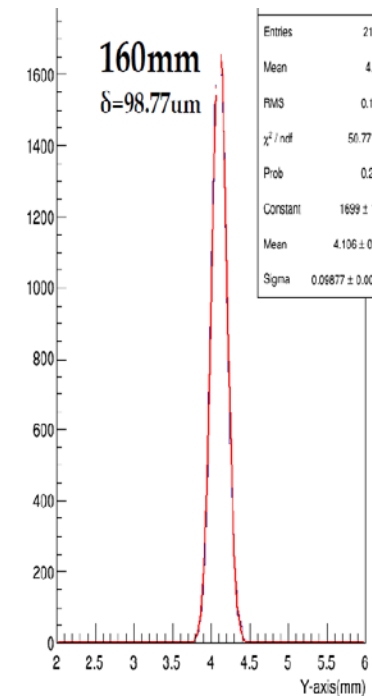
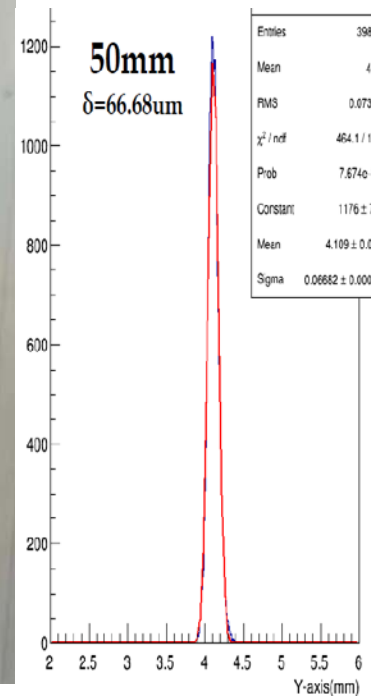
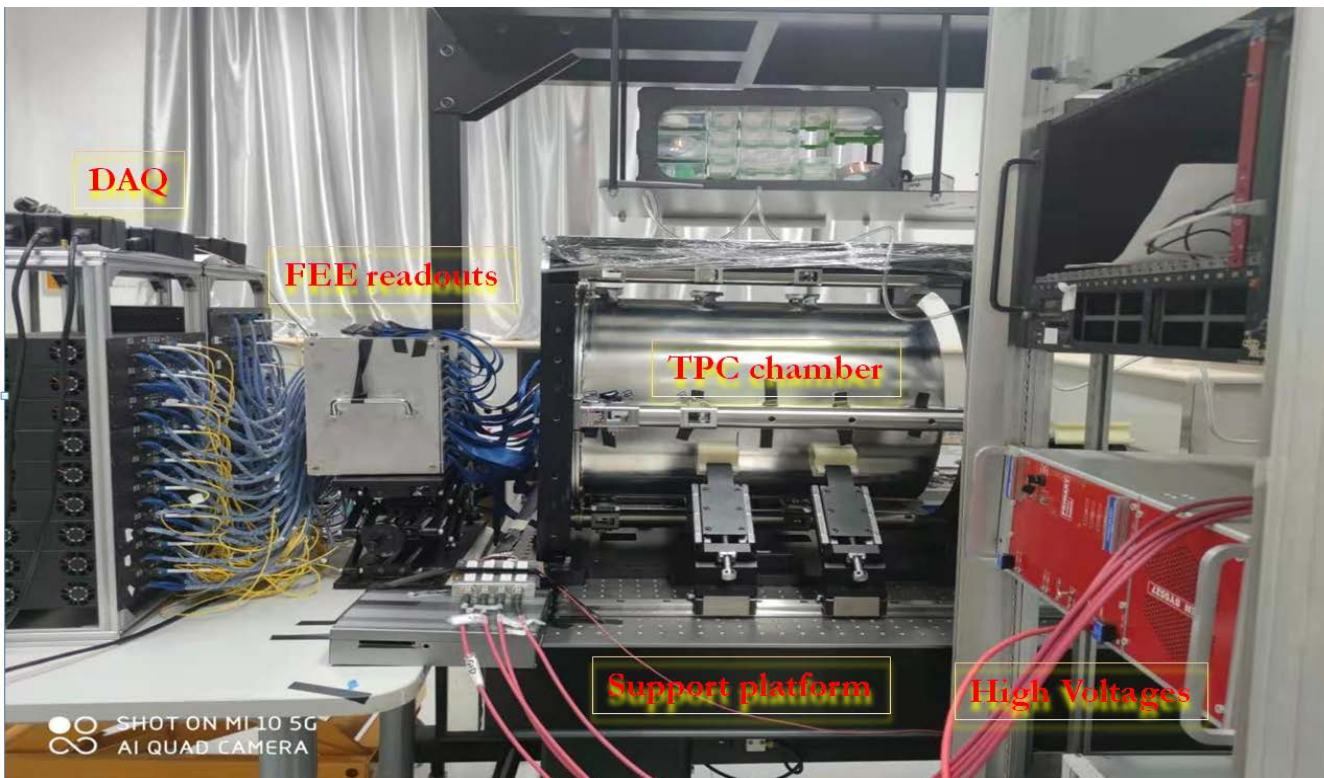
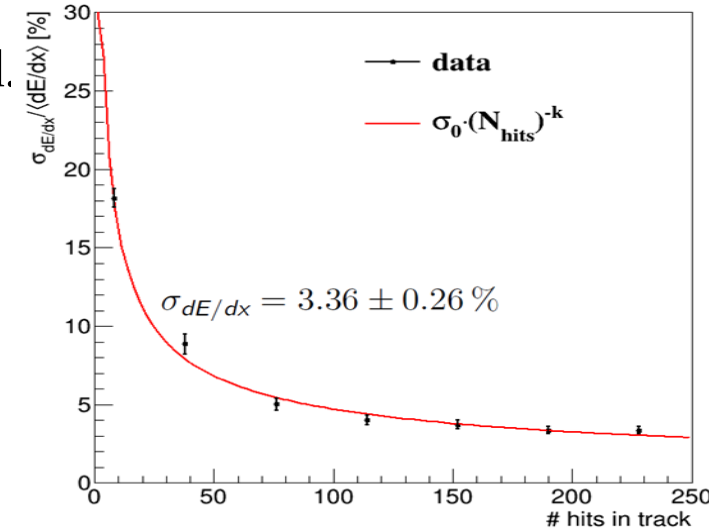
#1 Pad TPC technology – Detector Module

- For CEPC
 - Combined Micromegas + GEM readout has been developed, which promises a **lower ion backflow** (IBF) at CEPC TPC group without gating option.
 - $IBF \times Gain \sim 1$ at total gain of 2000 (primary ionization level)
- For LCTPC
 - GEM and Micromegas groups have finished analysis of test beam data with previous set of detector modules
 - Both technologies show **very similar performance**.



#2 TPC prototype with 266nm UV laser tracks

- The TPC prototype integrated 266nm UV laser tracks has successfully developed.
- Analysis of UV laser signal, the spatial resolution, dE/dx resolution
 - Spatial resolution can be less than **100 μm along the drift length** of TPC prototype
 - Pseudo-tracks with 220 layers (**same as the actual size of CEPC baseline detector concept**) and dE/dx is about $3.4 \pm 0.3\%$



#3 Low power consumption readout ASIC R&D

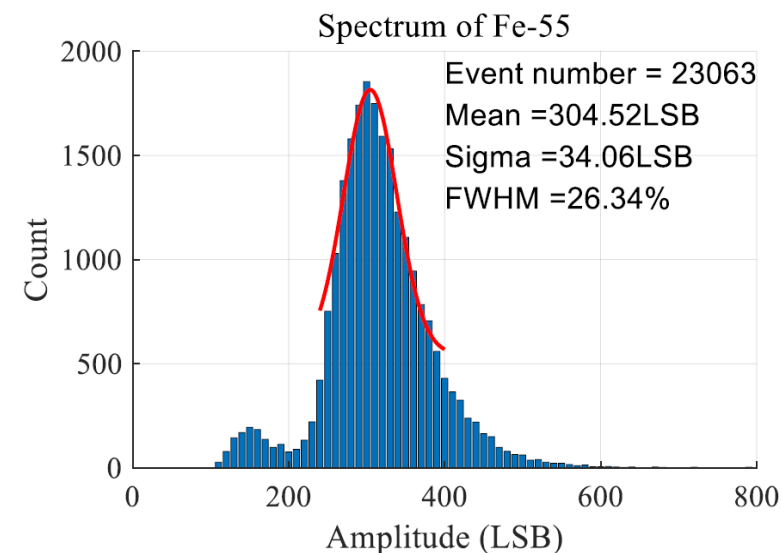
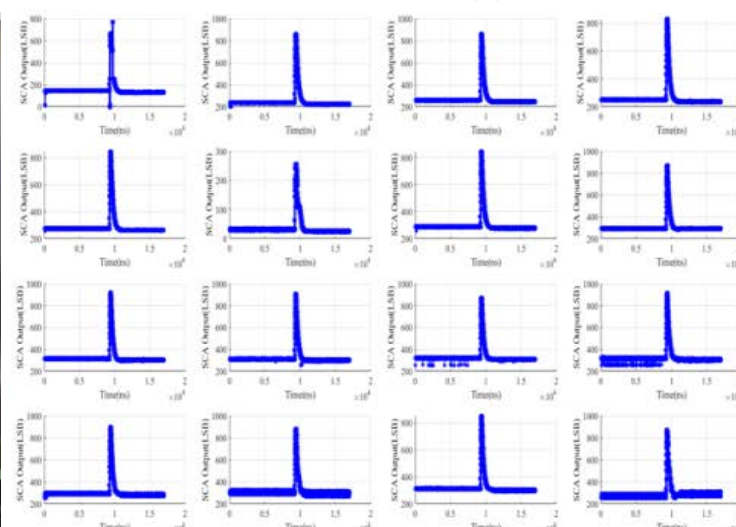
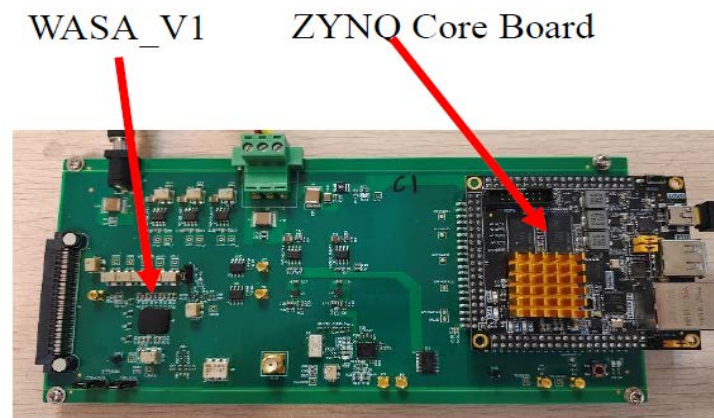
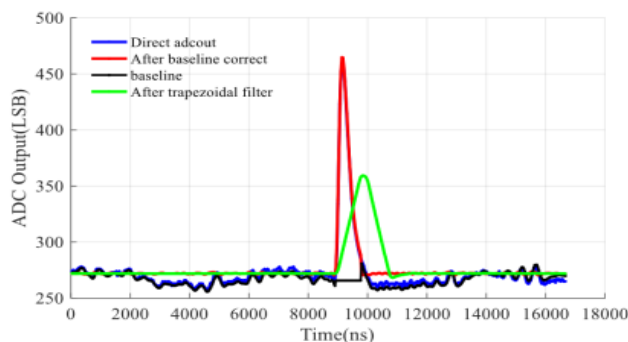
- WASA V1 has been developed: 16 channel AFE+ADC+LVDS data output
- Total power consumption with ADC function: ~ 2.4 mW/ch
- Tested with TPC detector using 128 channels at IHEP

See Zhi Deng's talk today

⁵⁵Fe testing

Testing parameters:

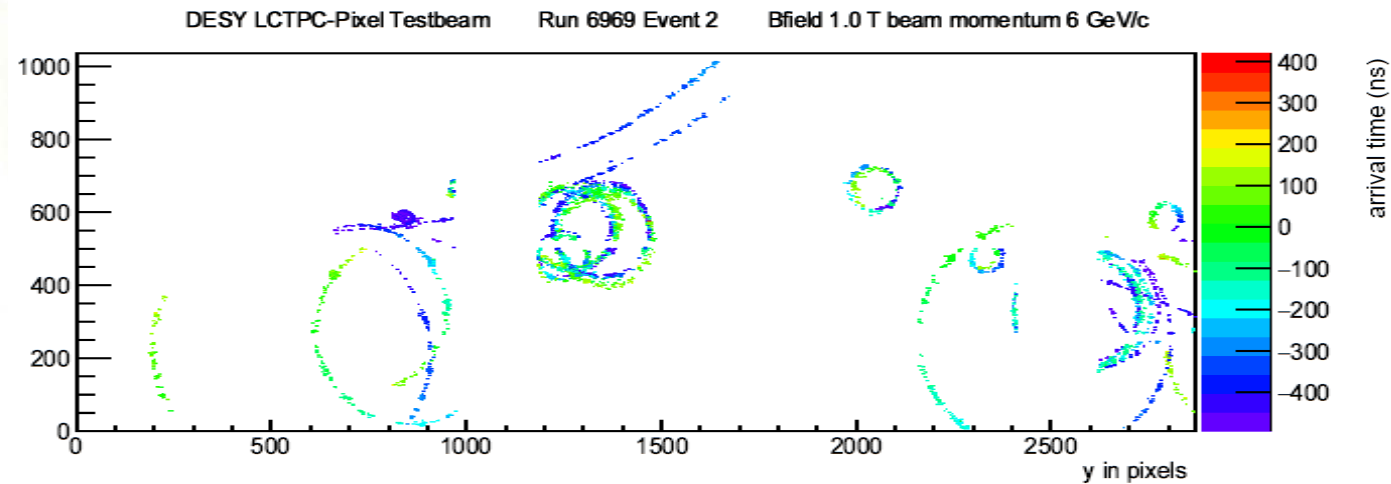
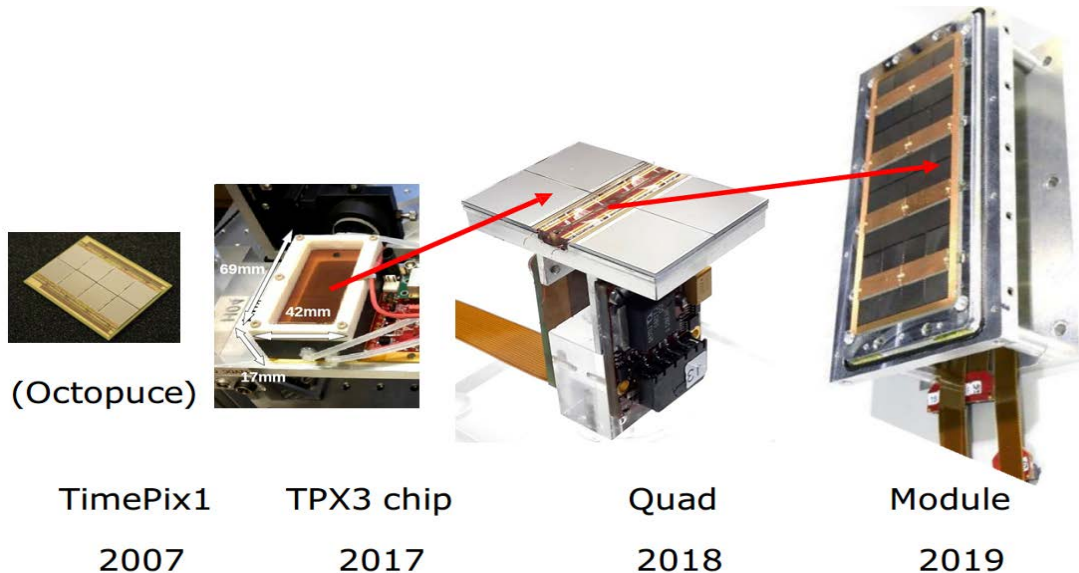
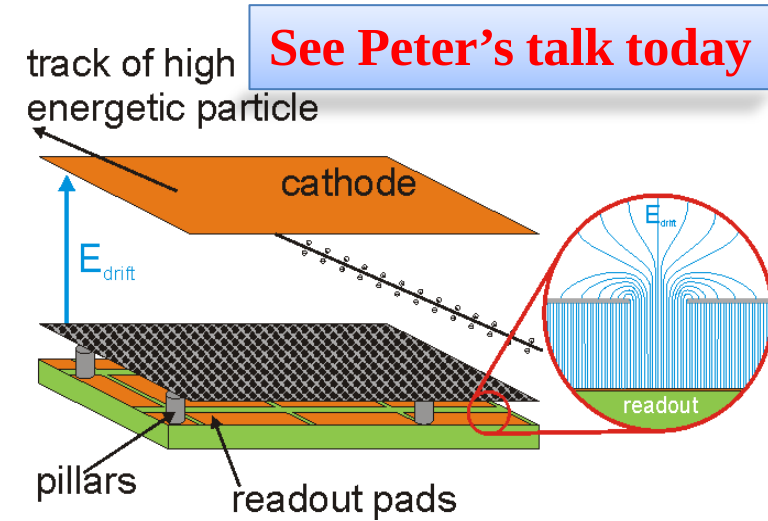
- GEMs detector: 280V-310 V
- $E_{\text{drift}}: \leq 280$ V/cm
- Operation gases: Ar/CF₄/iC₄H₁₀ 95/3/2 (T2K)
- Radioactive source: ⁵⁵Fe@ 1mCi
- Successfully commissioned and collected signals using DAQ



- Towards pixelated readout TPC technology

Pixelated TPC technology - Timepix3-based GridPix

- GridPix detector have moved from Timepix to Timepix3 ASICs. Tests with single and quad devices have been successfully done.
- A module **with 32 GridPixes has been constructed** and was in a test beam in $B=1.0T$ at DESY in June 2021.
- Very high detection efficiency results in **excellent tracking and dE/dx performance**. Timepix4 development is ongoing.
- Ion back flow of the module has been measured and can be further reduced by applying a double grid and the resistivity of the protection layer.



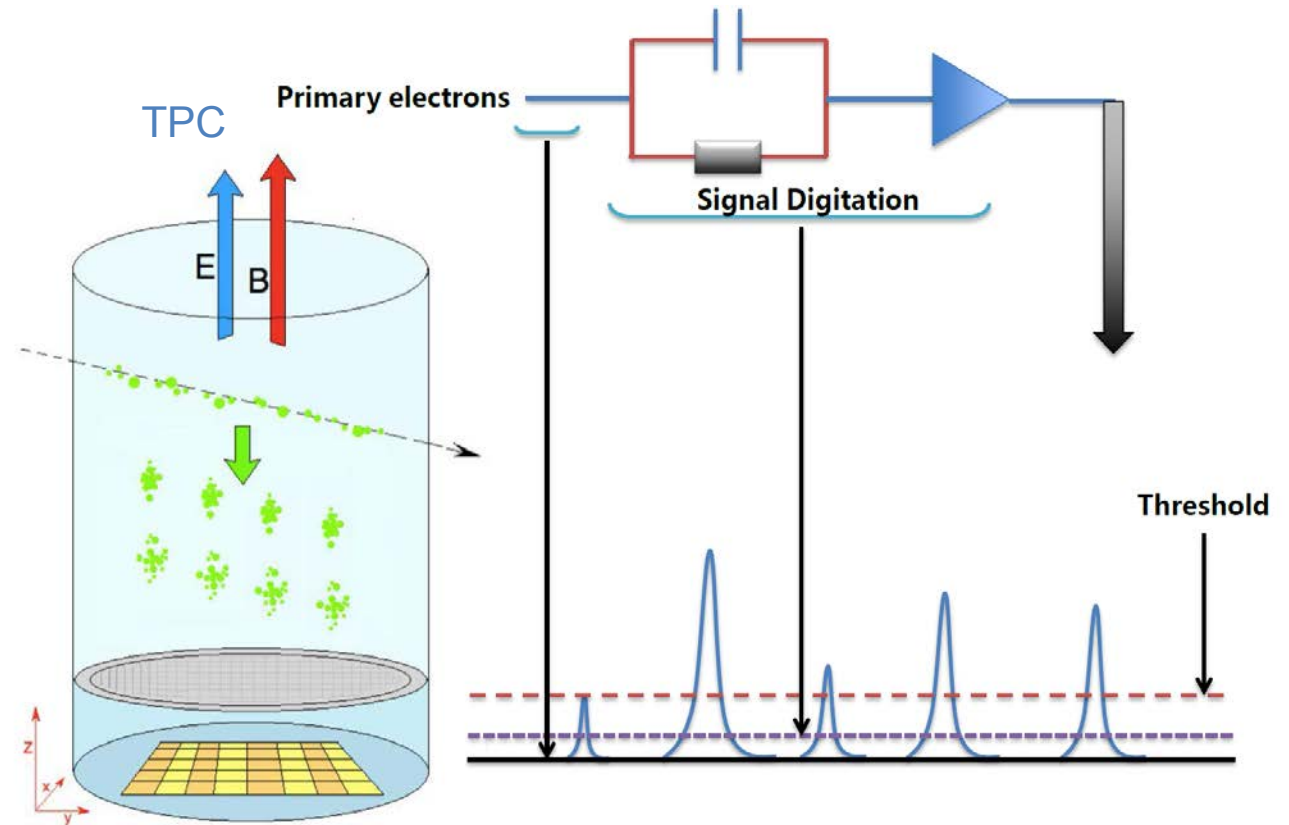
DESY testbeam in June 2021

Identify the clusters to achieve dN/dx and Occupancy

See Yue Chang's talk today

dN/dx cluster counting

- Challenging for the **low power consumption** electronics ($>40\text{mV/fC}$ needed at 2000 of gas gain)
- Pixelated readout
 - \rightarrow **high granularity readout in endplate**
 - \rightarrow the reasonable pixilation reveals the underlying cluster structure in 3D chamber
- Occupancy of the pixelated TPC
 - Occupancy is very **key issue** at the high rate or high luminosity
 - Smaller pad/pixel size
 - \rightarrow **smaller occupancy**
 - To be addressed by R&D
 - \rightarrow A detailed simulation would be necessary to determine the scaling factor
 - \rightarrow Simulation ongoing at IHEP

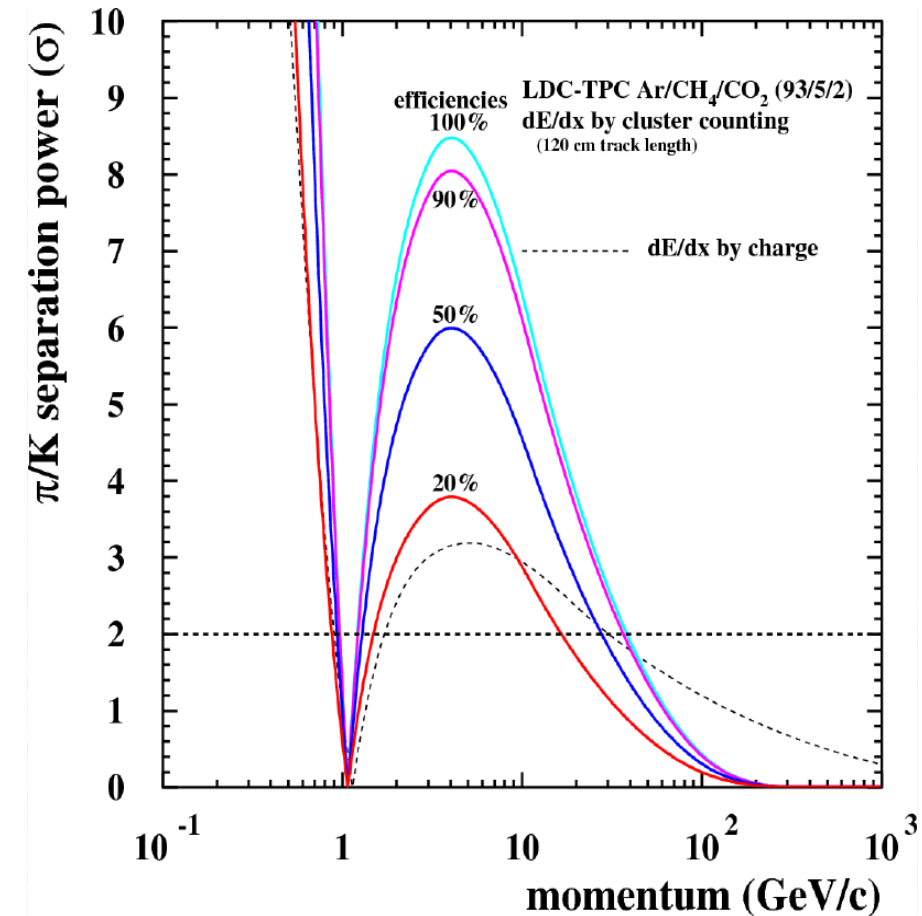


High granularity for improved PID in TPC

- For **traditional dE/dx detection**, the charge summation is performed using the center-of-gravity method.
- In most experimental study from small to large TPC
 - L: track length
 - N: number of readout rows
 - Constant L and changing granularity $G = N/L$

$$\frac{\sigma_{dE/dx}}{\langle \mu_{dE/dx} \rangle} \propto L^{-0.45} G^{-0.13}$$

- If pad size is at the level of cluster distances of primary ionization
 - i.e. **$\sim 300\text{-}500 \mu\text{m}$ in Ar-based**
 - Cluster counting becomes effective
- PID improvement
 - The potential of **better resolution by at least a factor 2**
 - Novel method studied by several R&D groups for the TPC for the e^+e^- collider



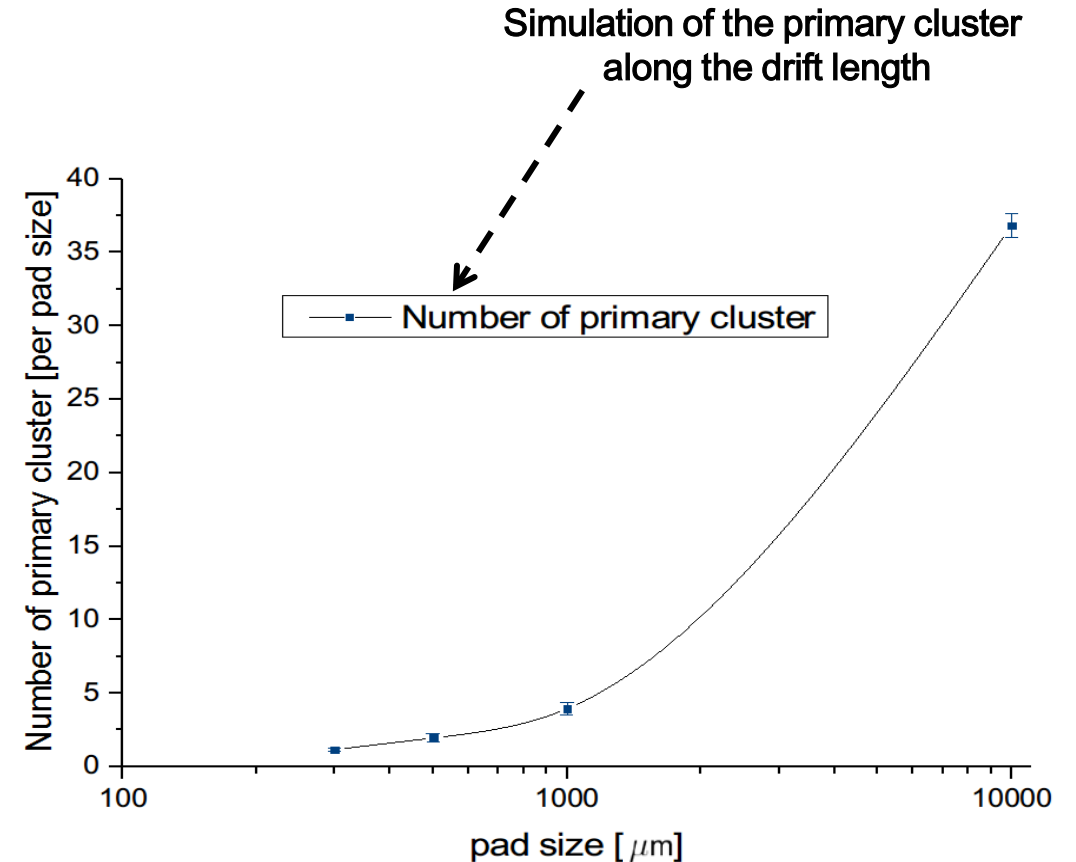
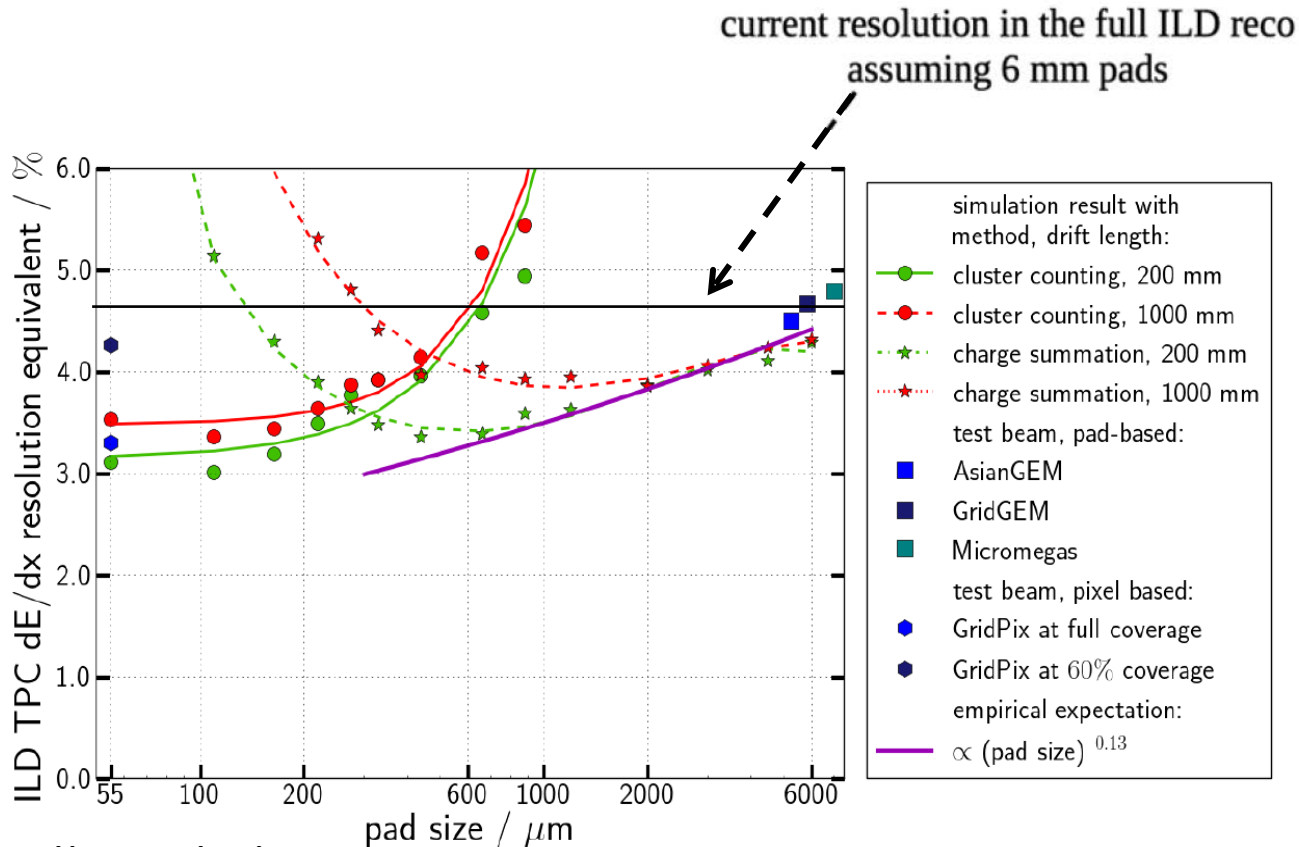
Hauschildt

<http://ific.uv.es/~ilc/ECFA-GDE2006/0>

High granularity for improved PID in TPC

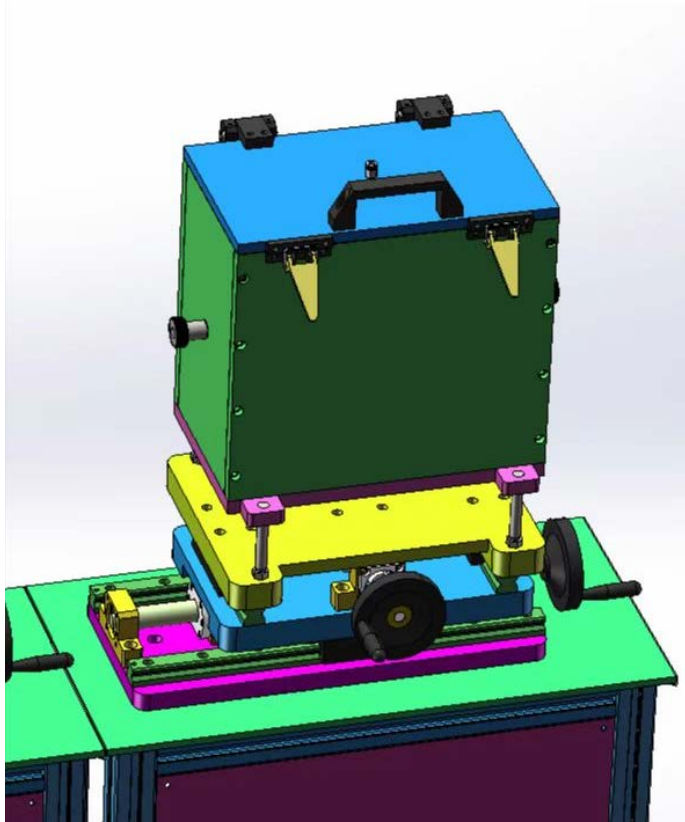
- Current full ILD reconstruction: 6mm pads \rightarrow **$\sim 4.8\%$ dE/dx resolution**
- 6mm \rightarrow 1mm: 15% improved resolution via the charge summation (dE/dx)
- 6mm \rightarrow 0.1mm: 30% improved resolution via the cluster counting (dN/dx)
 - Pad size of about 300 μm can record **~ 1 primary cluster along track length** at T2K gas
 - High **readout granularity** VS the primary cluster size optimization

All studies ongoing



New TPC prototype design and optimization (v0 -> v1)

- Study some new parameters complemented previous circular TPC
- Cascaded TPC detectors to test dE/dx and IBF distortion
- Plan: Single TPC detector to test **under 1.0T beam test in DESY in 2023**
- New FEE ASIC chip wafer production **will received in November.**(500um pixelated readout based)



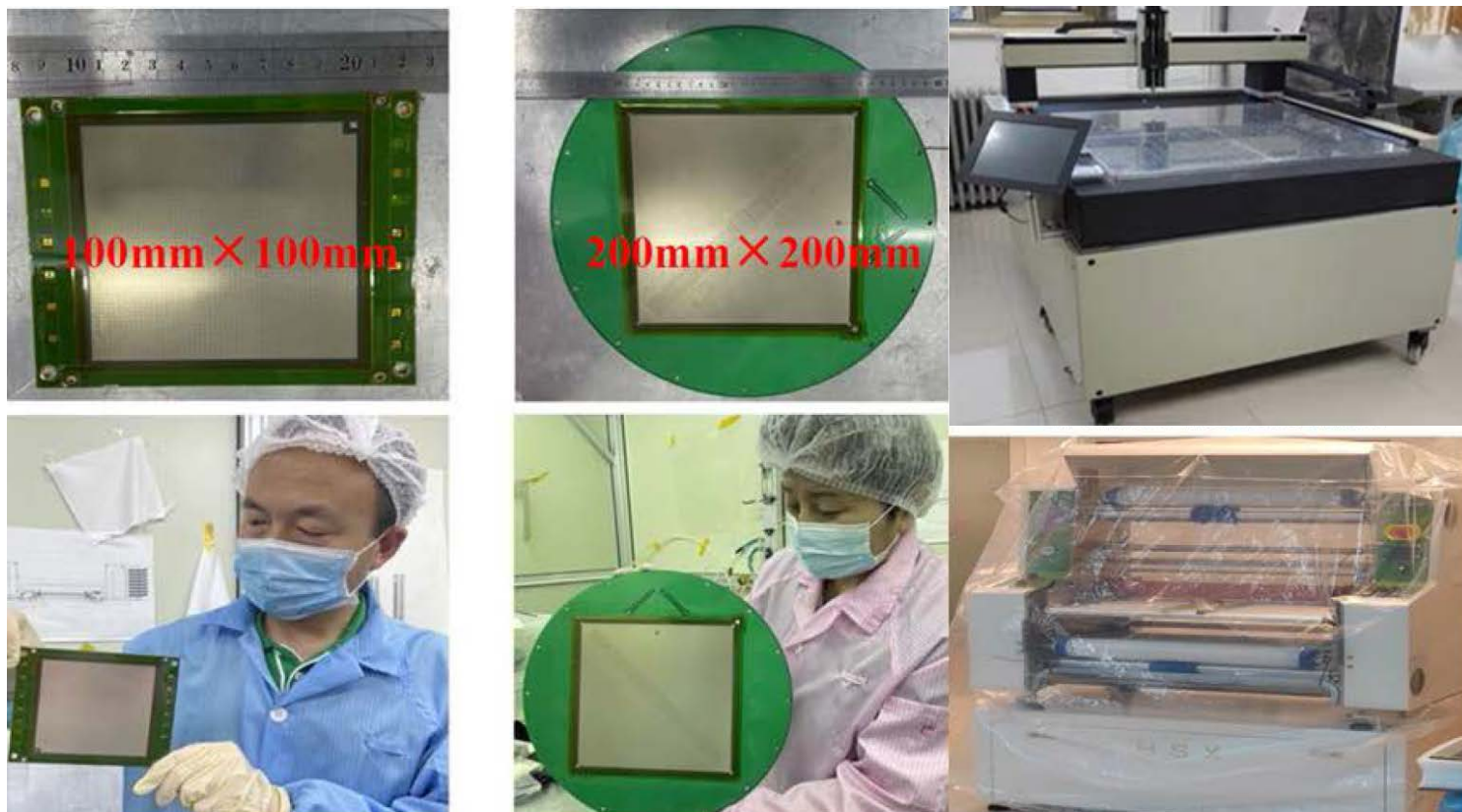
Bump bond pixelated readout with Micromegas detector	Module size	To be addressed by R&D
<ul style="list-style-type: none">• $\geq 300 \mu\text{m} \times 300 \mu\text{m}$• Developed the readout chip by Deng Zhi (Tsinghua)• Developed the Micromegas detector sensor at IHEP• Development of the new module and prototype	1-2 cm ²	<ul style="list-style-type: none">• Research on pixelated readout technology realization• Optimization of cluster profile and pad size• Study of the '$dN_{cl}+dx$'
	100 cm ²	<ul style="list-style-type: none">• Study the distortion using UV laser tracks and UV lamp to create ions disk• In-situ calibration with UV Laser system• Study of the '$dE/dx+dN_{cl}/dx$'

Highlights of CEPC TPC technology R&D

- Pad readout towards pixelated readout TPC to increased PID with dN/dx
- Massive production and assemble MPGD lab has been setup at IHEP
- Very active internal CEPC TPC R&D roadmap
- Active international collaboration with LCTPC and RD51

Publications by TPC group:

<https://doi.org/10.1016/j.nima.2022.167241>
<https://doi.org/10.1109/NSS/MIC44867.2021.9875566>
<https://doi.org/10.1088/1748-0221/15/09/C09065>
<https://doi.org/10.1088/1748-0221/15/05/P05005>
<https://dx.doi.org/10.1142/S0217751X20410146>
<https://doi.org/10.1088/1674-1137/41/5/056003>
<https://doi.org/10.1088/1748-0221/15/02/T02001>



CEPC-TPC regular meeting	11 events
Laser-TPC	124 events
Public conference	12 events
Neutron TPC	1 event
https://indico.ihepa.ac.cn	
Regular meetings	
CMOS Tracker	79 events
Drift Chamber	11 events
Pixelated TPC R&D	32 events

- In CEPC TPC study group, TPC detector using the pad towards pixelated readouts have been developed for the future e+e- colliders.
- Many simulations are still necessary to understand the detailed requirements of the pixelated detector (e.g. number of ADC bits, pixel readout sizes, occupancy, ion backflow, etc.), but also new ideas are welcome.
- R&D plan focus on the Pixelated TPC readout & prototype, optimization to the local configuration (for dN/dx, power consumption, ...) and global geometry optimization (inner Radius, etc)
- Synergies with CEPC/LCTPC/FCCee/EIC/T2K/ALICE allow us to continue R&D and ongoing, we learn from their experiences.

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- National Natural Science Foundation of China (Grant NO.: 11775242)
- National Natural Science Foundation of China (Grant NO.: 11675197)
- Some good inputs from LCTPC collaboration

Many thanks!