

# DAQ system of the dual-readout calorimeter for Future e+e- colliders.



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- Towers & 1 MCP-PMT Tower
- module 2 : 8 PMT Towers & 1 SiPM Tower
- Our module require 426 channels.

|  |               | Module 1 |         | Module 2   |      |
|--|---------------|----------|---------|------------|------|
|  |               | T 1,2,4  | Т 3     | T 1~4, 6~9 | T5   |
|  | readout       | PMT      | MCP-PMT | PMT        | SiPM |
|  | # of channels | 6        | 4       | 16         | 400  |

## **Ancillary detector**

• The purpose of ancillary detector is identifying particles without DREAM calorimeter (possible to select unbiased events)



## Delay wire Dual-readout <sup>50</sup> muon counter calorimeter chamber 1 bin = 25 ps 1 bin = 400 ps 1 bin = 400 ps **DAQ system** DAQ board • One board can cover 32 channels. • Use DRS4 chip. • Use 16 pin Ribbon cable • TCB board • Control the setting value of DAQ boards and the trigger system.

• Connect DAQ boards with TCP/IP cable, cover 40 ch DAQ.

MID 10

• All boards connected with PC using USB3 line

### • specification of DRS4 chip

- DRS (Domino Ring Sampler) based on SCA (Switched Capacitor Arrays)
- Channel number of input and trigger: 8 + 1 ch
- Sampling frequency: 1~5 GSPS (1 ns ~ 200 ps/sampling depth)
- Number of sampling depth: 10 bit Power consumption: max. ~40 mW/ch



SiPM DRS4 DAG







