Tracker Meeting Minutes

 Time: 2024/3/15 10:00 – 12:00

 Participants: 常悦, DONG Mingyi (董明义), FAN Yunyun (樊云云), FU Jingyu (付金煜), LI Gang (李刚), LI Yiming (李一鸣), 李瞻, LIANG Zhijun (梁志钧), 刘灿文, GENG Qinglin (耿青林), QI Huirong (祁辉荣), RUAN Manqi (阮曼奇), 佘信, SHI Xin (史欣), WANG Meng (王萌), WEI Wei (魏微), WU Linghui (伍灵慧), XU Zijun (徐子骏), YAN Xiongbo (严雄波), 曾浩, 张瑶, ZHAO Guang (赵光), ZHAO Lei (赵雷), 赵梅

 Minutes: WANG Meng

* LG/GQL: working on layout optimization of Si inner tracker with fast simulation.
* QHR: TPC with pixelated readout
	+ readout modules optimized, sensitive coverage increased from 92% to 96%.
	+ obtained hit density at inner radius with full simulation.
	+ to report at **Tuesday** meeting.
* ZG: dN/dx simulation with TPC
	+ reconstruction efficiency decreases as drift distance decreasing (?)
	+ pad size to be optimized
* LYM: data rate estimation for CMOS tracker
	+ without including beam background, data rate per chip is less than 1 Mbps
	+ timing requirements are discussed. A dedicated meeting is anticipated.
* SX/LZ: beam background simulation, and layout study
* FYY: update number of modules, channels, etc.
* YXB: readout scheme and cost estimation for LGAD and Si strip.
* The next meeting will be held on the next Friday.