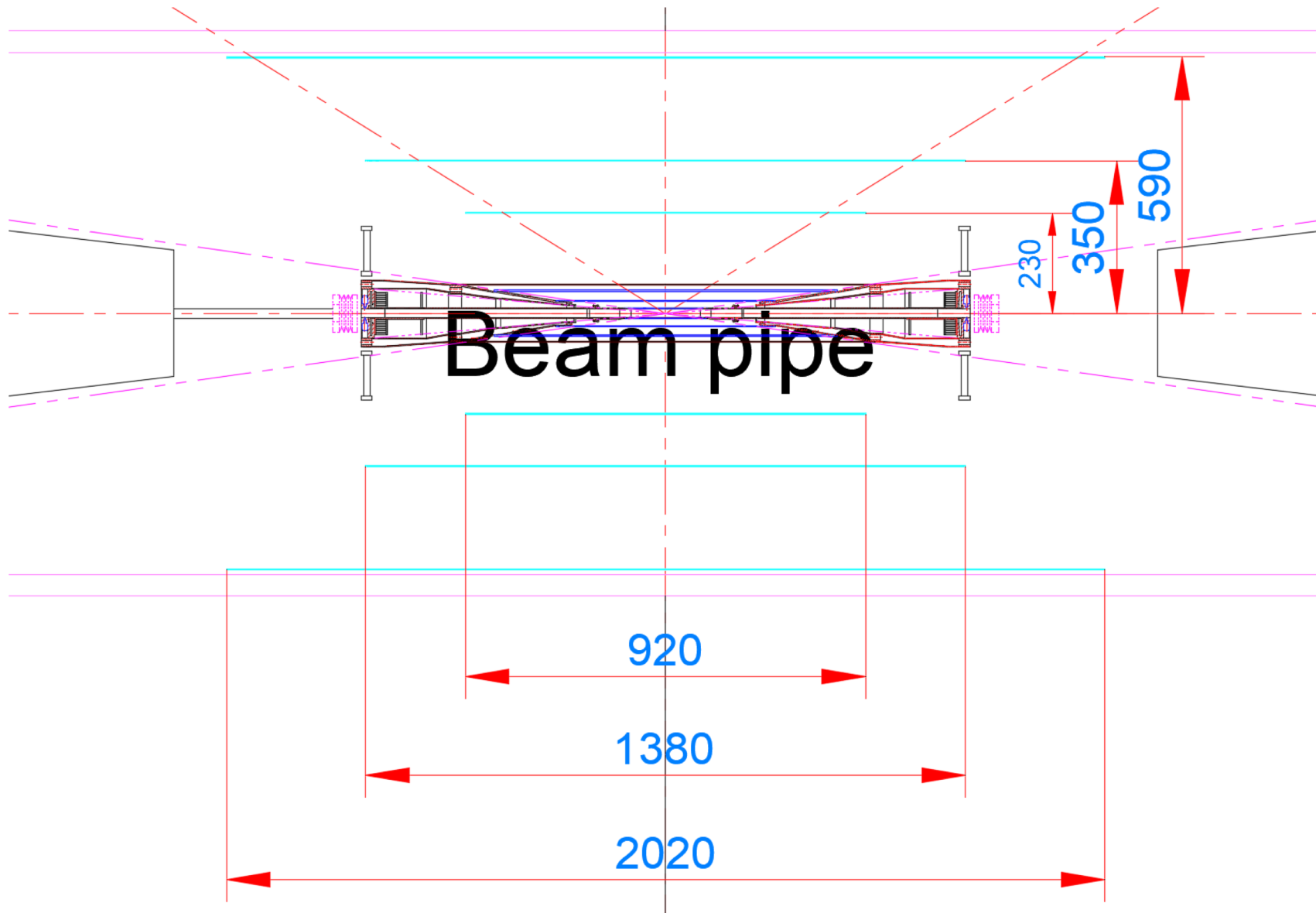


# Update on CMOS tracker

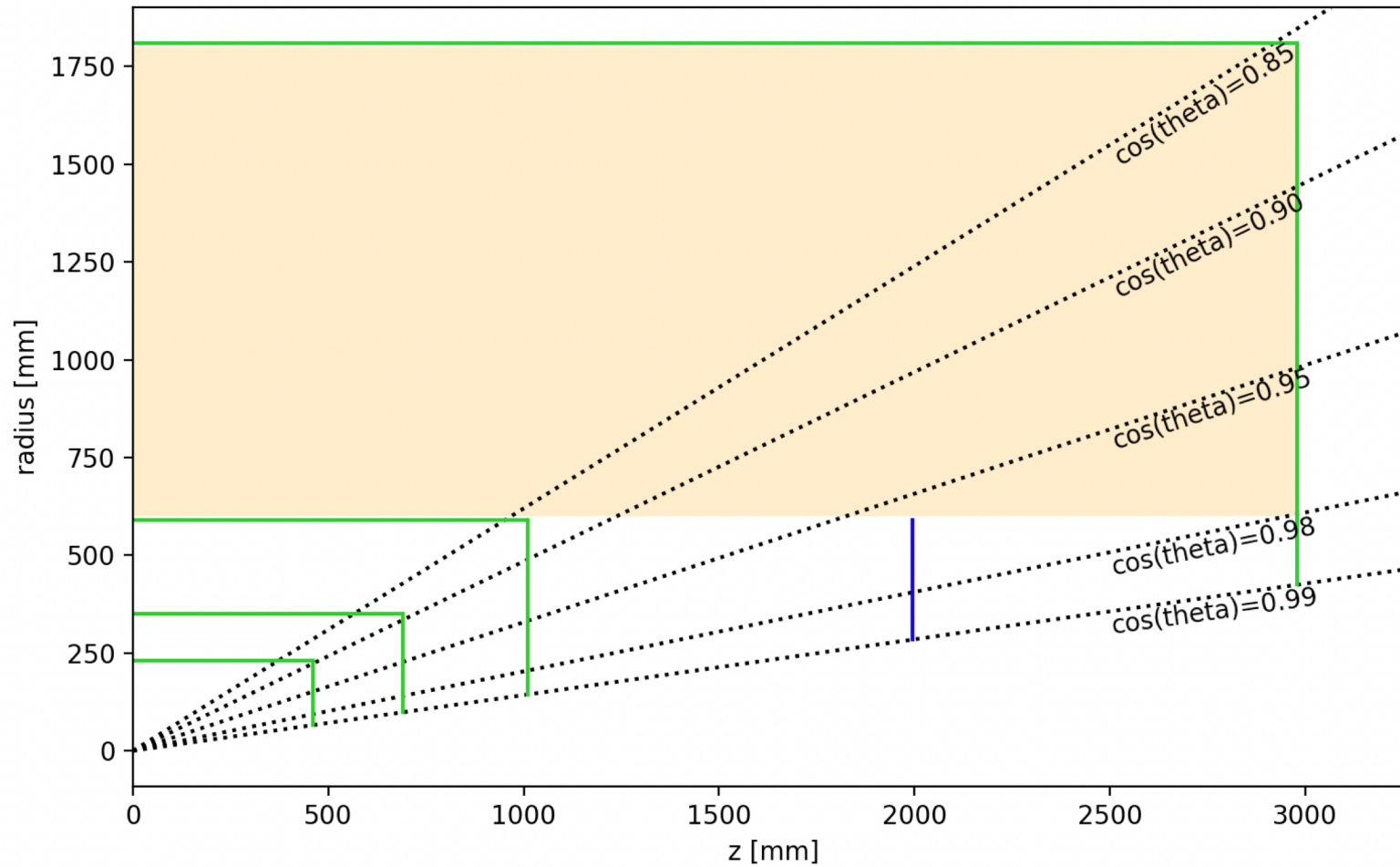
- Mechanical discussion
- Jinyu Fu, Quan Ji, Gang Li, Jianchun Wang, Yiming Li

CEPC reference TDR tracker meeting

22 Mar 2024



- Boundry conditions
- Outer: 600mm
- Inner: 15mm if vertex using long barrel
- Room for truss!
- => outermost layer at ~510mm (if support outside) or ~590mm (support inside)
- SIT mechanical design should consider **Beampipe installation** (the whole structure with vtx and Be pipe inside)



- An extra layer of endcap may be needed to ensure enough hits in forward region (position to be optimized)