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Design and high power test of 650MHz/800 kW high efficiency klystron for CEPC

中國科學院為能物現為完備 Institute of High Energy Physics Chinese Academy of Sciences

O. Xiao[#], Z. Zhou, Y. Liu, G. Pei, Z. Lu, Munawar Iqbal, Y. Wang, H. Xiao, F. Li, F. Wang Institute of High Energy Physics, Chinese Academy of Sciences, Beijing 100049, China



Introduction

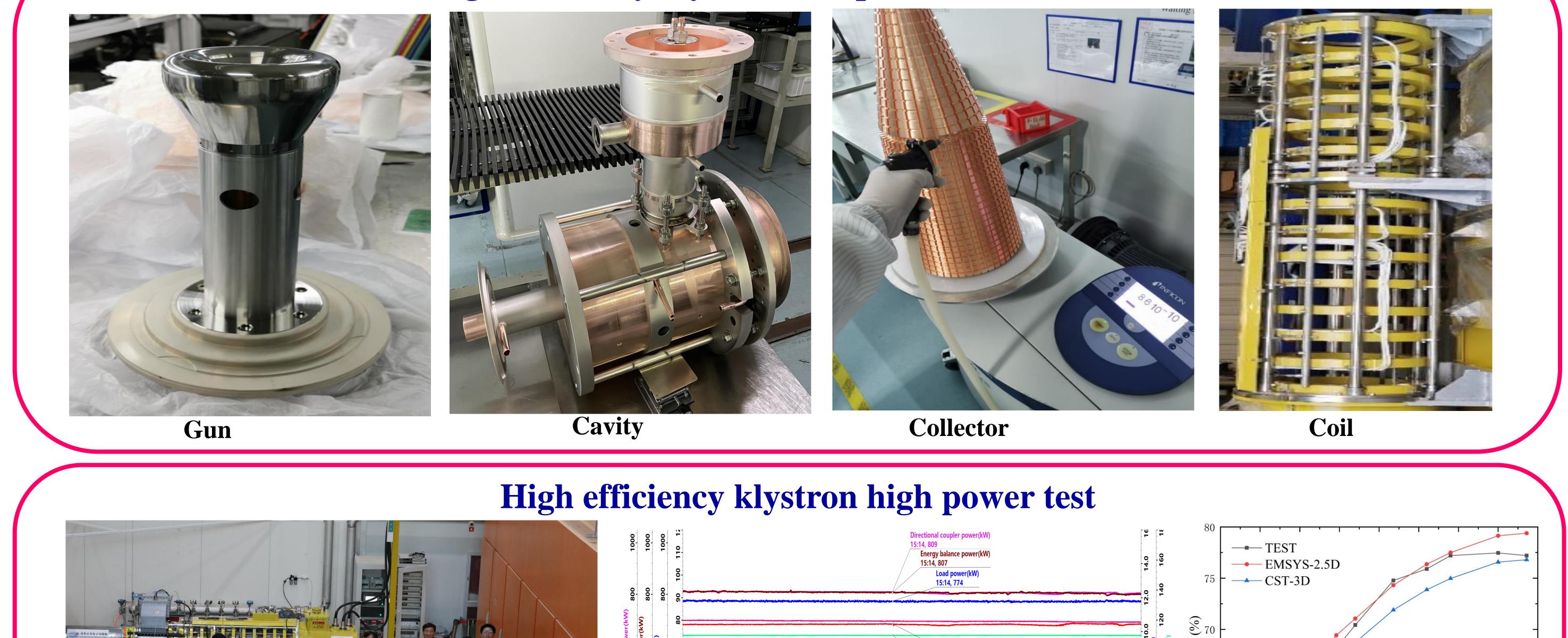
To reduce energy consumption and operating cost for circular electron positron collider (CEPC) in China, the high efficiency klystron are being developed as a priority frontier technology at Institute of High Energy Physics, Chinese Academy of Sciences. A high efficiency 800 kW continuous wave klystron operating at frequency of 650 MHz, using a low perveance (0.25 μ P) and a novel bunching method(CSM), has been successfully developed. The full 3-dimensional particle-in-cell simulation of the whole klystron in CST verified that the klystron efficiency was achieved up to 77% without instability and returning electrons. This prototype klystron has been finished high power acceptance test in August 2024. The test results show that the output power and efficiency have reached 803kW and 78%, respectively.

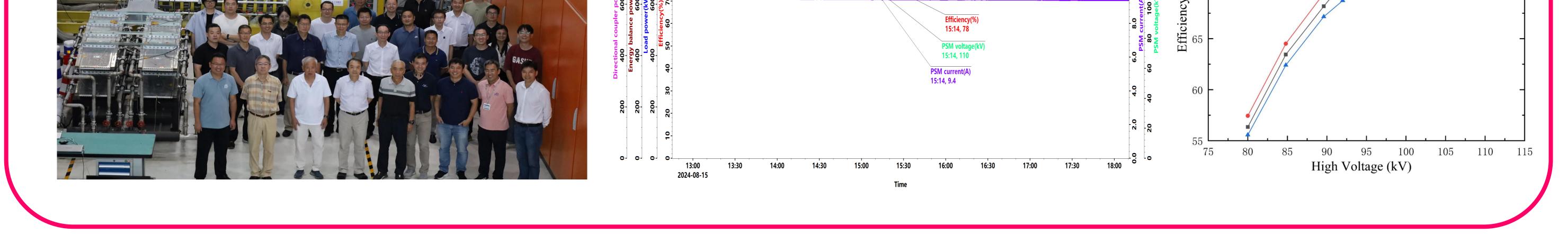


Klystron main design parameters

Parameters	Value	
Operating frequency	650 MHz	
Beam Voltage	113 kV	Time Signals
Beam Current	9.5 A	
Beam Perveance	0.25 μA/V ^{3/2}	
Efficiency at rated Output Power	≥75%	
Saturation Gain	≥43 dB	$\begin{array}{c c c c c c c c c c c c c c c c c c c $
Output power	800 kW	2e+005 1.8e+005 1.6e+005
1 dB Bandwidth	±0.5 MHz	1.4e+005 1.2e+005 1e+005 80000
Brillouin Magnetic Field	116 Gs	
Number of Cavities	7	0 -100 0 200 400 600 800 1000 1200 1400 1600 1800 2000 2 Pction [x] / mm 3D simulation results in CST

High efficiency klystron components manufacture





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