How to Improve Accuracy of Measurement When Using a Scope



Agenda

- Teledyne LeCroy Overview
- Which Specs Can Influent Test Accuracy
- How to Improve Accuracy of Measurement



Teledyne LeCroy Overview



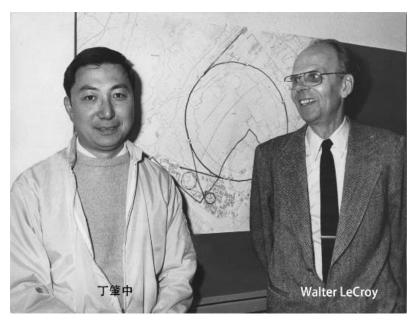
Teledyne LeCroy Overview

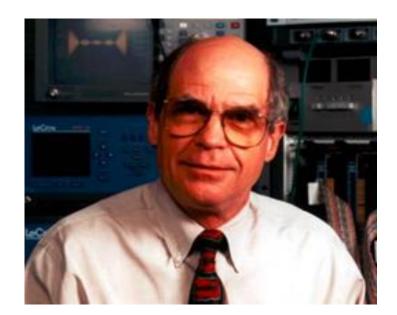


- LeCroy was founded by Walter LeCroy in 1964
 - Original products were high-speed digitizers for particle physics research
- Corporate headquarters is in Chestnut Ridge, NY (USA)
 - Regional sales/service offices in every major location around the world
- Long history of innovation in digital oscilloscopes
 - First digital storage oscilloscope
 - First 100 GHz real-time oscilloscope
 - First 12-bit high bandwidth oscilloscope
 - HD4096 technology 12 bits all the time
- LeCroy is also the world leader in high-speed protocol analysis.
- In 2012, LeCroy was acquired by Teledyne Technologies and renamed Teledyne LeCroy

Founder- Mr. Walter LeCroy







Nobel Prize Owner Sam Ting

Walter LeCroy (1935-2023)

Ting said on LeCroy's 30th anniversary, "The world High Energy Physics community owes a great deal to you through your brilliant instrumentation and innovation. None of our experiments could have been possible without the support and collaboration of your company."



First DSO in the world in 1971

TELEDYNE LECROY Everywhereyoulook[™]

LRS wo 2000 waveform digitiger	FULL SCALE TIME RANGE	 3 inch CRT Record length: 20 samples ADC Resolution: 8 bits Sample Rate: 1 GS/s Sample Resolution: 1 ns 50 ohm inputs 1 V full scale
SCALE ILLUM FOWER	NANDSECONDS MICROSECONDS MILLISECONDS	AND OUTPUT NUAL READY

Continuing Innovation

	Bandwidth	Sample Rate	Channels	ADC
2008	30GHz	40GS/s	4CH	8 bit
2010	↓ 45GHz	↓ 80GHz ┃		
2012	↓ 65GHz ┃	↓ 120GS/s		12 bit
2013	* 100GHz	↓ 240GS/s	80CH	
2023		320GS/s		



Teledyne LeCroy Scope Product Line





Other Instruments

Wave Generator

- 16 bits vertical resolution
- Up to 12.32 GS/s
- Up to 5Vpp with HW offset ± 2.5 V into 50Ω
- Up to 8Ch
- Multifunctional Platform:
 - Arbitrary Function Generator
 - Arbitrary Waveform Generator
 - Digital Pattern Generator



Time Domain Reflection

- Up to 40GHz @ 4ports
- Rise time up to 9.5ps
- Single-end, Differential
- Build in OSLT Calibration





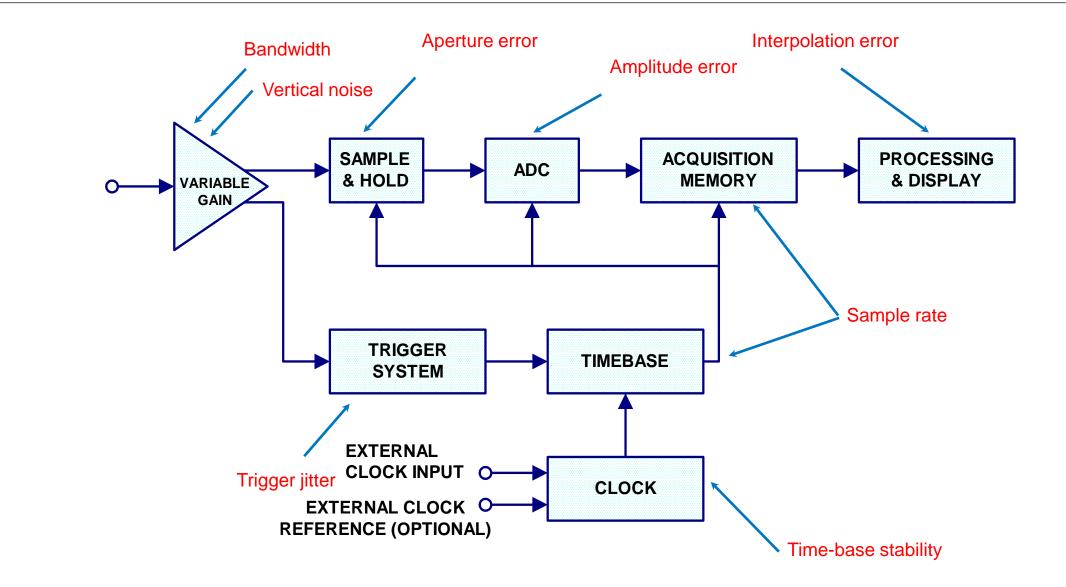


9

Which Factors Can Influent Accuracy



Workflow of Scope

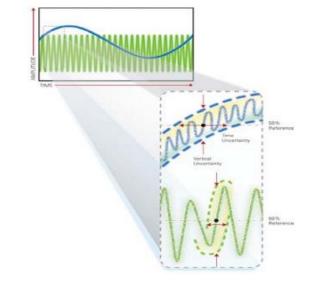




11

Factors influencing the accuracy of horizontal measurements

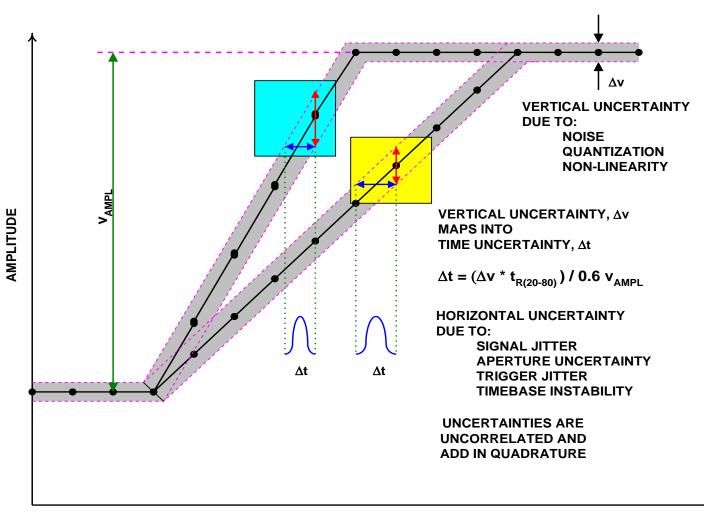
Clock Accuracy	< 1 ppm + (aging of 0.5ppm/yr from last calibration)
Sample Clock Jitter	Up to 10 µs Acquired Time Range: 100 fsrms (Internal Timebase Reference) Up to 6.4 ms Acquired Time Range: 150 fsrms (Internal Timebase Reference)
Delta Time Measurement Accuracy	$\sqrt{2} * \sqrt{\left(\frac{Noise}{SlewRate}\right)^2}$ + (Sample Clock Jitter) ² (RMS) + (clock accuracy * reading) (seconds)
Jitter Measurement Floor	$\sqrt{\left(\frac{Noise}{SlewRate} ight)^2}$ + (Sample Clock Jitter) ² (RMS, seconds, TIE)



The slope would convert vertical noise to horizontal jitter



How the measurement error of the vertical quantity affects the horizontal measurement quantity



Errors in edge amplitude caused by ADC quantization errors, vertical noise and gain errors, channel stacking mismatches, etc., are converted into horizontal jitter.

The intrinsic jitter associated with the amplitude error is calculated as follows:

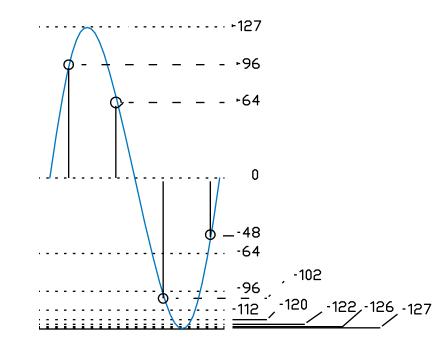
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\label{eq:dvdt} \begin{split} \mathsf{DT} &= \mathsf{e}/(\mathsf{dV}/\mathsf{dT}) = \mathsf{DV} \,^* \mathsf{t}_{\mathsf{R}\, 20\text{-}80} \, / \\ &0.6 \mathsf{V}_{\mathsf{AMPL}} \end{split}
```

For example: For example: Assuming a quantization error of 0.28 LSB (1.7 mV @ 200 mV/div for an 8bit ADC oscilloscope) and a 20%-80% rise time of 2ns for a 1 V signal:

 $DT = 1.7 \text{ mV}^2 \text{ns} / (0.6^* 1 \text{V}) = 5.6 \text{ ps}$



The digital foundation of an oscilloscope



Decimal	Signed Binary
+127	11111111
+1	1000001
0	1000000
-1	01111111
-128	00000000

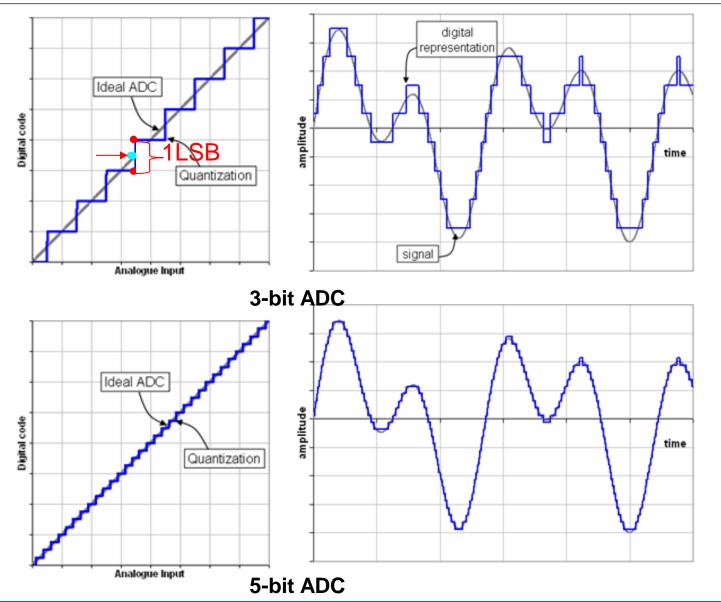
An N-bit precision ADC converts an analog voltage into a binary number of N bits

- <u># Bits</u> resolution
- **8** 256:1
- N 2ⁿ :1

LeCroy uses the binary format of symbols as the digital output, so the binary code at the top of the screen is +127, 0 in the middle of the screen, and -128 at the bottom of the screen.

Binary codes correspond to different voltage values by vertical gain and offset.

Quantification error



In the process of discretization of continuous analog signal into digital signal, because there is no infinite number of discretized digital levels to reassemble the continuous analog signal, there will always be a deviation between the actual analog voltage value and the corresponding digital level value, and this deviation value is called quantization error. The voltages within a certain range of values are represented by the same binary code, so the uncertainty of this quantization is \pm 1/2LSB. LSB can be understood as a step to digitization. The quantization error refers to the difference between the quantization result and the quantized simulation, and obviously the more quantization series, the smaller the relative error of quantization. Quantization series refers to the number of series in which the maximum value is equalized, and the size of each mean is called a quantization unit.

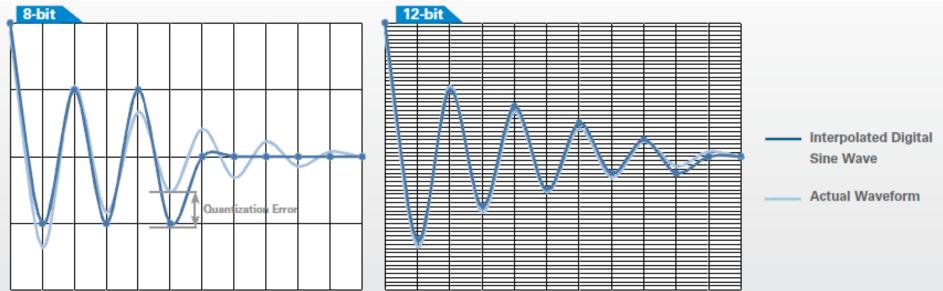
TELEDYNE LECRO

What is the resolution of an oscilloscope?

The quantification level of the ADC = 2 N bits of Resolution

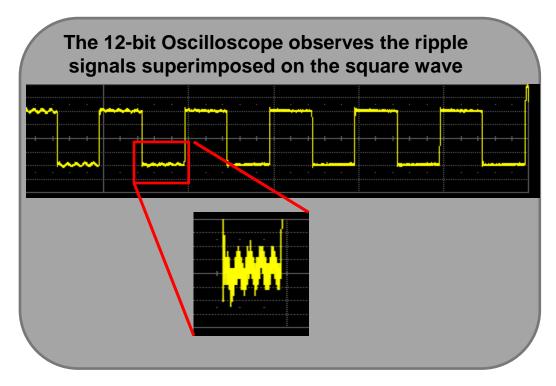
ADC Resolution	Quantification level	Dynamic range
8	256	~48 dB
12	4096	~72 dB

 quantification level—A 12-bit oscilloscope has 16 times more quantification levels than a traditional 8-bit oscilloscope

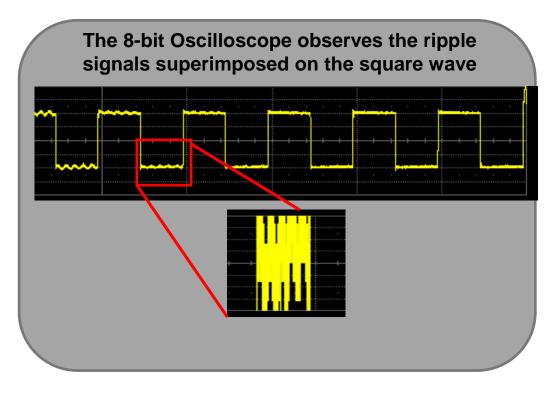


12-bit oscilloscope VS 8-bit oscilloscope

The higher vertical resolution allows for unobstructed waveform detail, while the signal detail of the 8-bit oscilloscope is drowned out in quantization noise



Ripple parameters (frequency, RMS, etc.) can be observed and measured



Can you see ripples?

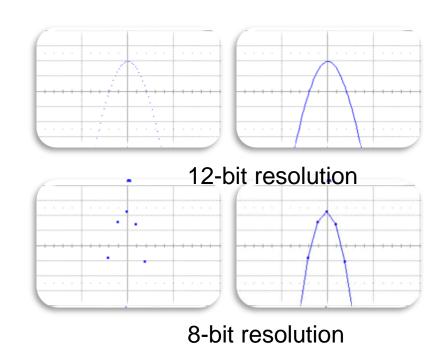
17

Materialize the effect of the number of bits of the ADC on the measurement

A higher number of digits means that smaller voltages can be measured

		Smallest Voltage Step		
	Full Scale	8 bits	12 bits	
	80 V	312.5 mV	19.5 mV	
	40 V	156.2 mV	9.76 mV	
	20 V	78.1 mV	4.88 mV	
1.95 mV=8V/2^12	8 V	31.3 mV	1.95 mV	
31.3 mV=8V/2^8	4 V	15.6 mV	976 µV	
	1.6 V	6.3 mV	390 µV	
	800 mV	3.1 mV	195 µV	
	400 mV	1.56 mV	97.6 µV	
	160 mV	625 µV	39 µV	
	80 mV	313 µV	19.5 µV	
	40 mV	156 µV	9.76 μV	
	16 mV	62.5 µV	3.9 µV	
	8 mV	31.2 µV	1.95 µV	

 The minimum detectable voltage value for an 8V signal measured with a 12-bit ADC is 1.95 mV, while the minimum detectable voltage value for an 8-bit ADC is 31.3 mV.



How to Improve Accuracy of Measurement

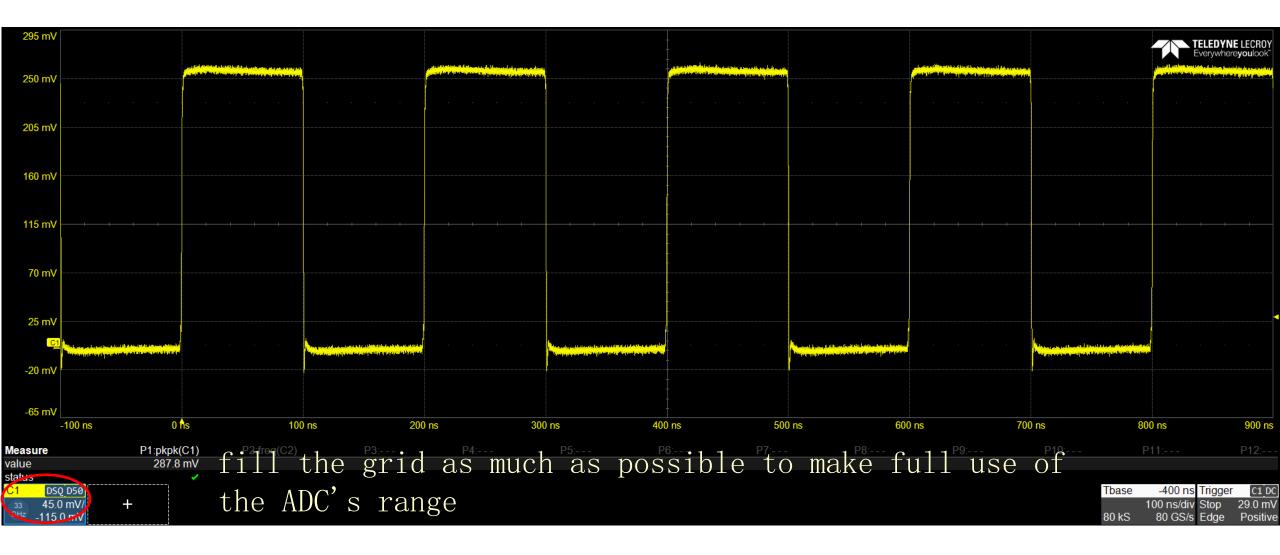


First rule of capturing signals: Minimize quantization errors

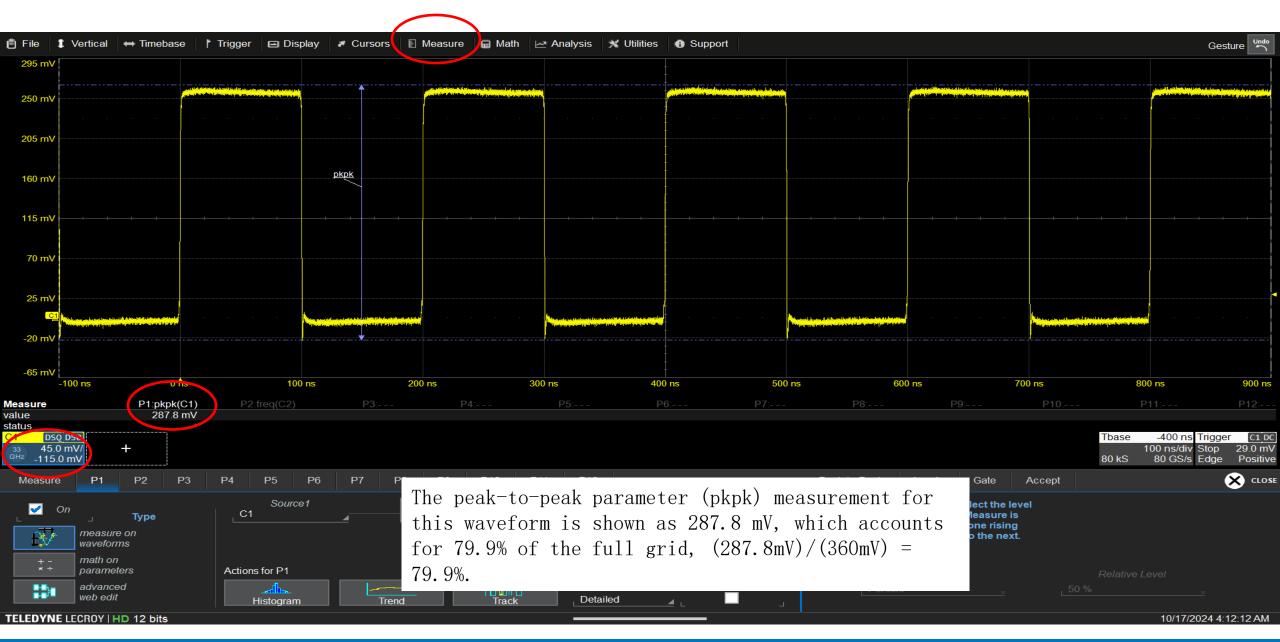
 Try to fill the grid with the waveform to make the most use of the ADC's range.

Let's see an example

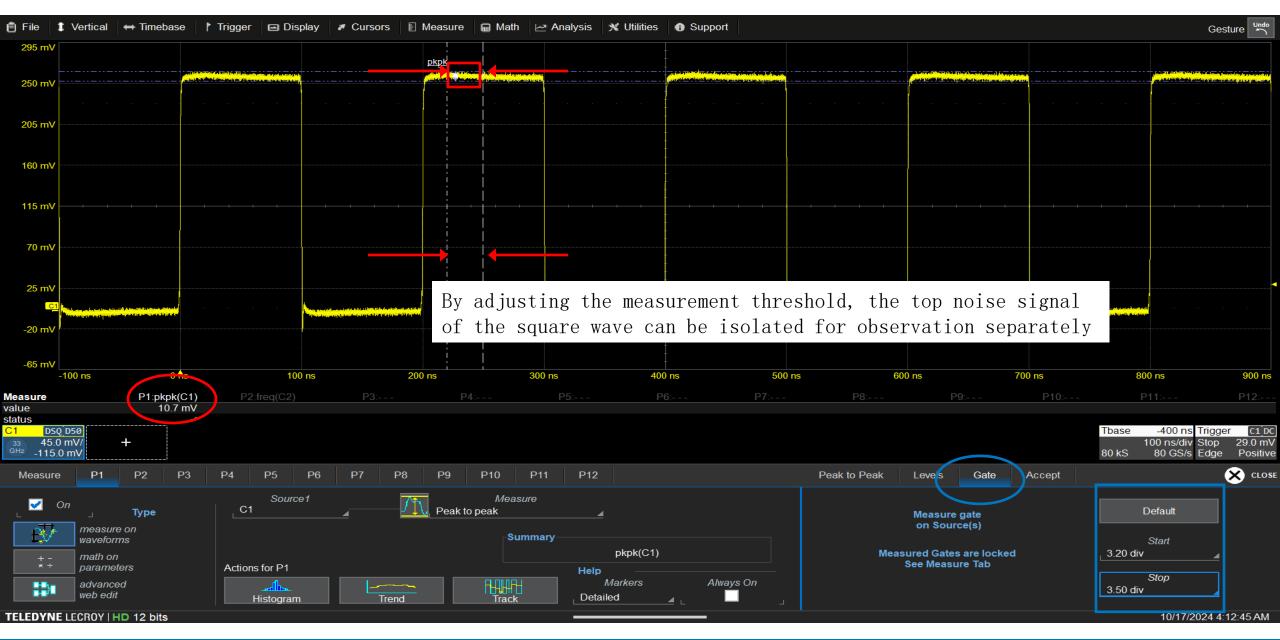




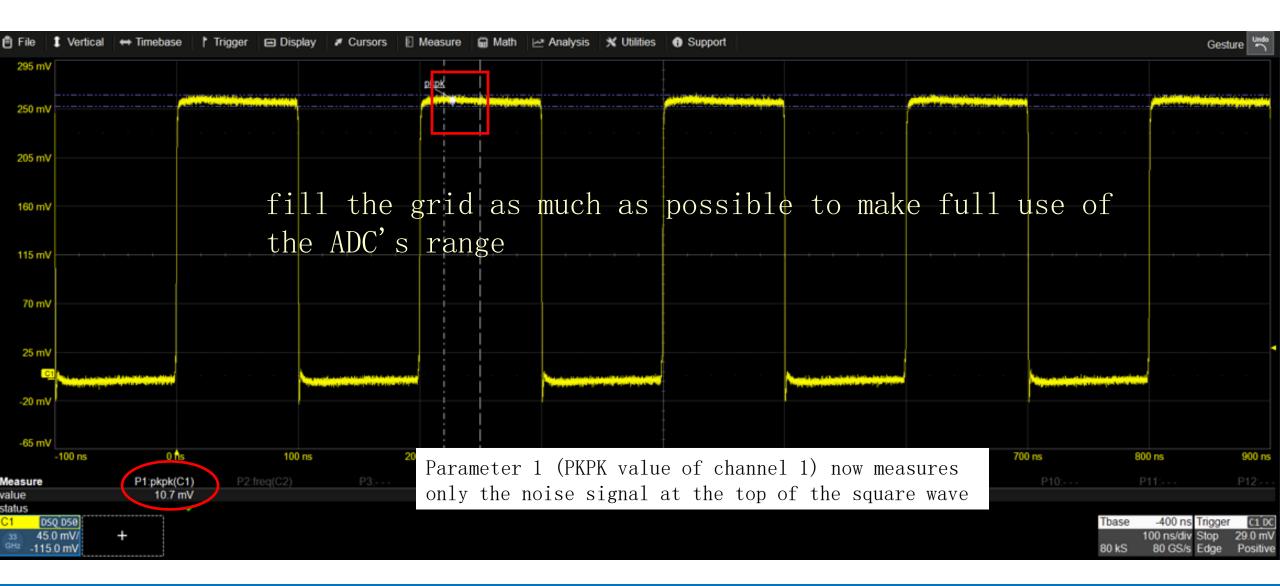




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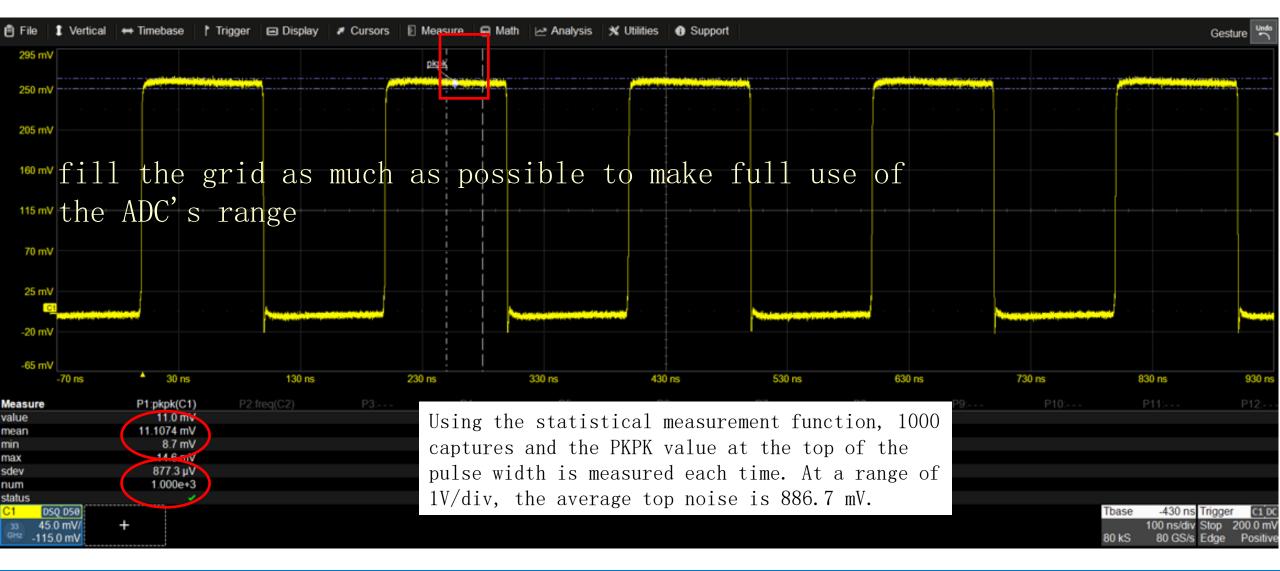




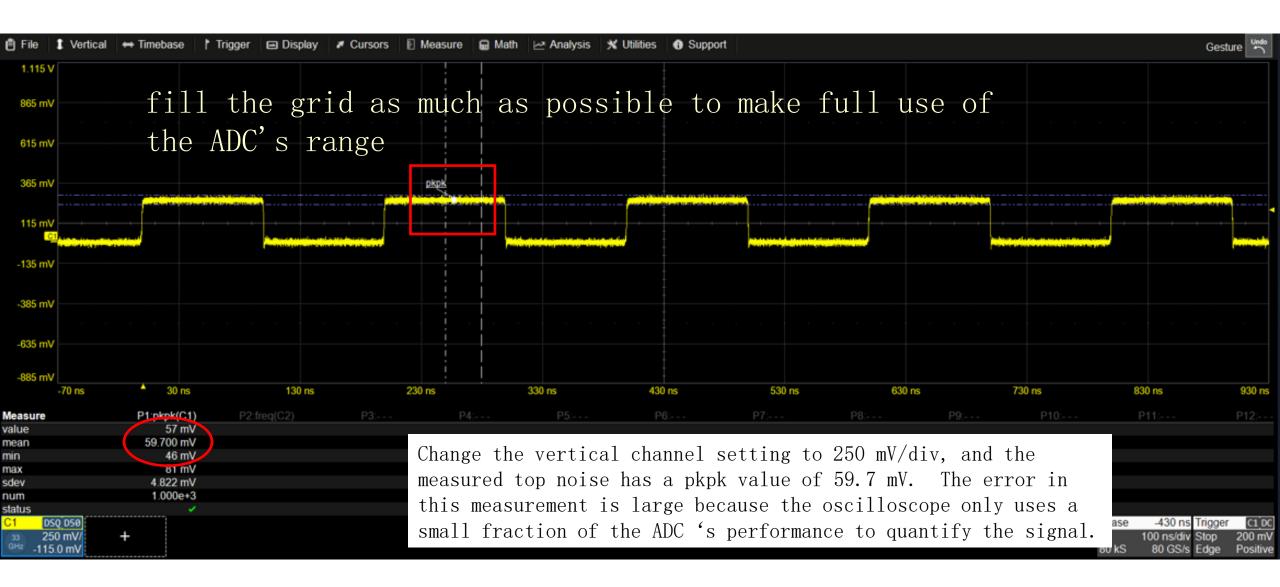


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Everywhereyoulook*



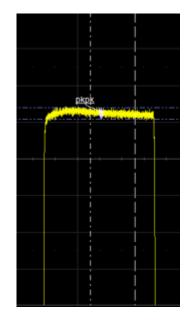
TELEDYNE LECROY Everywhere**you**look[®]

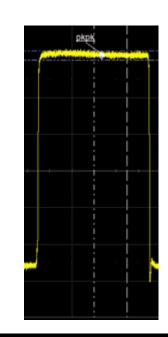


CROY

Everywhere**you**look"

The bigger vertical scale you use, the larger result you will get





leasure

mean

min

max

sdev

num

status

DSQ D50

45.0 mV/

-115.0 mV

P1:pkpk(C1)

11.1074 mV

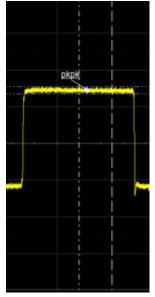
11 0 m

8.7 mV

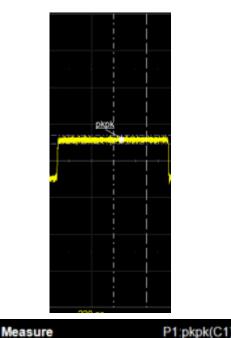
14.6 mV

877.3 µV

1.000e+3



Meas	ure		P1:pkpk	(C1)	Meas
value) mV	value
mean			19.816	δmV	mear
min			16	òmV	min
max			26	3 mV	max
sdev			1.540) mV	sdev
num			1.000)e+3	num
status	;			1	statu
C1	DSQ D50				C1
33 GHz	100 mV/ -116.0 mV	+			33 GHz



value

status

DSQ D50

250 mV/

-115.0 mV

57 m

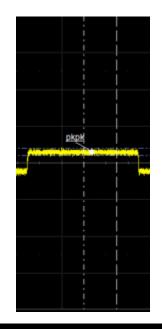
46 mV

81 mV

4.822 mV

1.000e+3

59.700 m

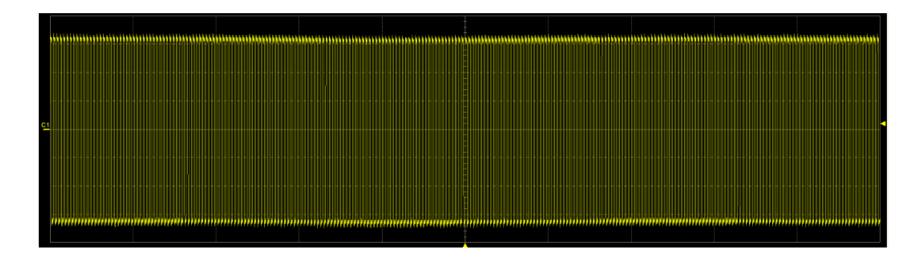


Measure	P1:pkpk(C1)
value	98 mV
mean	106.471 mV
min	84 mV
max	144 mV
sdev	8.306 mV
num	1.003e+3
status	×
C1 DSQ D50	
33 500 mV/ GHz -115.0 mV	+

Measure	P1:pkpk(C1)
value	6.5 mV
mean	6.6959 mV
min	5.4 mV
max	8.5 mV
sdev	484.9 µV
num	1.000e+3
status	× .
C1 DSQ D50	
33 20.0 mV/	+
GHz -227.2 mV	



Second Rule: always be vigilant about the sampling rate



	20GS/s	10GS/s	5GS/s	2 5GS/s
status				×
num	155.127e+3	160.107e+3	153.633e+3	156.870e+3
sdev	7.49 ps	9.82 ps	64.90 ps	< 122.71 ps
max	429 ps	475 ps	641 ps	< 802 ps
min	319 ps	323 ps	356 ps	< 357 ps
mean	349.67 ps	369.67 ps	466.02 ps	< 589.43 ps
value	357 pe	364 ps	386 ps	653 ps
Measure	P1:rise(C1)	P1:rise(C1)	P1:rise(C1)	P1:rise(C1)

Measuring the rise time of the same signal at different sampling rates gets very different results. Therefore, we must always be vigilant about sampling rates.

28

Third Rule: Using the Statistical Function



The oscilloscope captures a single pulse signal at near full scale and measures pulse width, rise time,

P3:fall(C1)

543 ps



Use statistical functions to improve the accuracy of your measurements



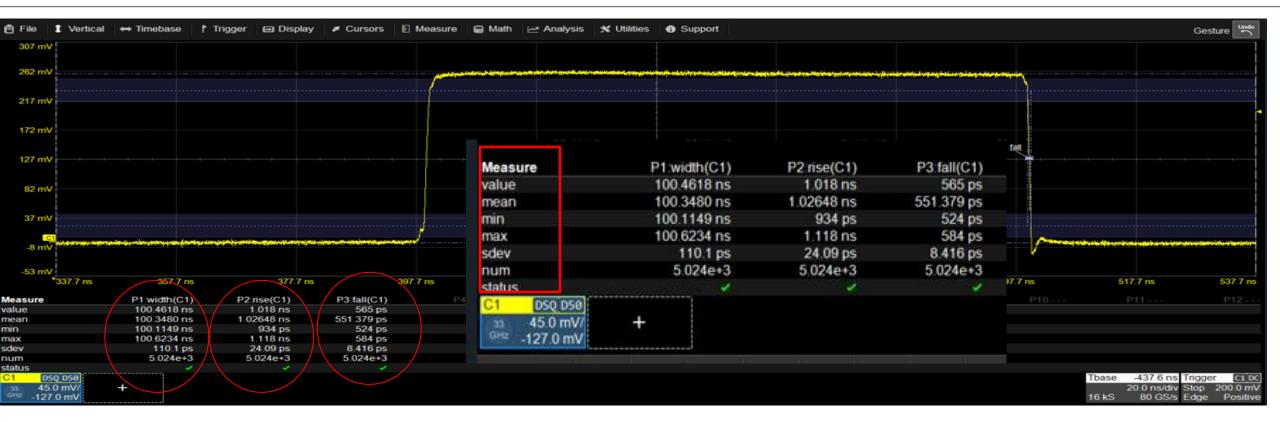
The measurements shown on this image are different from the last one, which one is "correct" ?

In fact, both are correct. The shape of the waveform is unstable, so the measurement results of the captured waveform are different each time.

Measure	P1:width(C1)	P2:rise(C1)	P3:fall(C1)
value	100.5411 ns	1.043 ns	554 ps
status	1	1	× .
C1 DSQ D50 33 45.0 mV/ GHz -127.0 mV	+		



Use statistical functions

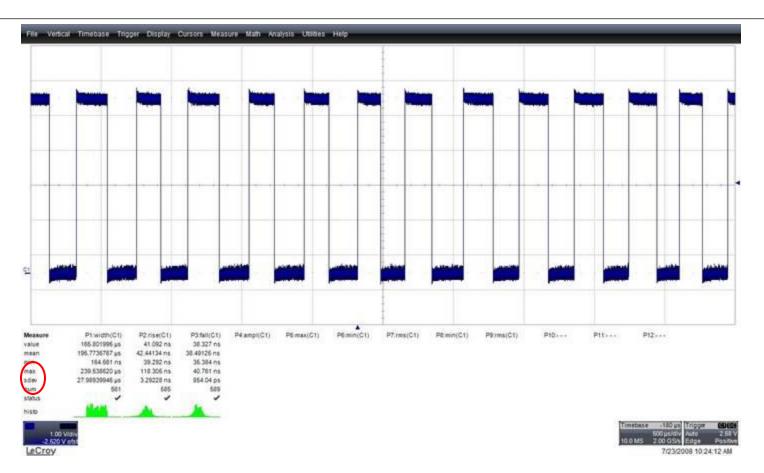


The oscilloscope now shows the parametric statistics. "Num" indicates the number of captures. (approx. 5,000)

With measurement statistics, you can discover the worst case of circuit performance and/or the parameter values of some intermittent signals. This is a good way to determine if a key signal feature is within specification.

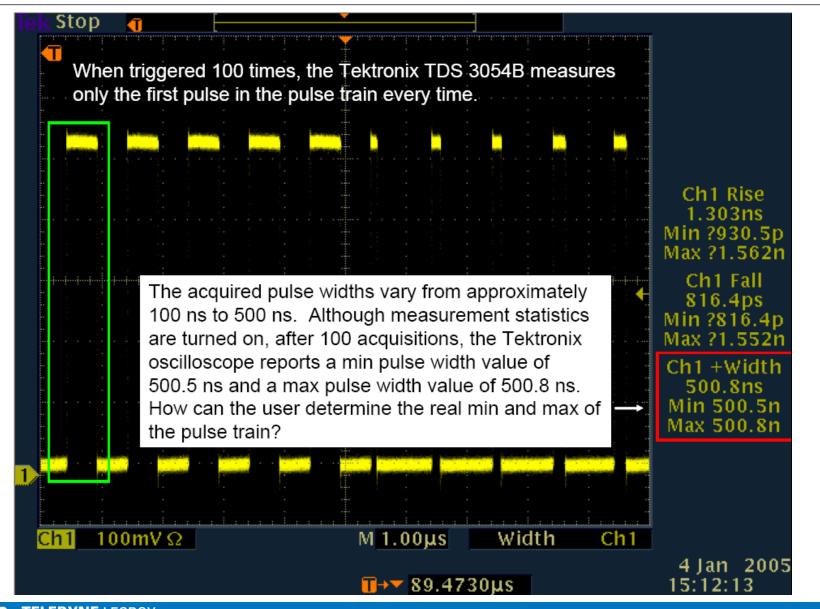


Use statistical functions to improve the accuracy of your measurements



This is a typical signal on industrial control, and although it is slow, this example is applicable to signals at any speed. Comparing the "max" and "min" parameter values with the "mean" values, if there is a rare event, the parameter values will indicate this event. In this example, the pulse width is about 195 ns, but there is a rare pulse width of only 165 ns.

whether the oscilloscope you are using supports true measurement statistics



In this image, you can see that the pulse width is noticeably different, but the maximum and minimum values are almost the same. This is a pseudo-statistic, which simply measures the leftmost pulse duration parameter on the screen. Be wary of this feature when using such an oscilloscope for statistics.

Everywhere**you**look"

Improve measurement accuracy with averaging and filtering



Observe small signals with noise



The image above is a single captured signal. The image below shows the signal observed using the signal averaging function, which clearly reveals the true characteristics of the signal under the noise.

When making such measurements, the waveform should be triggered stably, and the storage depth and sampling rate should be sufficient.

Signal analysis:Improve signal-to-noise ratio

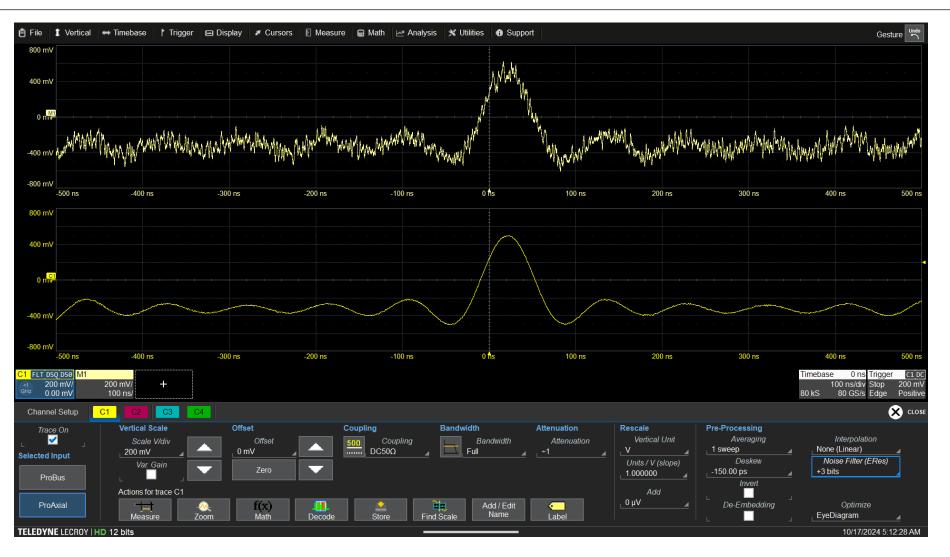


For periodically repeatable signals, the noise of the signal can be reduced by using the method of multiple averaging after stable triggering. The averaging algorithm also reduces the noise introduced by the oscilloscope's analog front end and probes. You can see more detail and measure the signal characteristics more precisely.

1000 measurements averaged



Signal analysis:Improve signal-to-noise ratio



The FIR filter can also reduce noise, but it does not need to be averaged by multiple captures, so it can be used in cases where non-periodic repetitive waveforms cannot be stably triggered. LeCroy oscilloscopes offer a choice of high-precision modes that can simultaneously display a single captured waveform and a high-precision filtered waveform.

Single capture Reduce noise with 3 bit ERES



Summary: How to Improve Accuracy of Measurement?

- Minimize quantization error: Maximize the waveform to fill the grid
- Always be vigilant about sampling rates: oversample, not under sample
- Take advantage of the measurement statistics function of the parameters
- Signal averaging (no need to measure the noise of the signal and the waveform is stable)
- Utilizes Els's Phil filter (no measurement noise required)
- Use High-precision oscilloscopes that use more digits of ADCs and lower noise analog front-ends

