

MPD ITS Status and Perspectives

Yuri Murin on behalf of the NICA MPD ITS Consortium

*CEPC Workshop,
Hangzhou October 22-27, 2024*



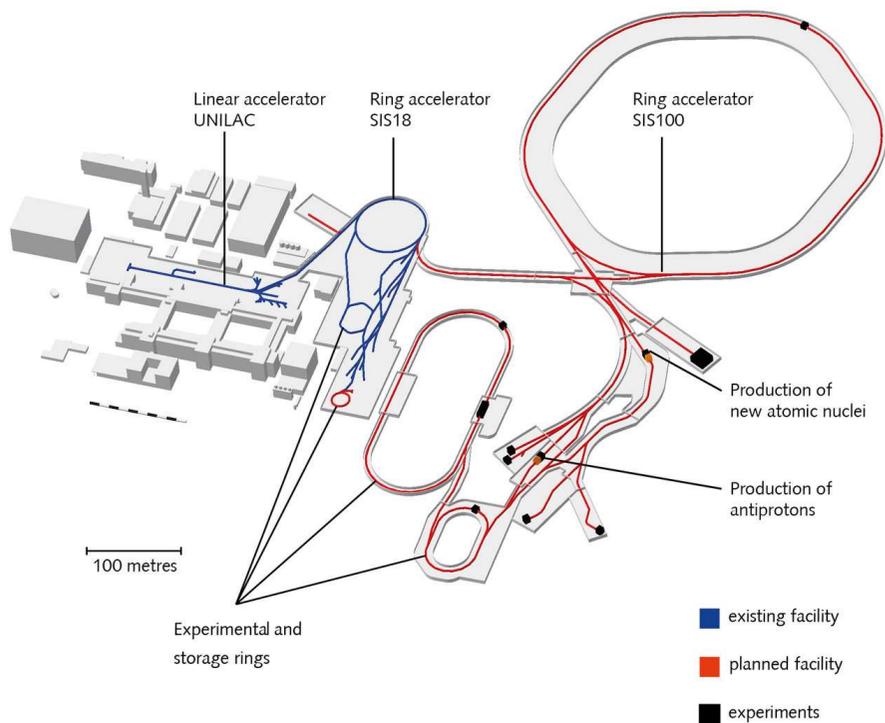


Outline

MPD - ITS



- ❑ Merge of Nuclear and Particle Experimental Physics at FAIR, NICA and HIAF
- ❑ Impact of the “chip war” on possibility for further upgrade of the Eastern Facilities
- ❑ The MPD ITS project status and perspectives
- ❑ Summary and Plan



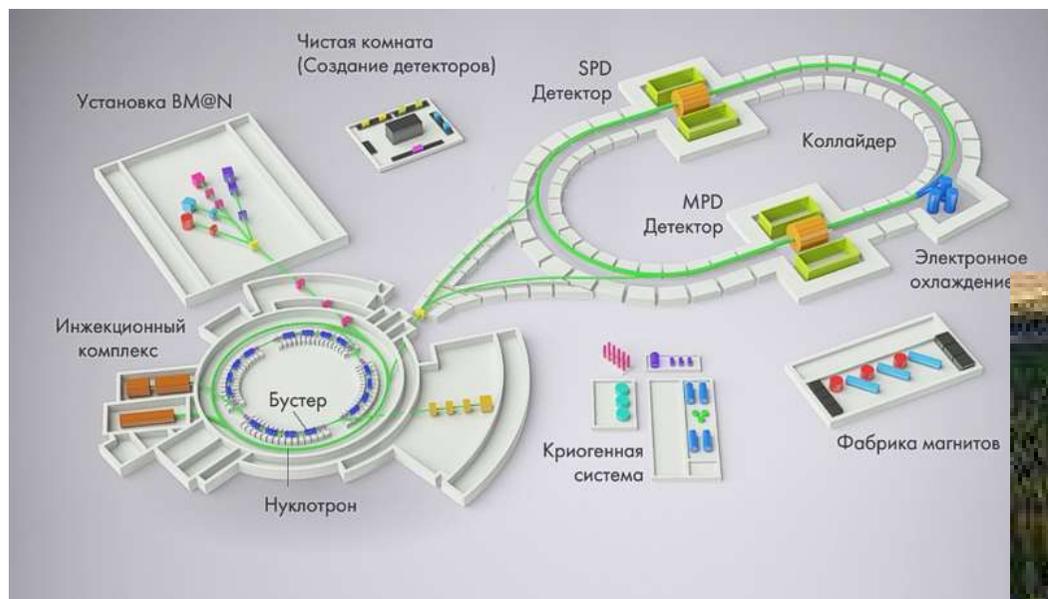
FAIR start in 2014

First beam in 2028



➤ Challenges

The goal of the FAIR (SIS100) is to achieve intense pulsed (5×10^{11} ions per pulse) U^{28+} beams at 1 GeV/u and intense pulsed (4×10^{13}) proton beams at 29 GeV with 1GHz frequency



NICA start in 2013

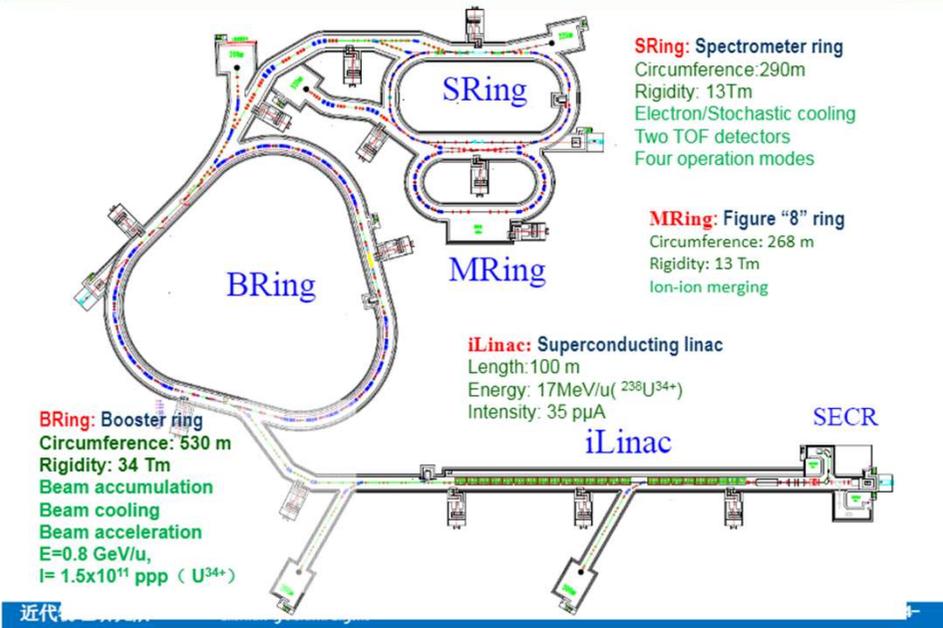
First beam in 2025



➤ The NICA challenge for MPD physics

$\sqrt{s_{NN}}$ 3 ÷ 11.5 GeV Bi+Bi collisions $L=10^{27} \text{cm}^{-2}\text{sec}^{-1}$

HIAF Layout ----Phase I



Start in December 23, 2018

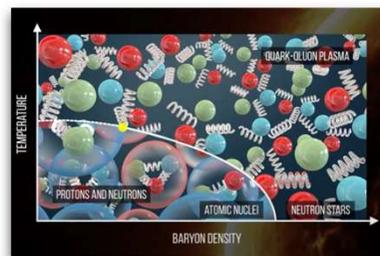
Expected beam in 2025



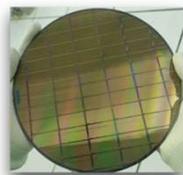
➤ **Operation modes 800A MeV U for different research fields**

- Radioactive ion beams
- Plasma physics
- Ion-ion merging and collision (QED and QCD)
- Atomic physics and applications

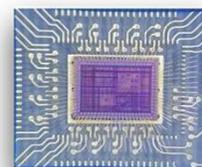
- ✓ A well-defined physics case(s)



- ✓ State-of-the-art sensors Cutting-edge readout electronics



+



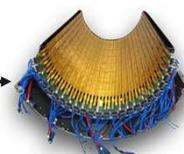
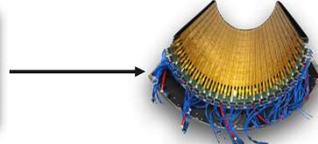
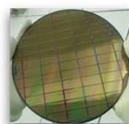
- ✓ Produce the expected detector in-Time & in-Budget:

- Distributed assembly centers



- Automatic management system for control and standardization

- QA procedures at all assembly steps

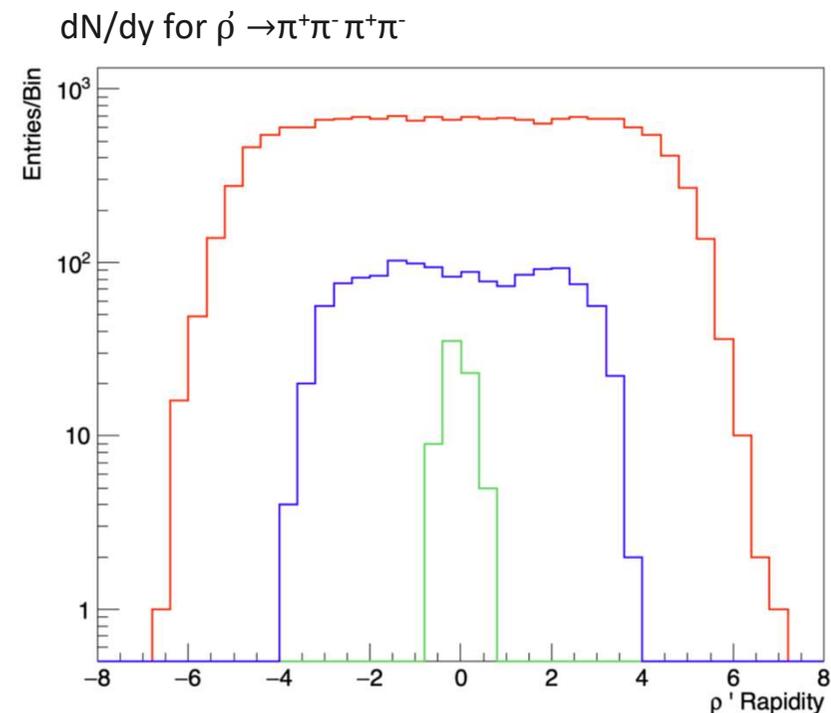
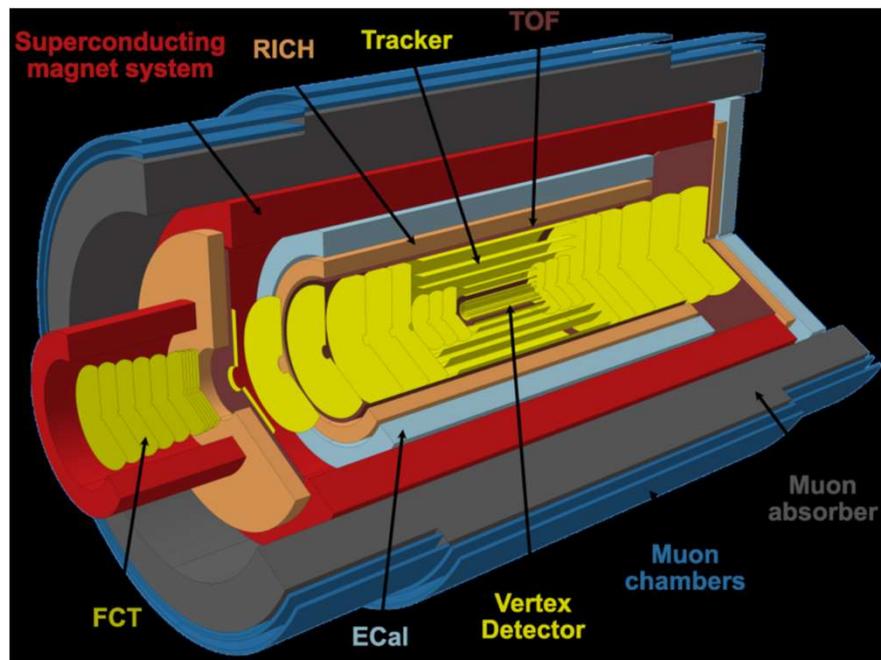


Activity Name	Activity Type	Start Date	End Date	Location	Activity Status	Activity Result
...	...	2012-01-01
...	...	2012-01-01	2012-01-01
...	...	2012-01-01

<https://arxiv.org/abs/2211.02491>

The Dawn of the “Silicon Age” at CERN by 2030 ?

“Cutting-edge” sensors are **Monolithic Active Pixel Sensors (MAPS)** and **Low Gain Avalanche Diodes (LGAD)**

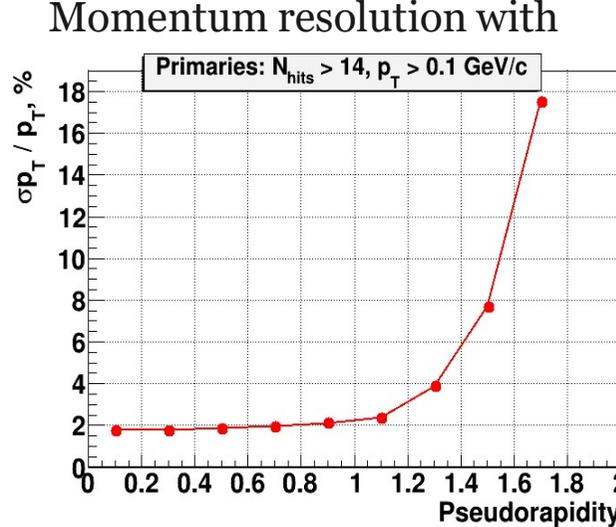
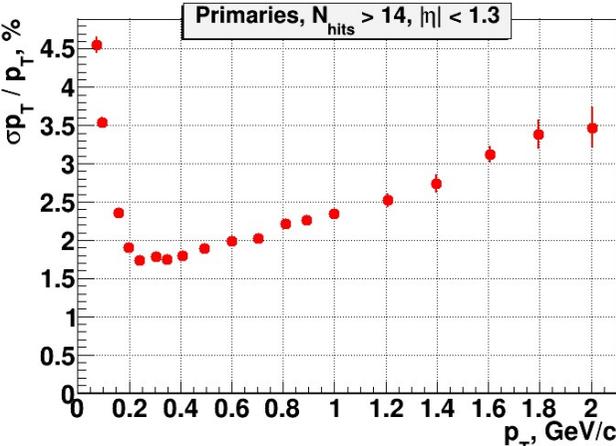


ALICE3 Conceptual Design based on MAPS and LGAD usage for 4D tracking

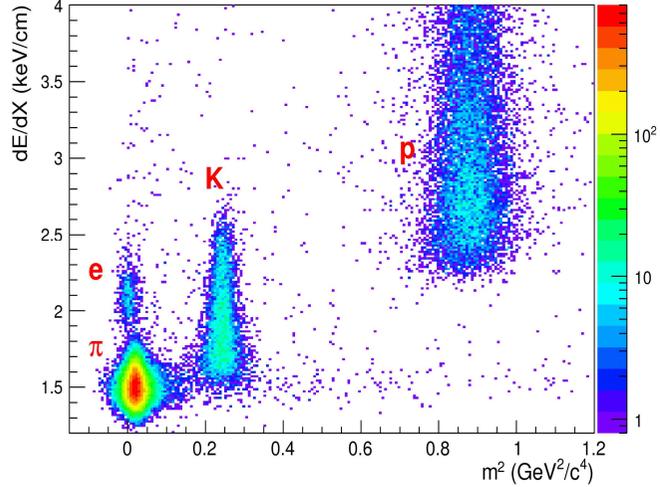
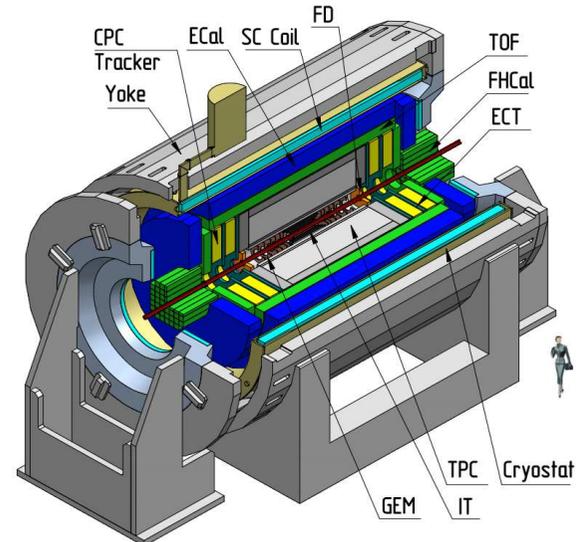
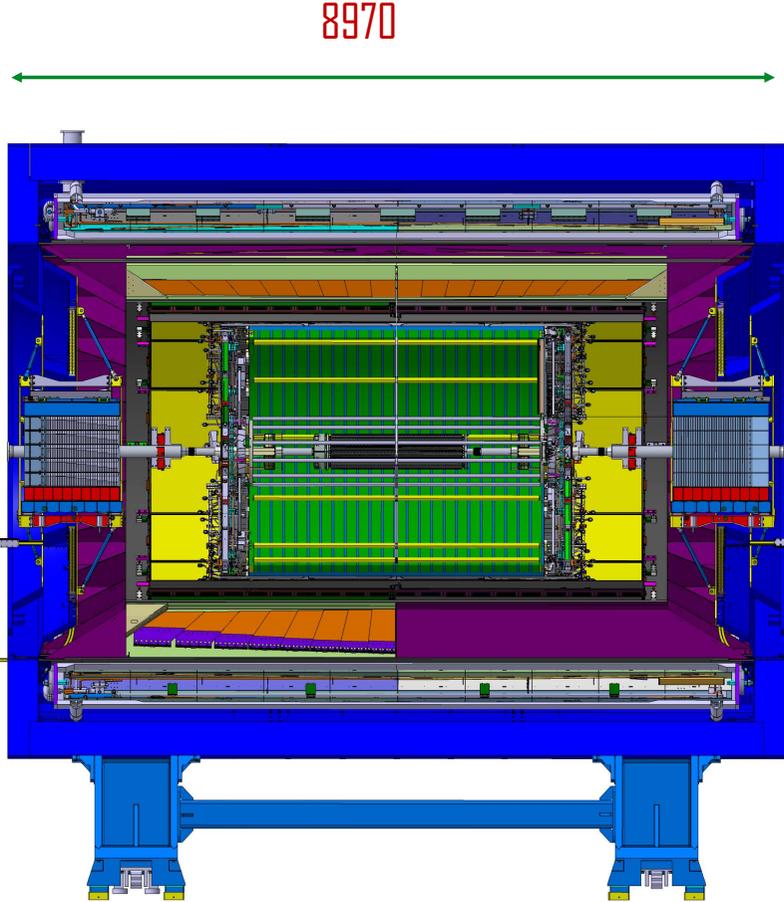
Letter of intent for ALICE 3: A next-generation heavy-ion experiment at the LHC, 4 Nov 2022

MPD - stage 1 by 2025

MPD - stage 2 by 2027 (ITS-OB)-2029 (ITS complete)



Momentum resolution in the forward direction

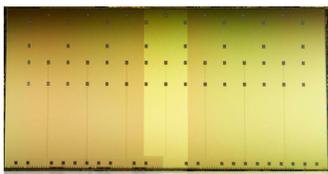


Particle identification capability

The MAPS chip - ALPIDE

- » High-resistivity ($> 1\text{k}\Omega\text{ cm}$) p-type epitaxial layer ($20\mu\text{m} - 40\mu\text{m}$ thick) on p-type substrate.
- » Small n-well diode ($2-3\ \mu\text{m}$ diameter), ~ 100 times smaller than pixel \Rightarrow low capacitance.
- » Deep PWELL shields NWELL of PMOS transistors, allowing for full CMOS circuitry within active area.
- » Global shutter readout pixels' matrix

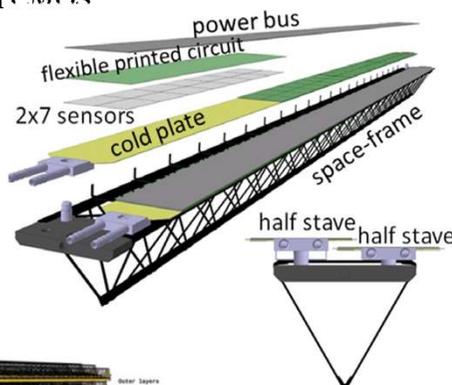
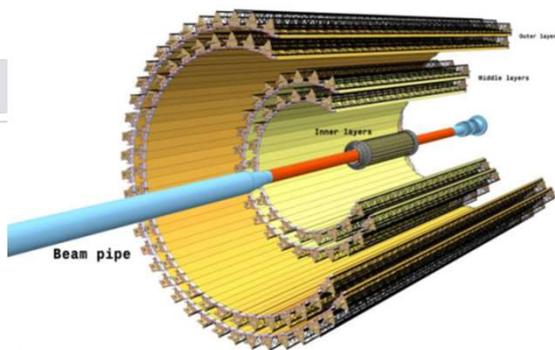
512 x 1024 pixels



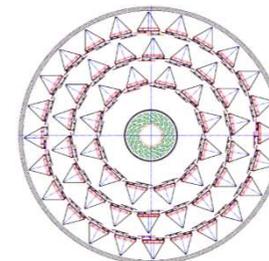
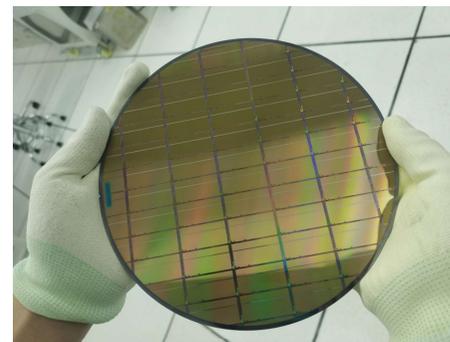
Sensor architecture

Size: $15\text{mm} \times 30\text{mm}$
 Pixel pitch: $28\mu\text{m} \times 28\mu\text{m}$
 Event time resolution: $< 2\mu\text{s}$
 Power consumption: $39\text{mW}/\text{cm}^2$
 Dead area $1.1\text{mm} \times 30\text{mm}$

- 24120 pixel sensors
 - $\sim 12,5$ Gpixels
 - 10 m^2 active area



The MAPS chip - MICA

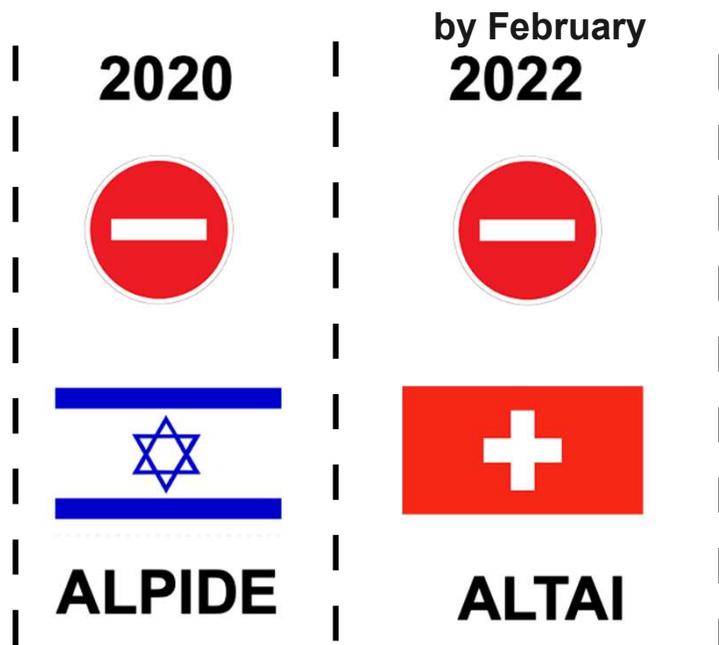


Number of staves
 - IB = $(12+16+20) \times 2$
 OB = $(12+18+24)$

- 11556 pixel sensors
 - $\sim 5,8$ Gpixels
 - 5 m^2 active area

By 2021 we had been fighting for a year for receiving the already paid ALPIDE MAPS (~ 1.8 MCHF).

CERN agreed to create a non radiation-hard version: the ALTAI.



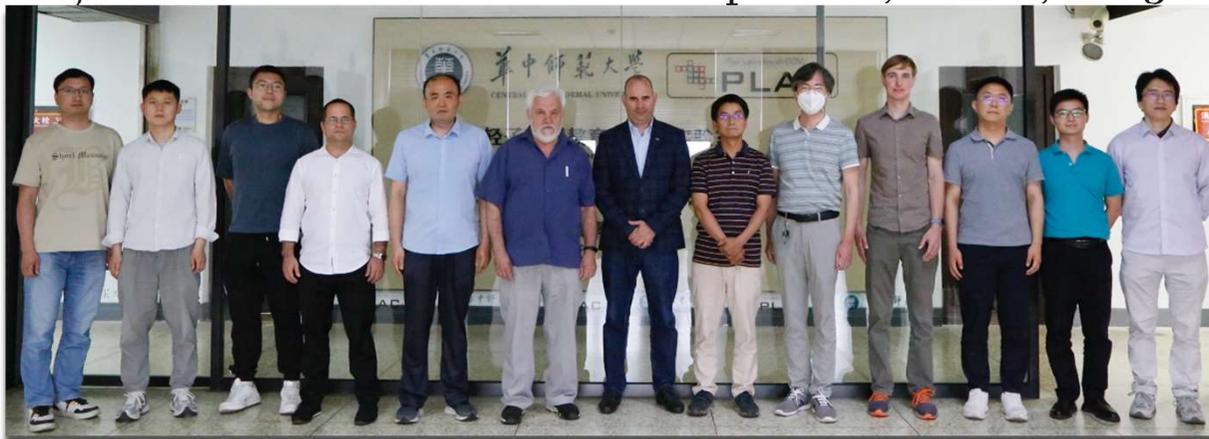
We fought for another year trying to get the ALTAI chips...and failed

Highly prioritized tasks:

- Strengthen the international cooperation (Specially with China).
- Solve the microelectronic limitations (due to sanctions).
- Finish the mechanics on time for the commissioning of the MPD.

The long-term sustainable proposal

NICA-MPD/ITS Seminar on China-Russia Cooperation, Wuhan, 2023.06.15-16



Participants: JINR, CCNU, USTC, IHEP and IMP.

It was agreed: A joint development and construction of Monolithic Active Pixel Sensors (**MAPS**) for fundamental and applied science experiments **including front-end electronics** to make this technology **freely accessible** to China and Russia.

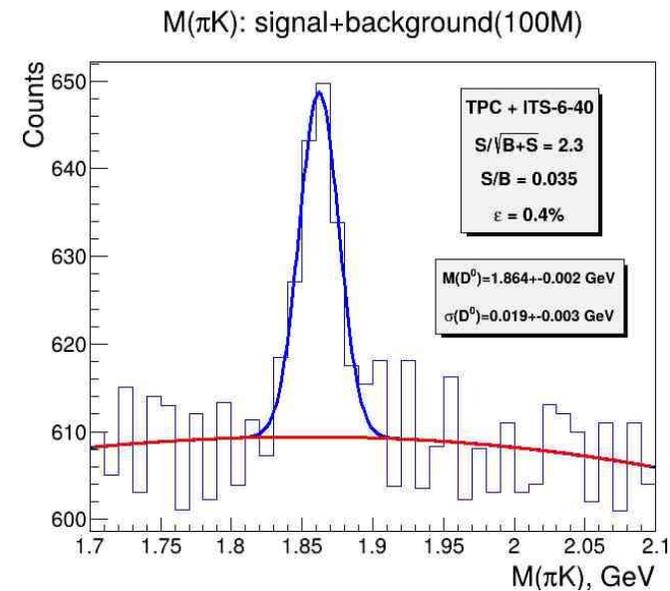
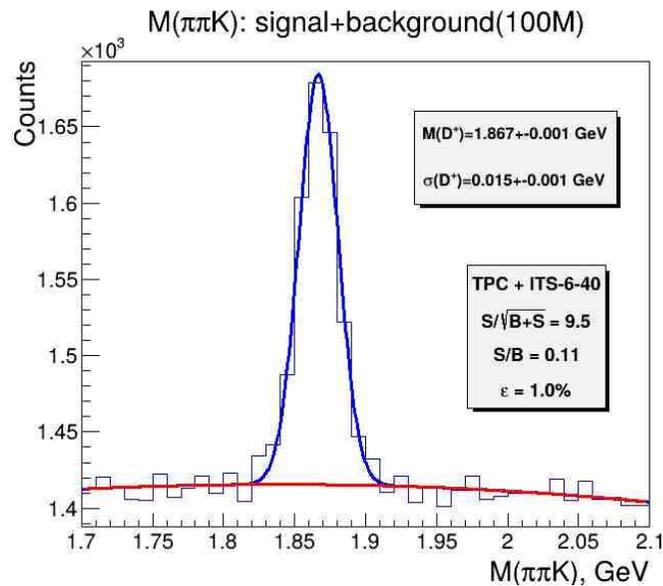
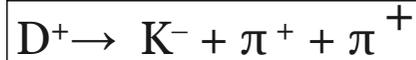
Yu. A. Murin, C. Ceballos Sanchez for the MPD-ITS Collaboration, "*Modern Microelectronics for MPD-ITS. Monolithic Active Pixel Sensors and Readout System*", accepted for publication in the 4th issue of Phys. Part. and Nucl. in 2024

2023



MICA

D⁺ and D⁰ reconstruction using KF with TPC-TOF PID



$N_D = 19\,000$ mesons/month for D^+

$N_D = 3\,200$ mesons/month for D^0

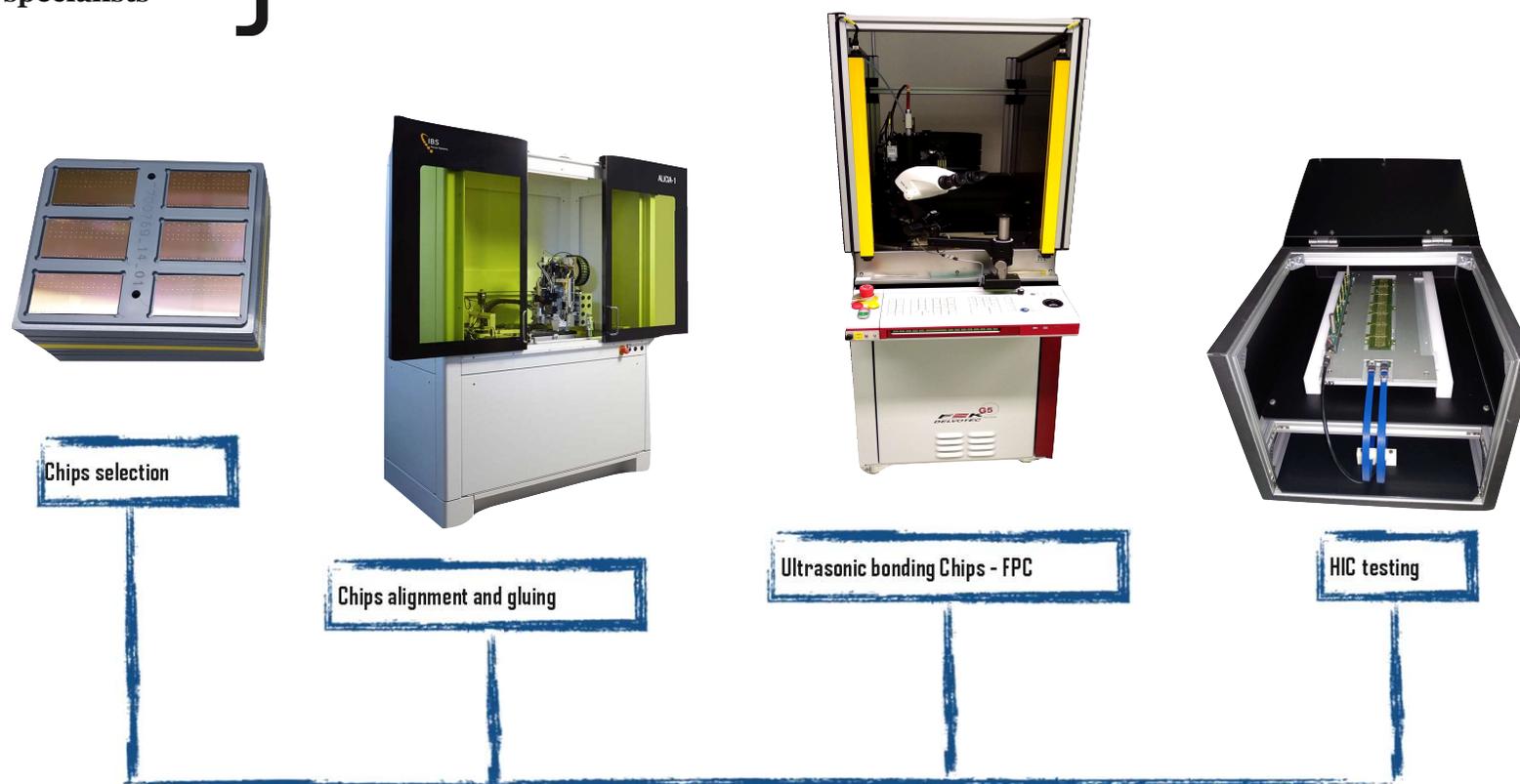
Using the optimal BDT cut allows to reconstruct D^+ and D^0 with an efficiency of **1.0%** and **0.4%** respectively.

Particle	D^+	D^0
Efficiency, %	1.0	0.4
Significance	9.5	2.3
S/B(2σ) ratio	0.11	0.035

Full technological transfer from ALICE to MPD

- Complete KnowHow
- Detector assembly and testing hardware/software
- Supervision and support from ALICE specialists

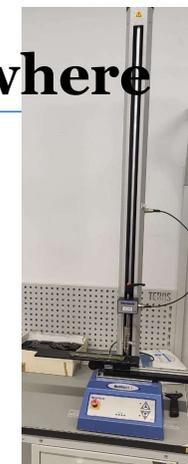
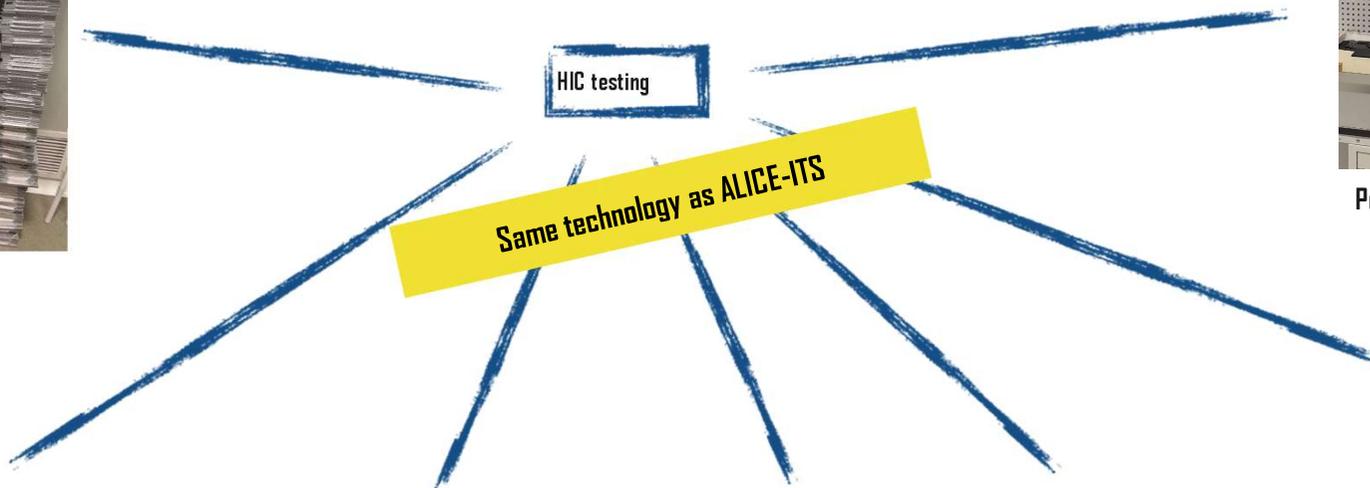
Setup at JINR of the full detector assembly line from chips to detector layers



Full technological transfer from ALICE to MPD



Carrier Plates



Peel test station



Qualification and Endurance test boxes



MOSAIC boards



Power boards

(*) Power Boards BoB to be produced

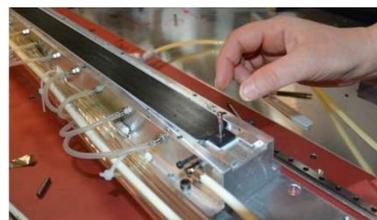


Pull test station



Visual inspection Station

Full technological transfer from ALICE to MPD



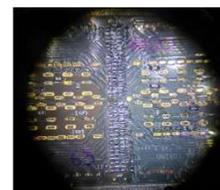
Cold Plate positioning



Glue deposition



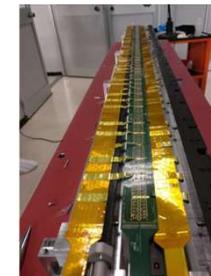
HIC positioning



HIC to HIC interconnection



Space frame on CP



Power Bus position & folding



Cold plate

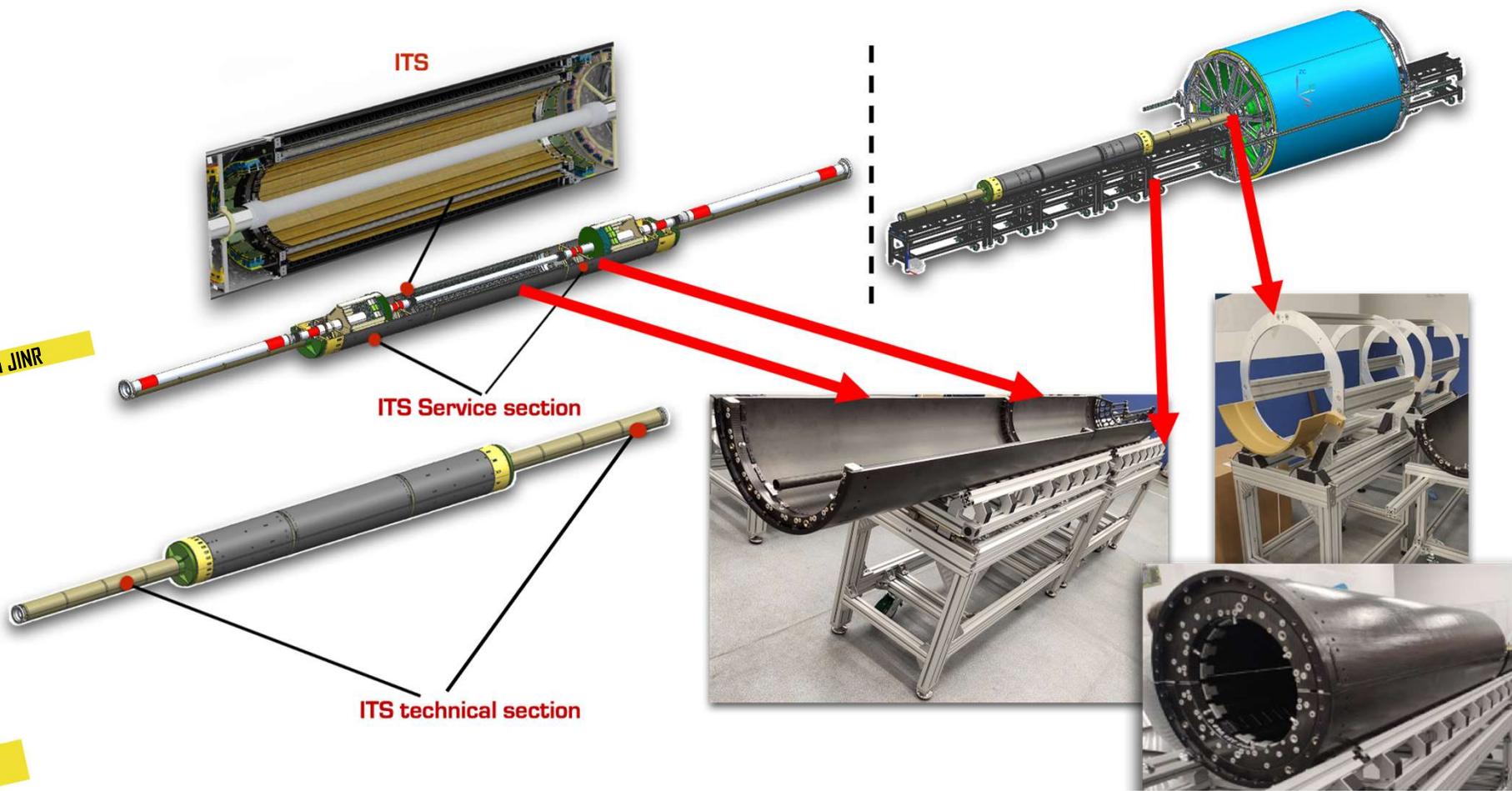


Made in JINR

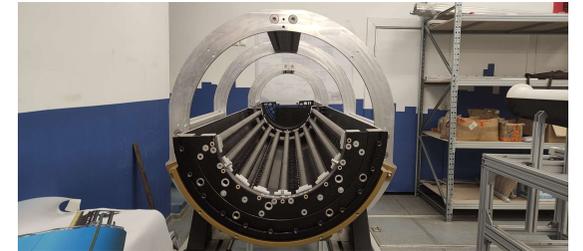
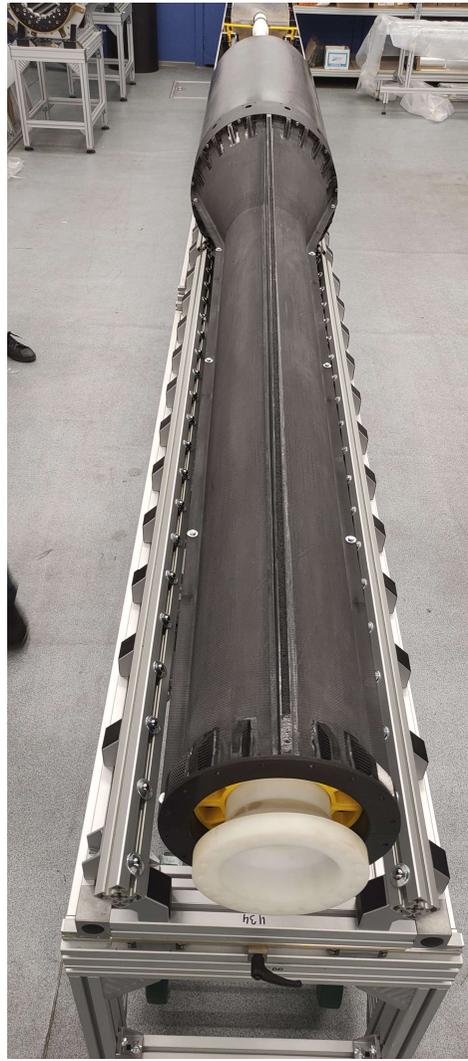
CF trusses



Made in SPbSU



Current status: 95% IC readiness for dry tests of the integration scenario



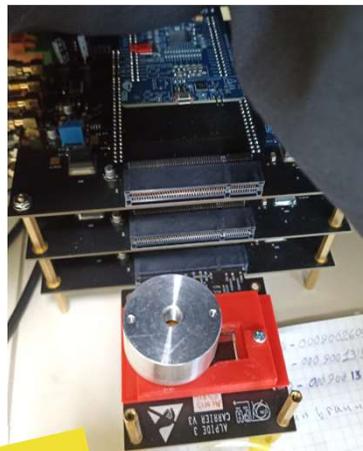
- Designed & produced in the house by D.Andreev +3
- GrafitPro (Moscow) cage manufacturing

Preparation for sensor bench & in-beam tests

CERN-Equivalent DAQ boards and MAPS carrier-plates
Made in JINR



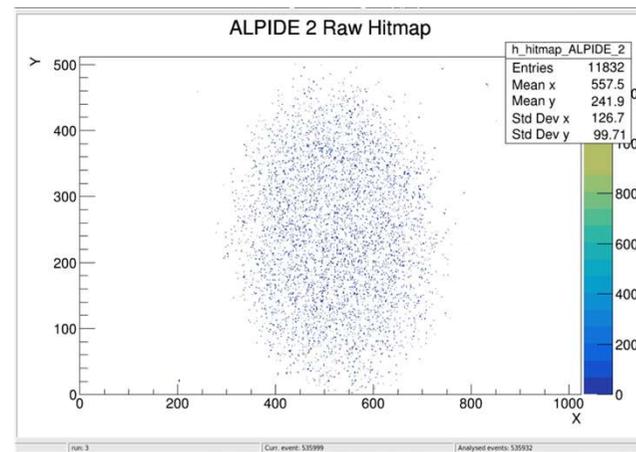
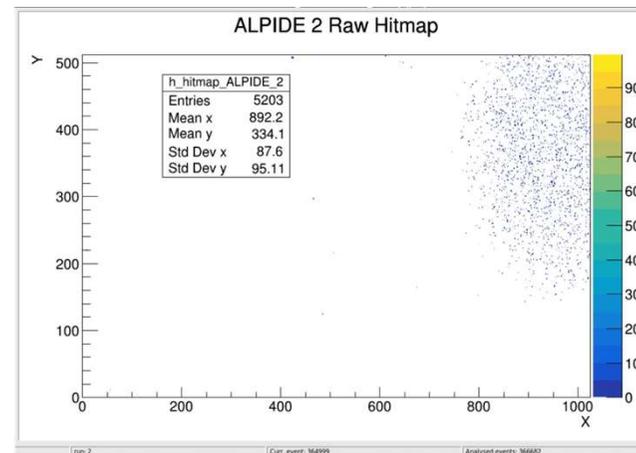
Electronics



MAPS courtesy of SPbSU

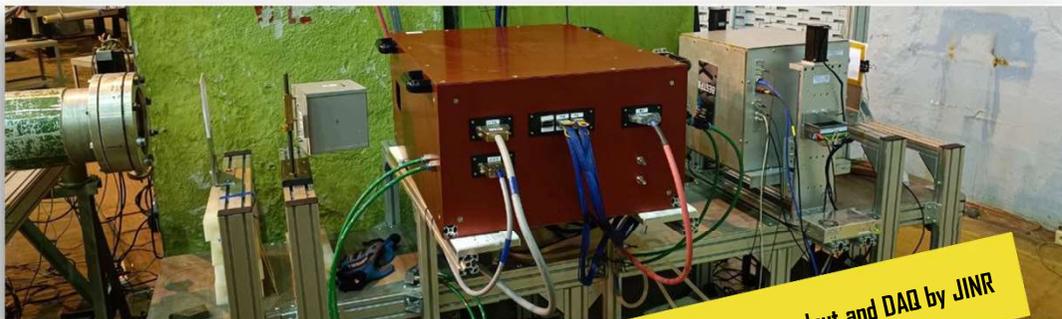
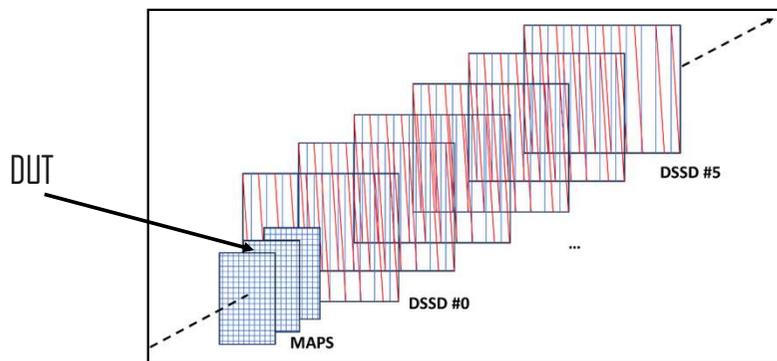


^{55}Fe source with Aluminum collimator

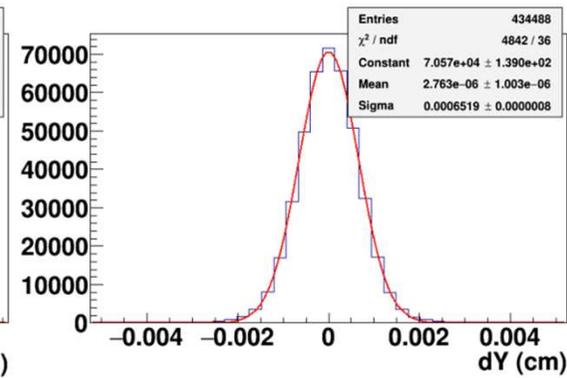
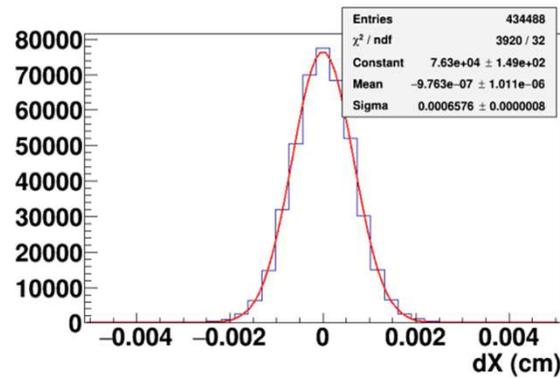


Tests with 1 GeV proton beam in Gatchina

Residuals

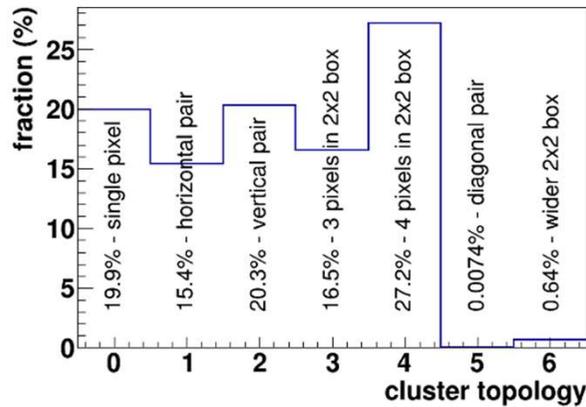


MAPS courtesy of SPbSU readout and DAQ by JINR

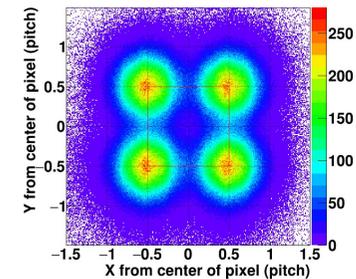
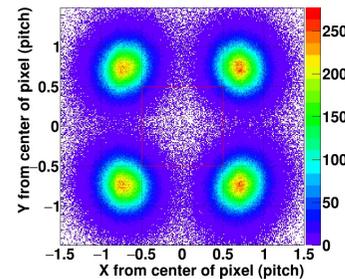
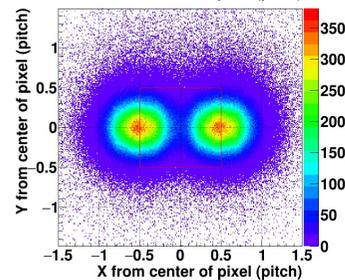
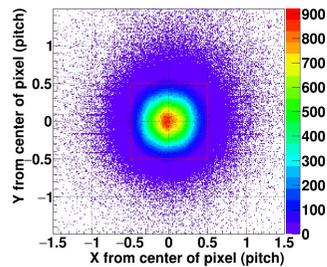
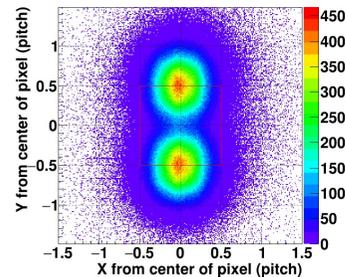


Residual X/Y = 6.58 μm / 6.52 μm ;
 Spatial resolution X/Y = $4.1 \pm 0.4 \mu\text{m}$ / $4.06 \pm 0.4 \mu\text{m}$;
 Efficiency > 99 %

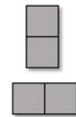




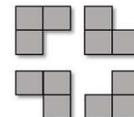
Cluster Topology



1-Pixel Clusters



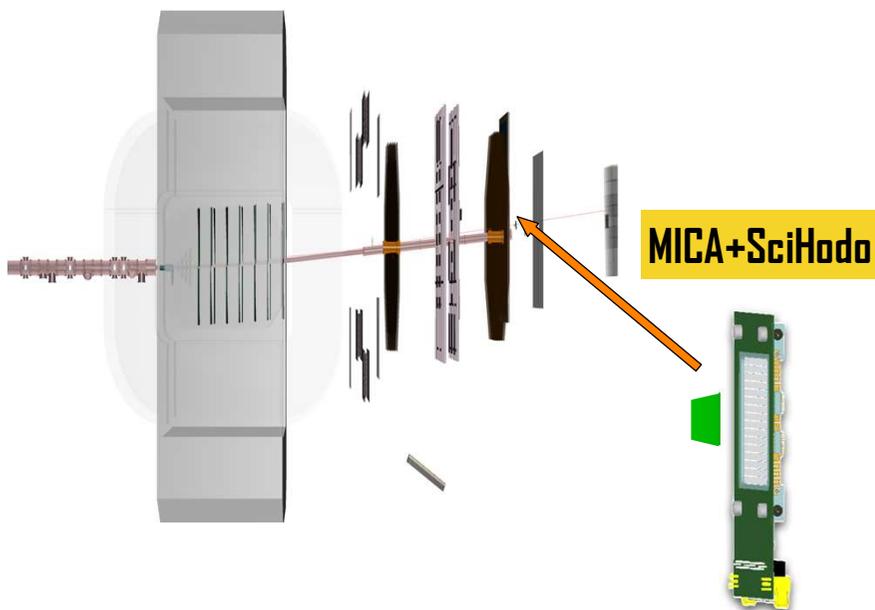
2-Pixel Clusters



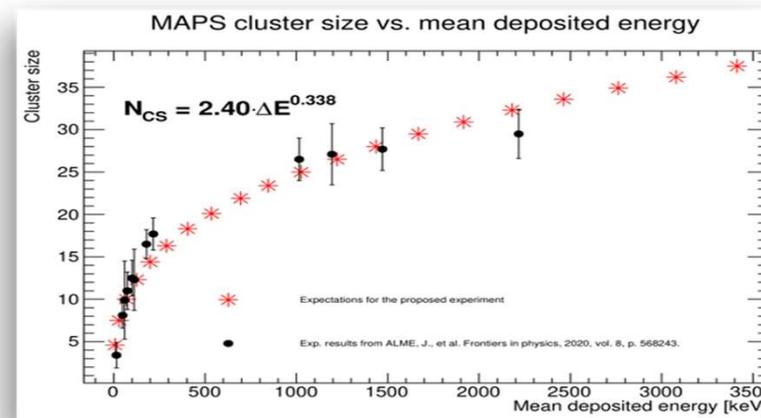
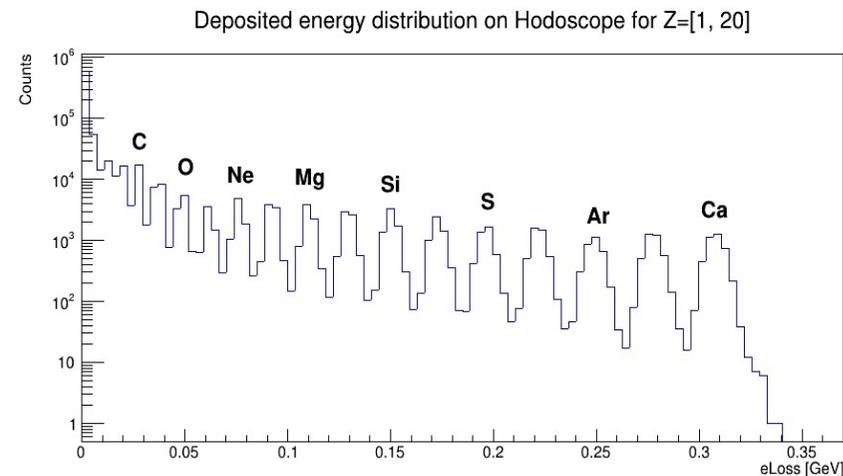
3-Pixel Clusters



4-Pixel Clusters



Proposal for exploiting magnetic separation and simplicity of light fragments charge identification of the projectile nucleus to measure cluster size dependence on deposited energy in the MICA chip



Cabling

Electronics

Data Cable

S-PCB-MOLEX-10006800107

ERF8-MOLEX-10006800107-V1-1

Ready for serial production

S-PCB-MOLEX-10006800107

Cables MOLEX-10006800107

ERF8-040-01-S-D-RA-L

Test station

CAEN A1676A
Controller (up to 6 EASY crates controlled)

EASYBUS

CAEN FAN UNIT CTR CABLE

VENTILATION UNIT 1U

MAINFRAME

CAEN A4528	CAEN A4531	CAEN SY4527
A4528 - SY4527/SY5527 CPU Module FULL	SY4527 Primary Power Supply 600W	Universal Multichannel Power Supply System

RJ45 FEED THROUGH 1U

CAEN

VME8100 8U (WVARIEAAAA) 7 PU+RU per crate

Power Board

PP_2

RU # 1

PP_3

BOB right

BOB left

Slave # 1

Filter Board "R"

Filter Board "L"

Data

Slave # 2

Filter Board "R"

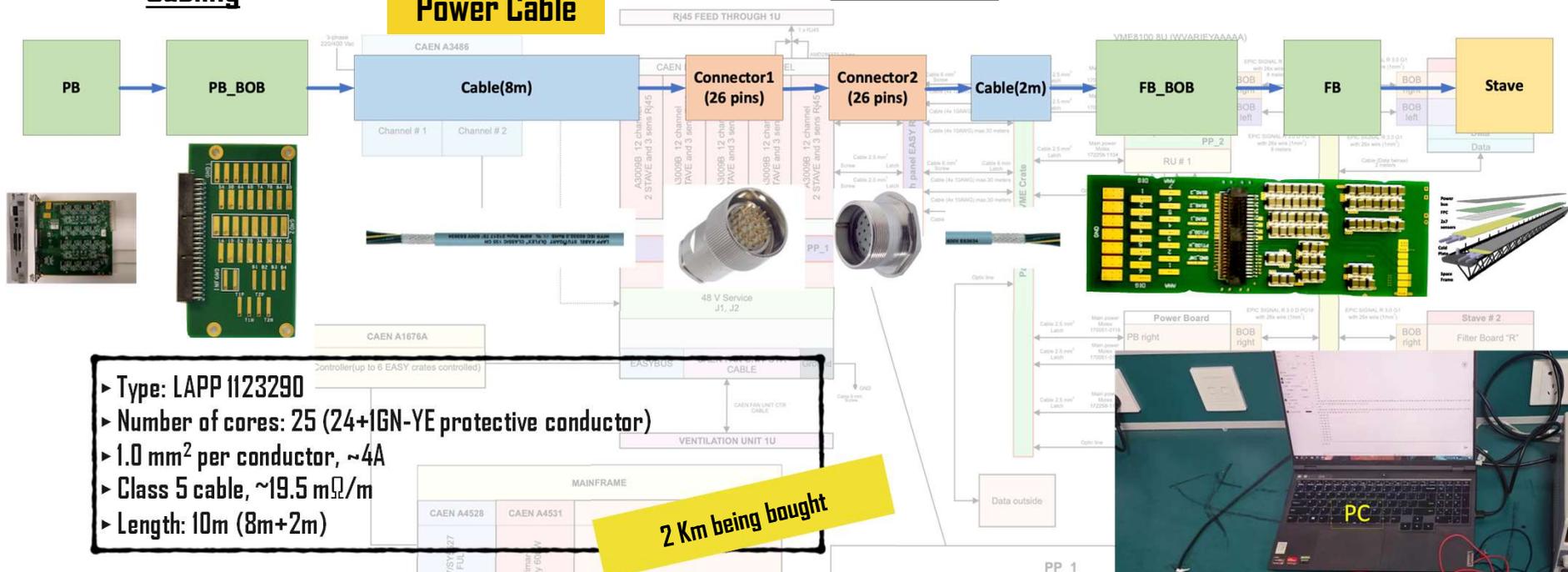
Filter Board "L"

Data

Cabling

Power Cable

Electronics

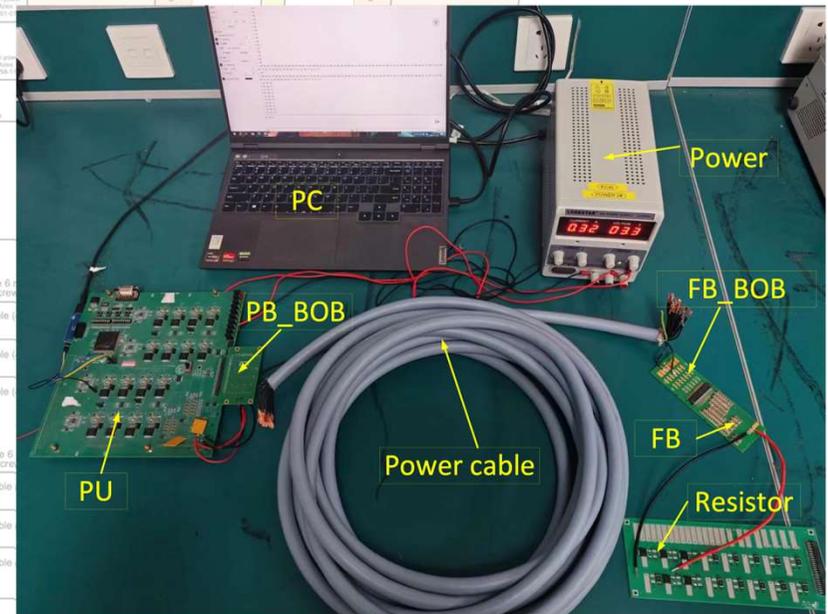


- ▶ Type: LAPP 1123290
- ▶ Number of cores: 25 (24+IGN-YE protective conductor)
- ▶ 1.0 mm² per conductor, ~4A
- ▶ Class 5 cable, ~19.5 mΩ/m
- ▶ Length: 10m (8m+2m)

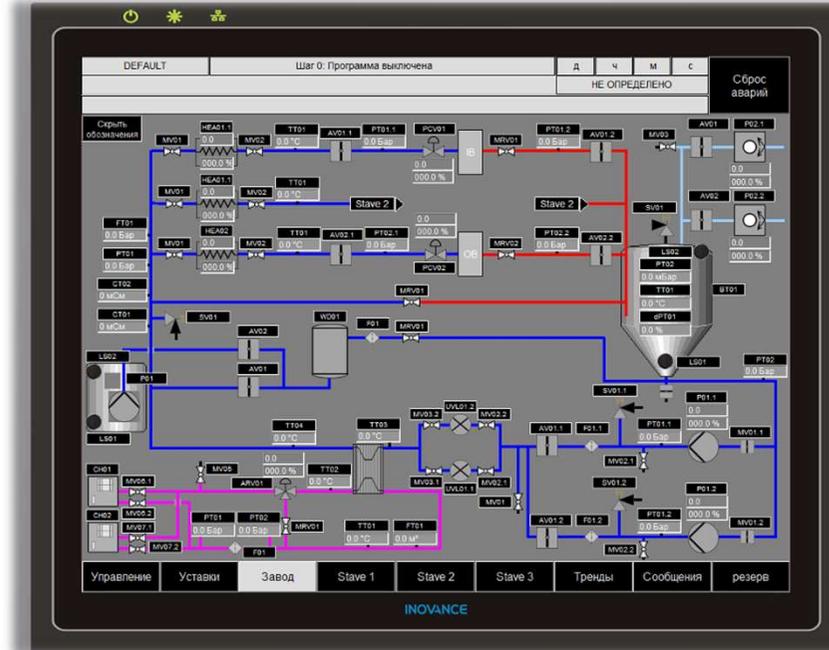
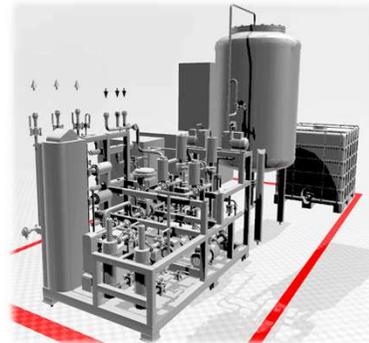
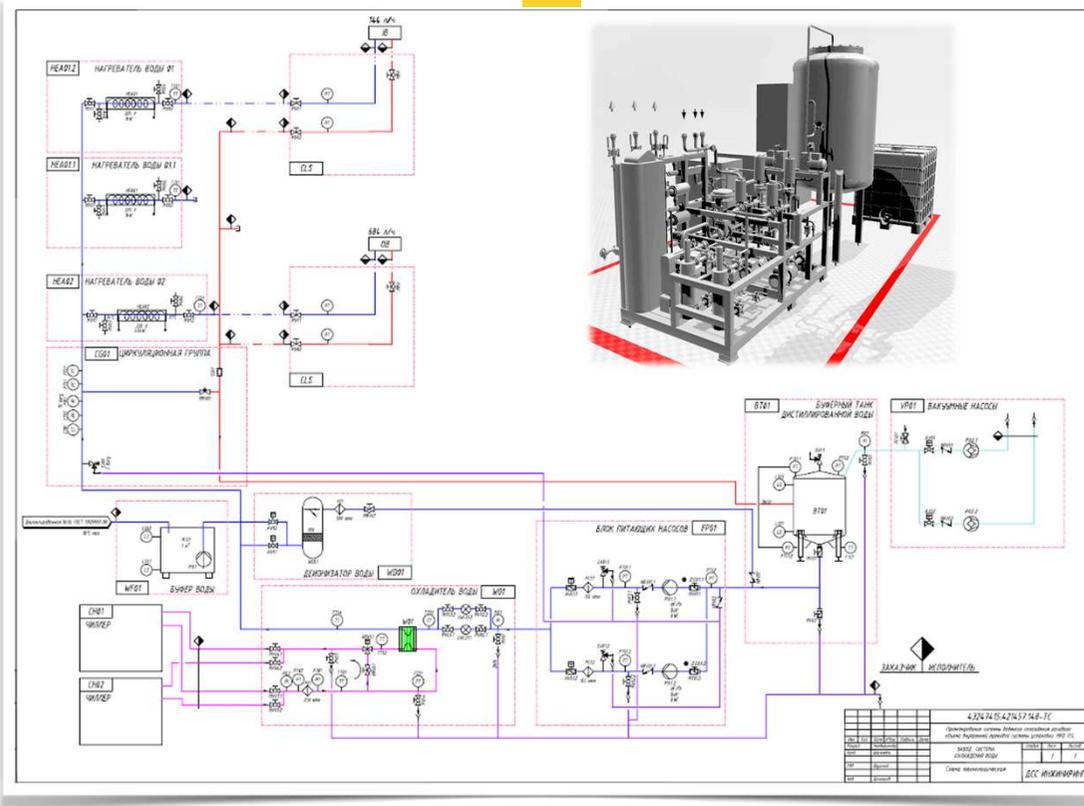
2 Km being bought

Load resistance(Ω)	Voltage at PU(V)	Voltage at load(V)	Current(A)	Voltage drop(V)	Resistance between PU and load(Ω)
1.2	1.805	1.313	1.094	0.492	0.450
1.8	1.802	1.442	0.801	0.360	0.449
4	1.798	1.617	0.404	0.181	0.448
5	1.798	1.649	0.330	0.149	0.452
7.5	1.798	1.696	0.226	0.102	0.451

Test station



Cooling Plant by DSSE for leak-less cooling

TX
ATX


- ▶ Delivery of instrumentation and control equipment (Oct. 2024)
- ▶ Delivery of installation materials (Oct. 2024)
- ▶ Production and tests (Jan. 2025).

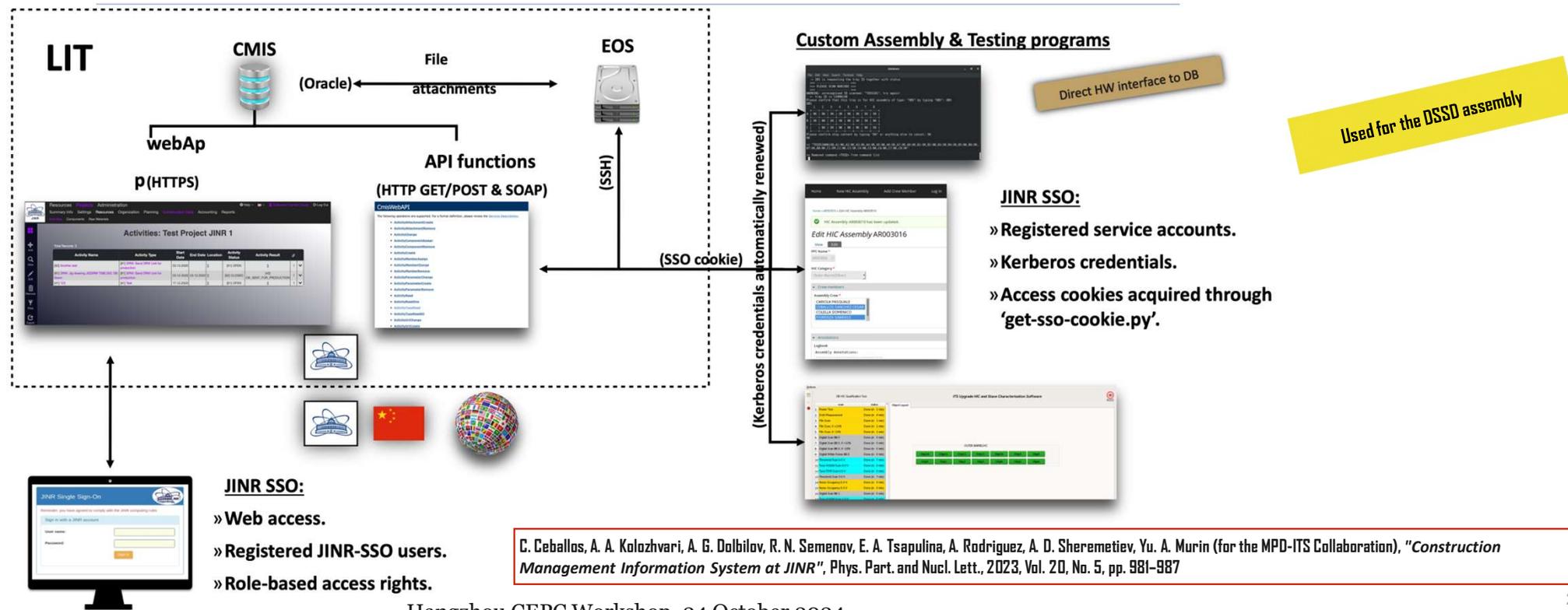
Barrel type	No. of Staves	No. of Panels	No. of Circuits	Power in the circuit [W]	Flow [l/h]
IB	96	96	24	240	288
OB	54	108	9	2187	684
Total ITS	150	204	33	2427	972

Construction Management Information System (Commissioned)

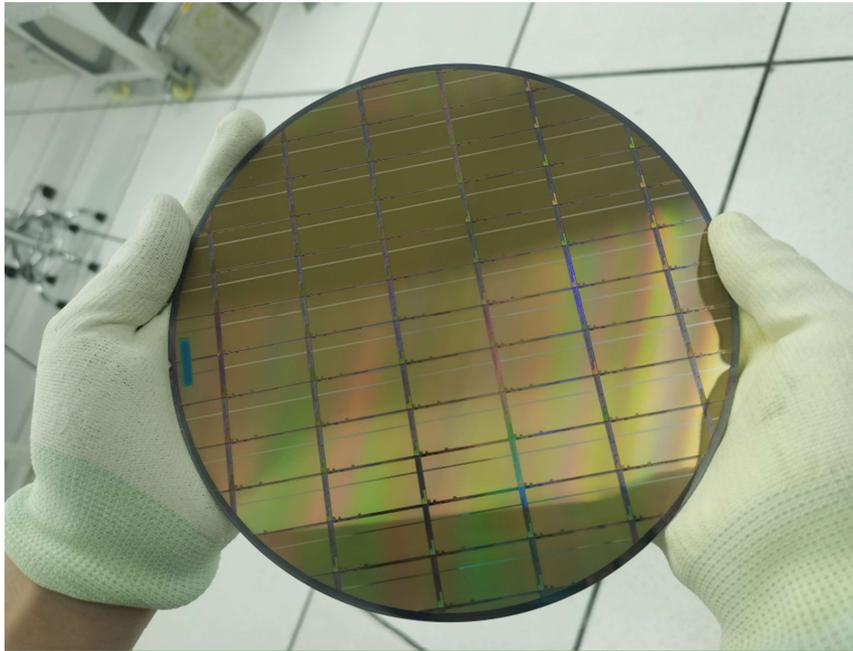
An Oracle-based all-around project management database system that allows the organization and follow-up of every aspect of a hardware production project.

It is designed to be accessed by human users and interfaced hardware independently.

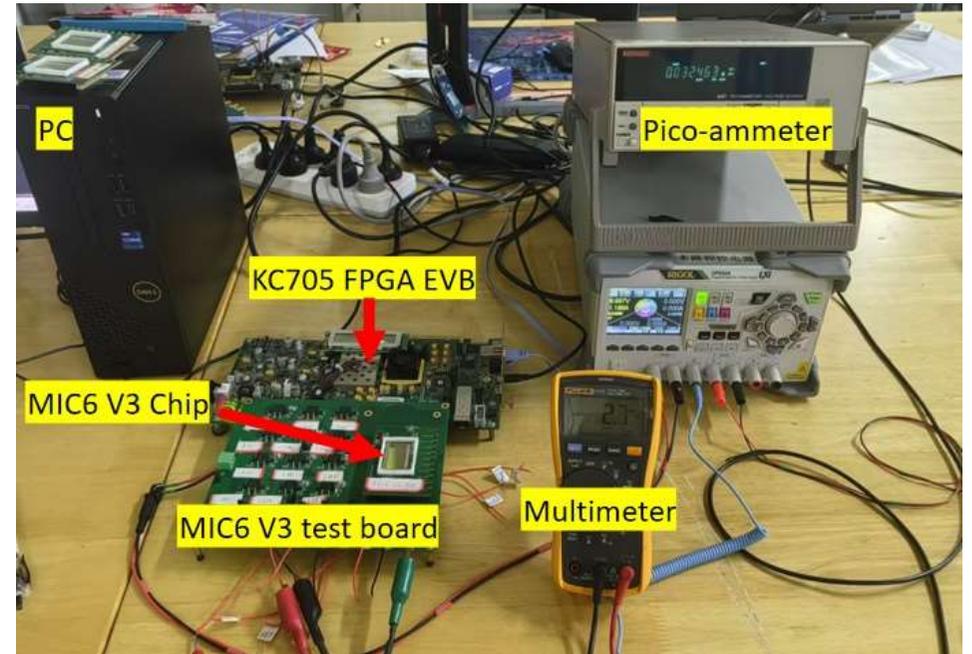
It is installed at LIT and might be accessed in real-time over the internet.



R&D of MICA chip (Next talk of Prof. Xiangming Sun (CNU))



MIC6 V3 Wafer photo



MIC6 V3 Test Platform

Establish the ‘NICA MPD-ITS Consortium’

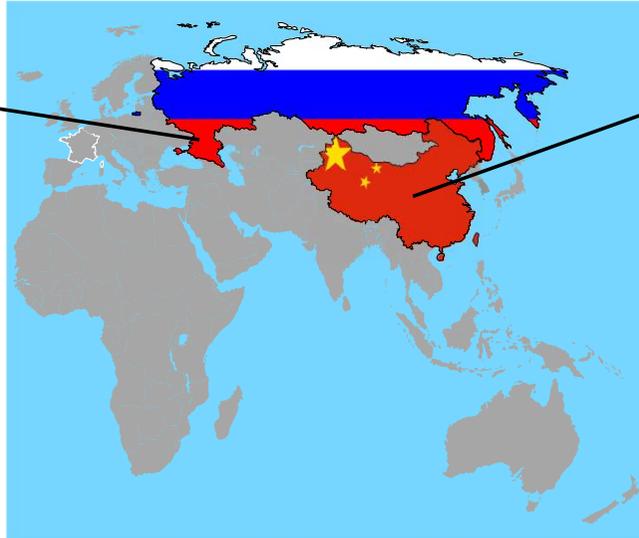
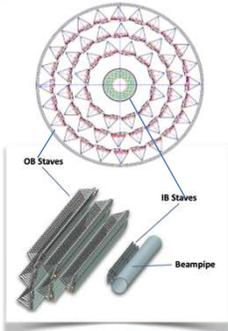
- **In order to further cooperate between JINR and Chinese institutions, the "NICA MPD-ITS Consortium" has been established:**
 - ◆ The acting time for the consortium is 5 years;
 - ◆ The coordinator center within the Russian Federation will be the JINR and in China will be the CCNU
 - ◆ The other institutions participating in the Consortium will have each one representative on the project structure for decision making and control.





Summary: uniting human and financial resources

MPD - ITS



Site for Assembly and QA tests at JINR



Site for Assembly and QA tests at CCNU

Proposal for joint JINR-China projects

Project: Monolithic Si-Pixel Detector for Collider Experiments and Other Applications

	2024	2025	2026	2027	2028	2029
MICA R and D	R and D and testing			Preseries run		
Readout	PU 8FPGA version RU R&D complete	ASIC version RU R and D complete				
GBTx and ROC	R and D complete					
Assembly	R and D Setup assembly line at Gand CNU and IMP	R and D, Assemble HICs/staves and testing at CCNU, IMP and JINR	Assembly 1/12 of the tracker including Readout	Assembly the full tracker (IB, OB) and test at the experimental site. Ready to take data in 2030		

- 6 layers vertex detector.
- Monolithic Active Pixel Sensors (MAPS) & ASICs-based Readout:
 - ▶ Developed and made in China.
 - ▶ Unrestricted access for China and Russia (**Currently forbidden**).
 - ▶ Applicable also to Space science and Medical Imaging.
- 5μm spatial resolution.
- 5.5 GPixels in total.

Hangzhou CEPC Workshop, 24 October 2024



Credits and Thanks

MPD - ITS



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Musa Luciano
 Di Mauro Antonello

from the NICA MPD ITS Consortium



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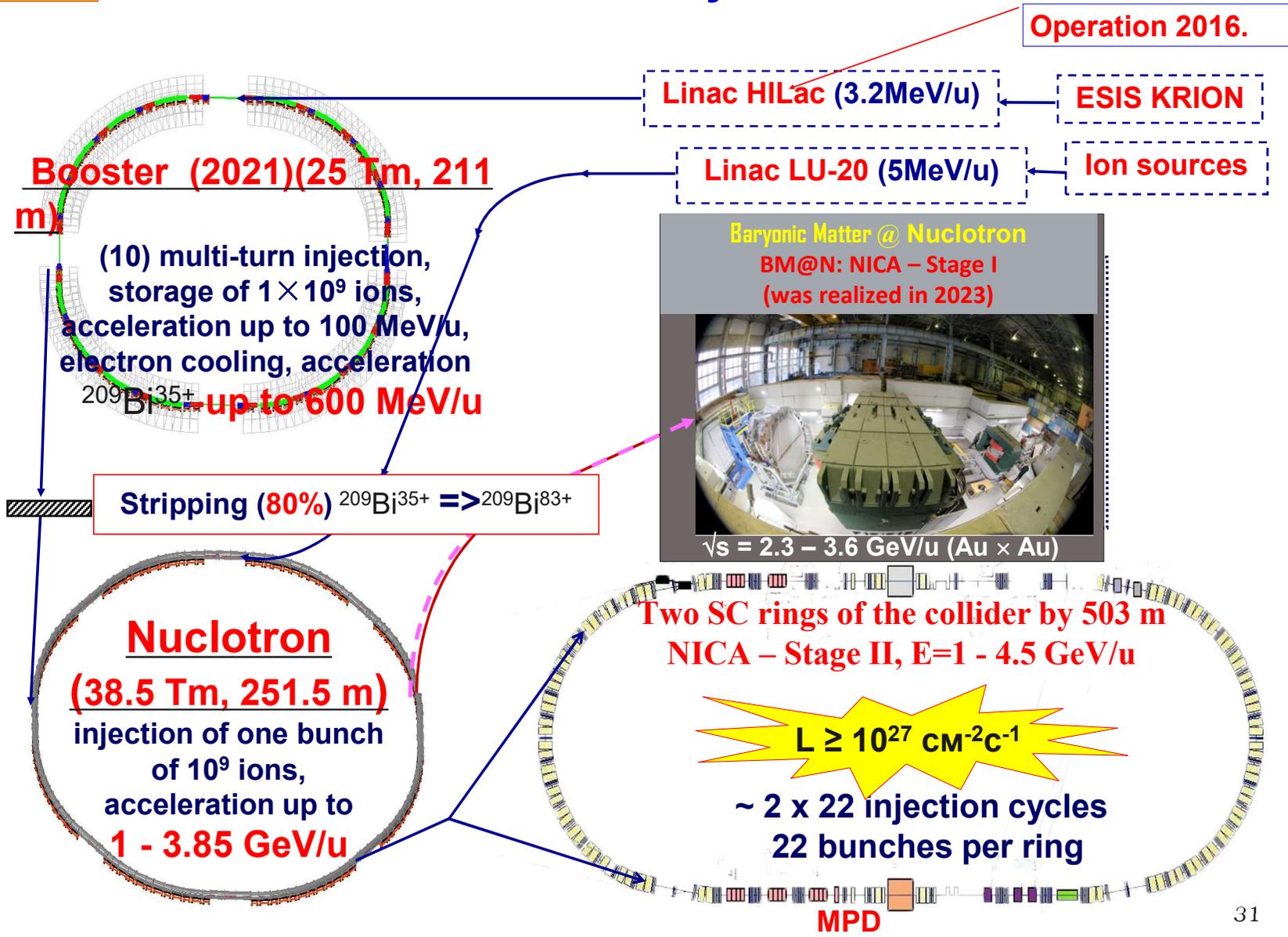
Backup slides

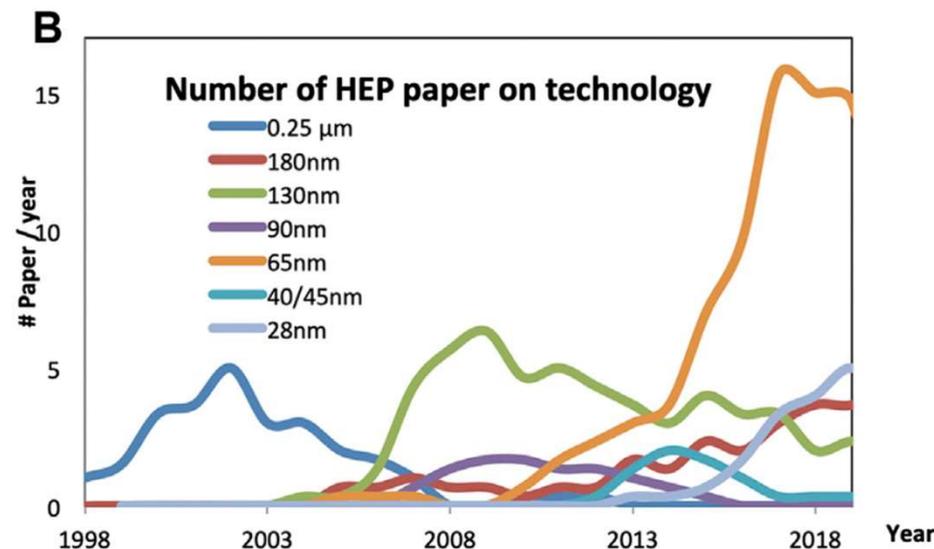
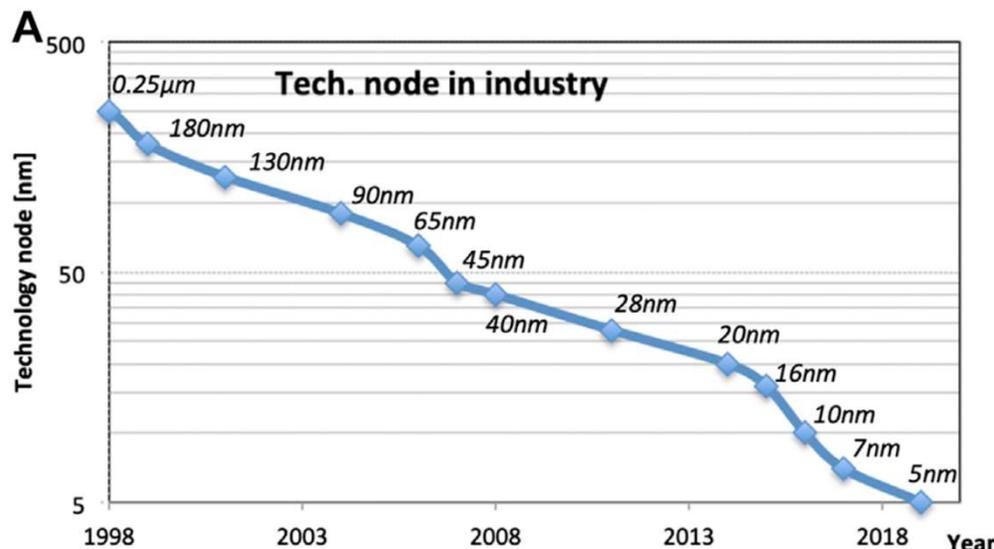
MPD - ITS



2025

NICA – collisions for Heavy ion mode





A) development of technology nodes in industry (example from one factory); (B) Number of HEP papers relevant to R&D of microelectronics design where a specific CMOS technology is directly specified in the title: data shown are three years average and are obtained from a web based search

N.Demaria in Frontiers in Physics, 8 March, 2021