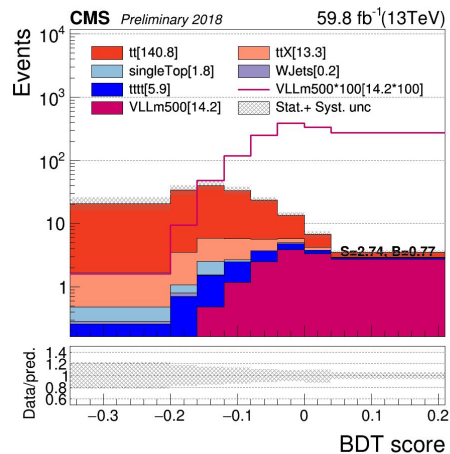


Analysis of 4top process with VLL model(2018)

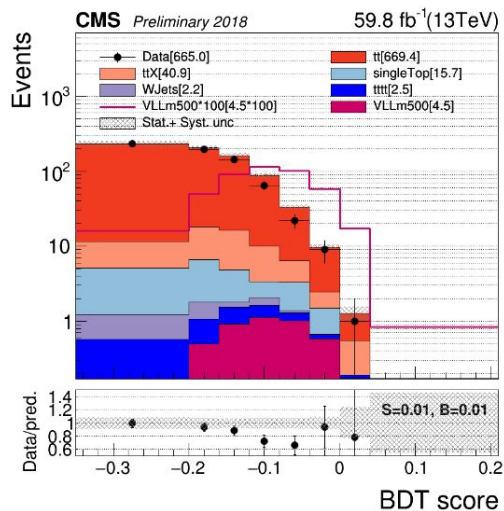
VLLm500 **best bin**(last bin: 0.04-0.2)



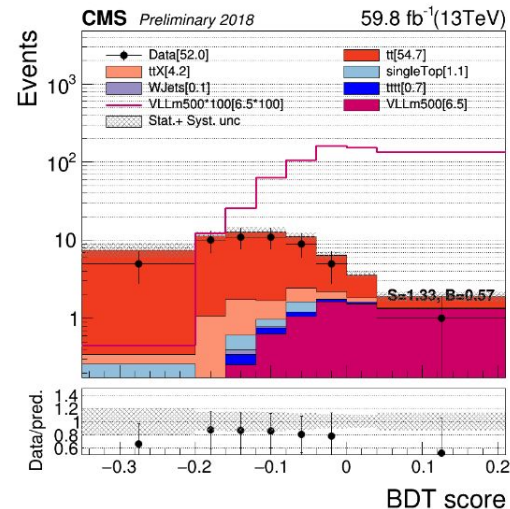
SR

limit: 0.75

significance: 2.87

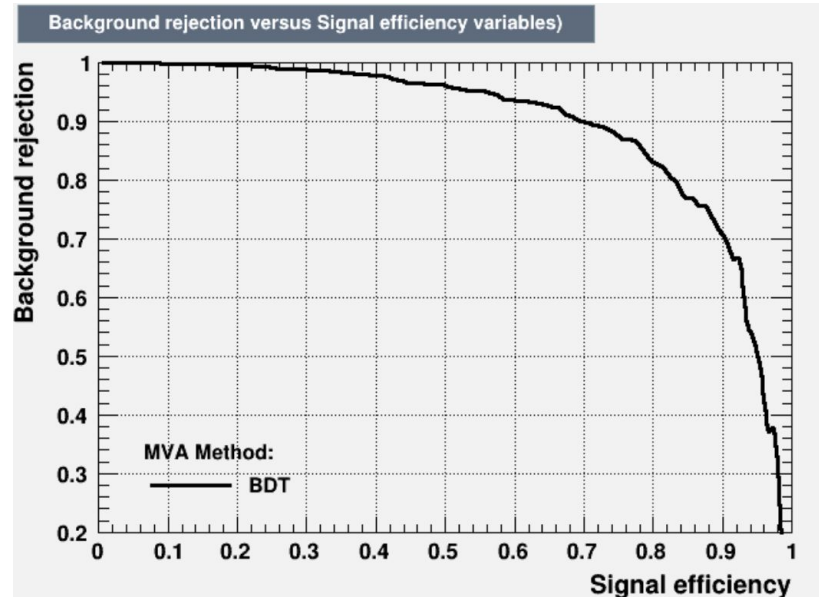
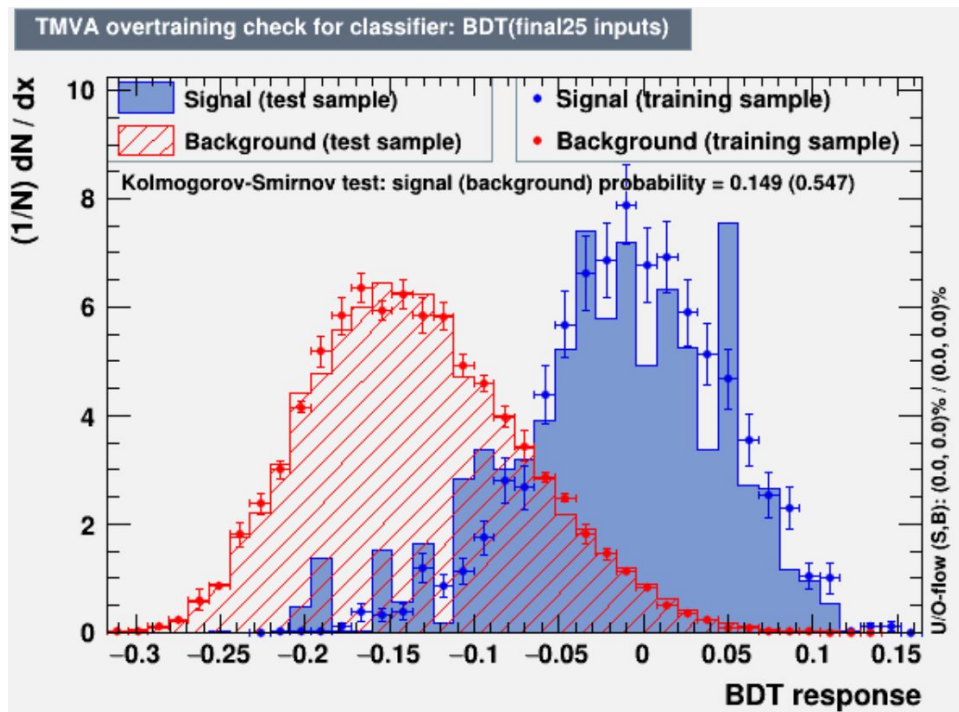


CR1

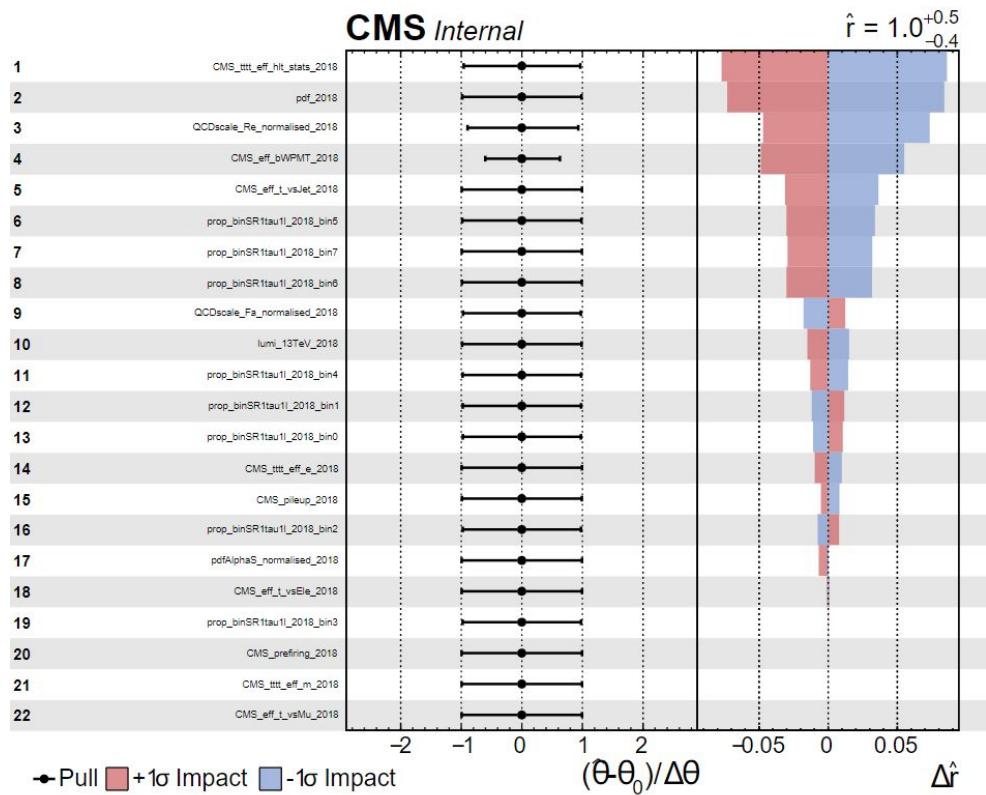


CR2

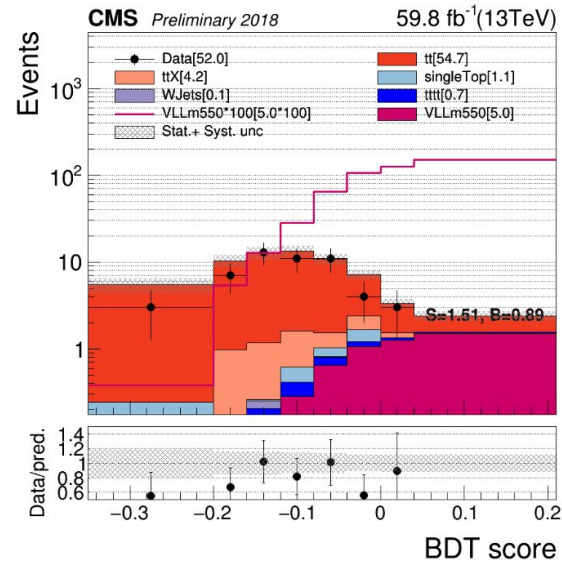
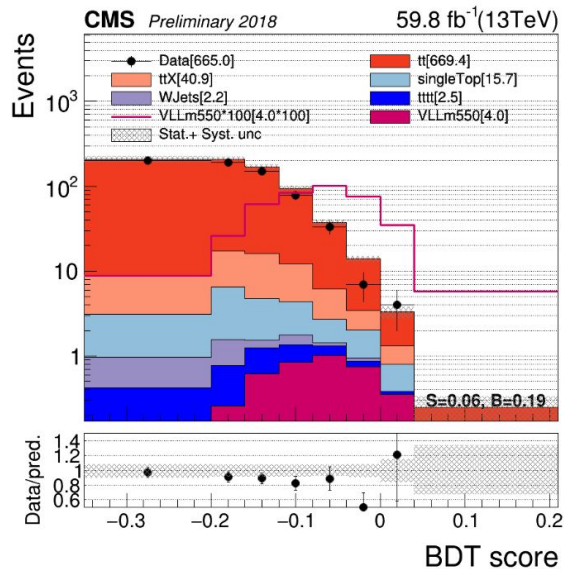
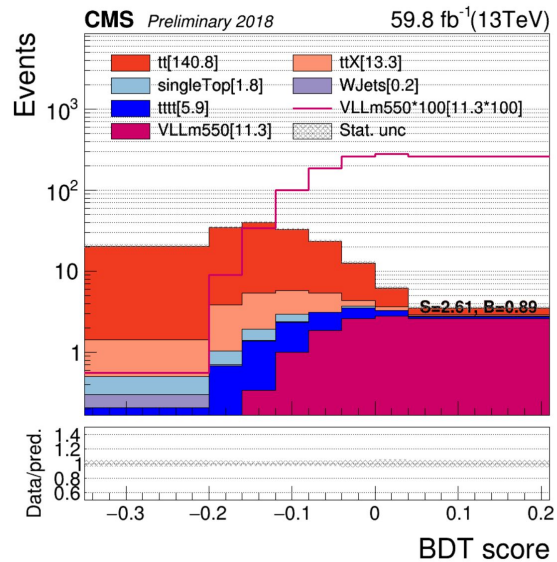
TMVA check



Combine impact result

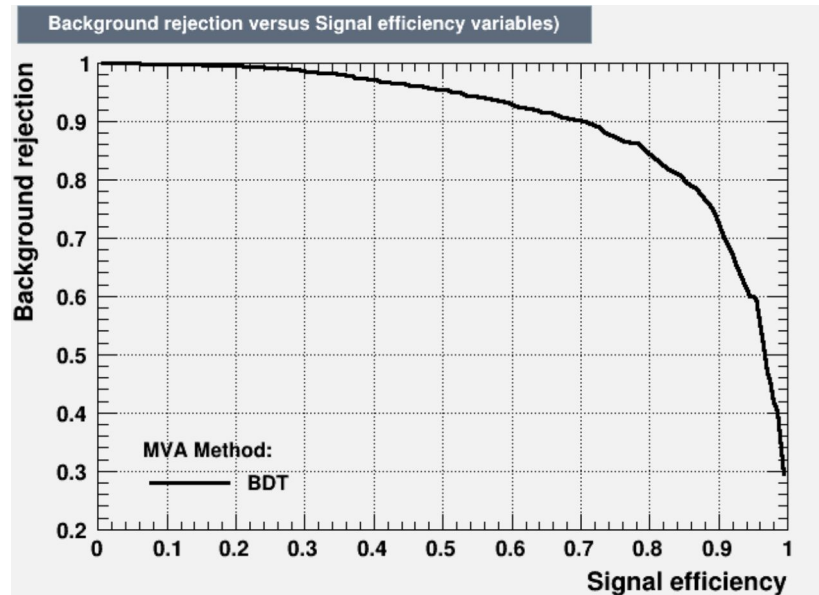
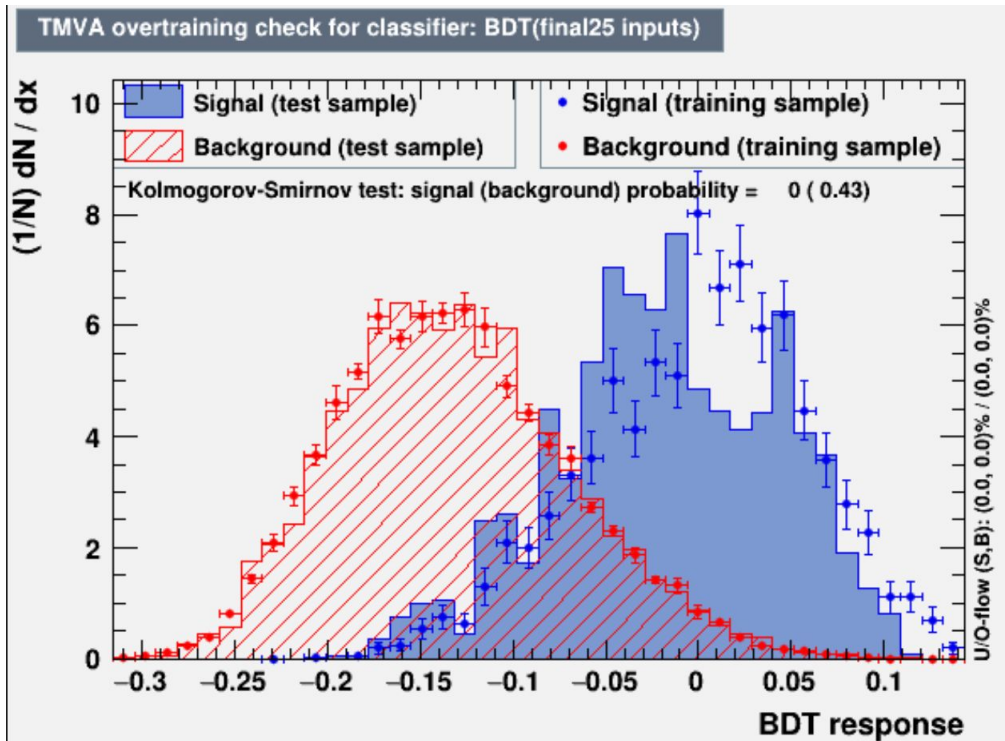


VLLm550 **best bin**(last bin: 0.04-0.2)

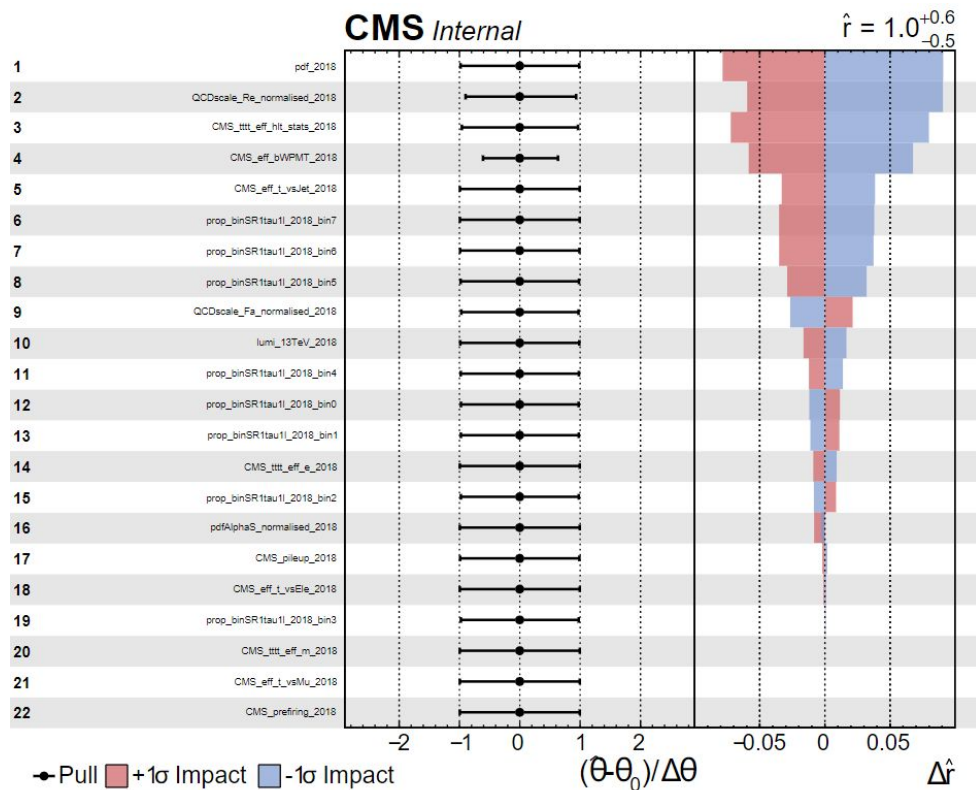


limit: 0.89 significance: 2.51

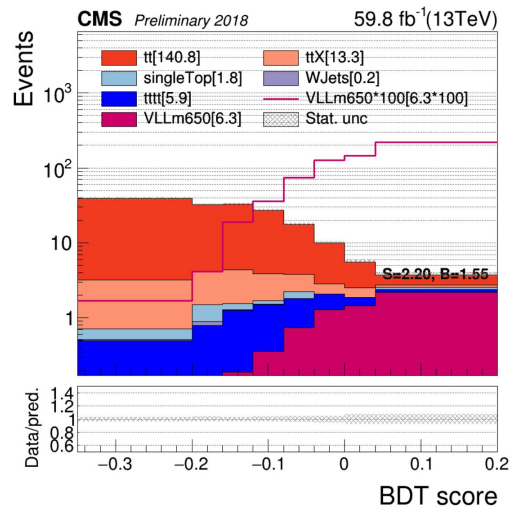
TMVA check



Combine impact result



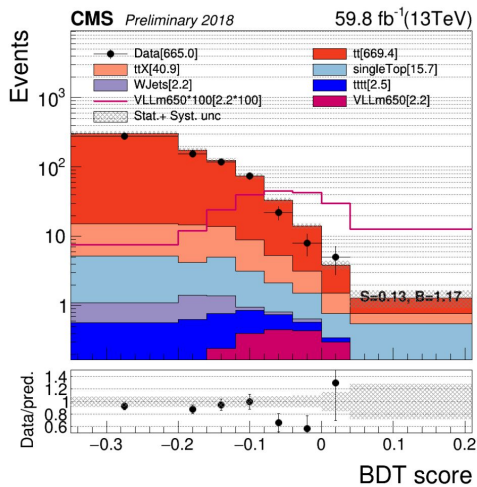
VLLm650 **best bin**(last bin: 0.04-0.2)



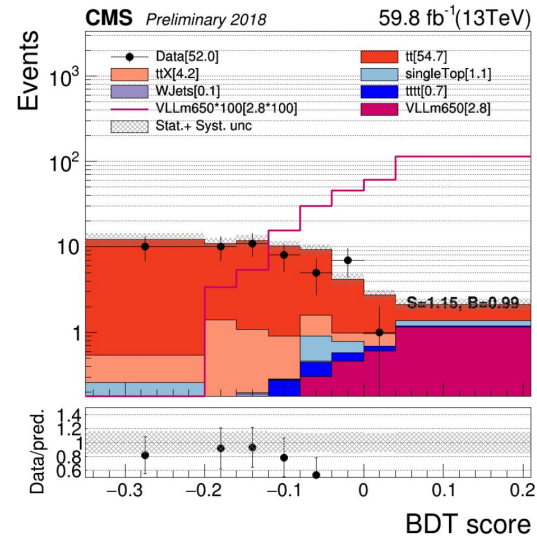
SR

limit: 1.47

significance: 1.61



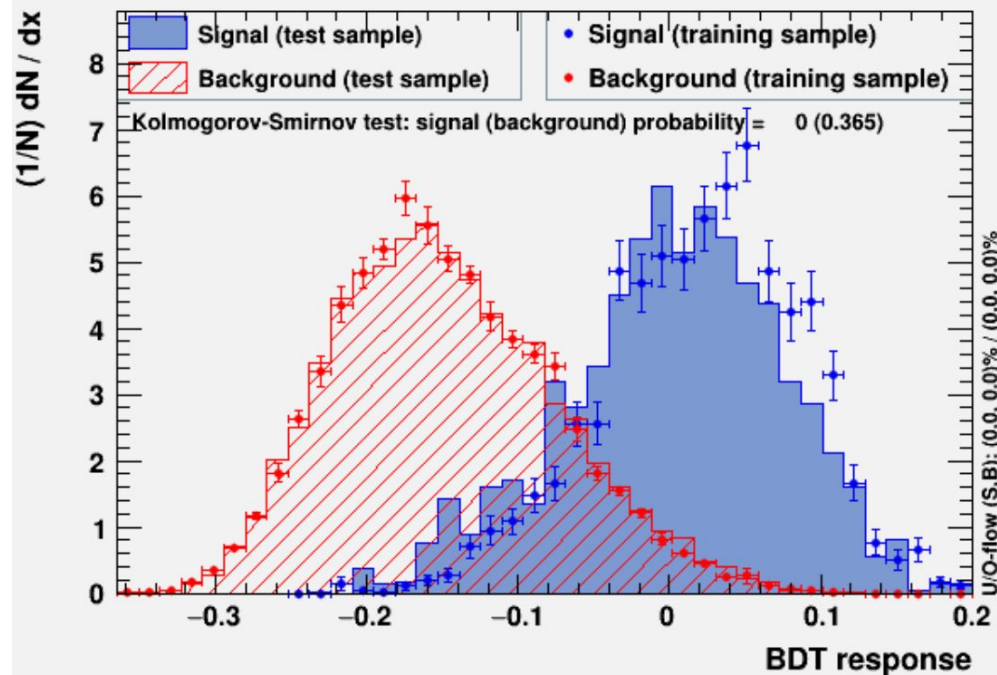
CR1



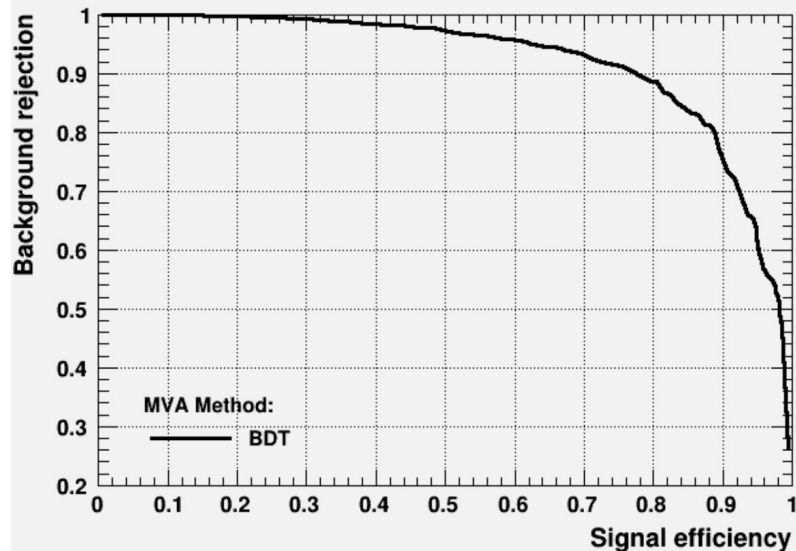
CR2

TMVA check

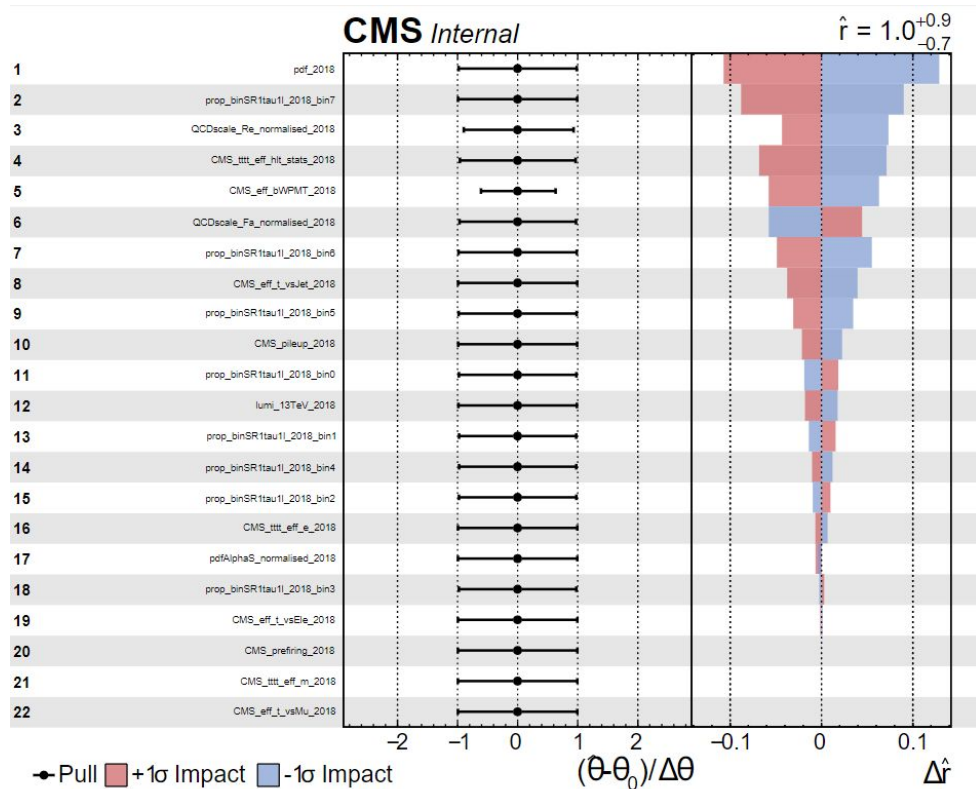
TMVA overtraining check for classifier: BDT(final25 inputs)



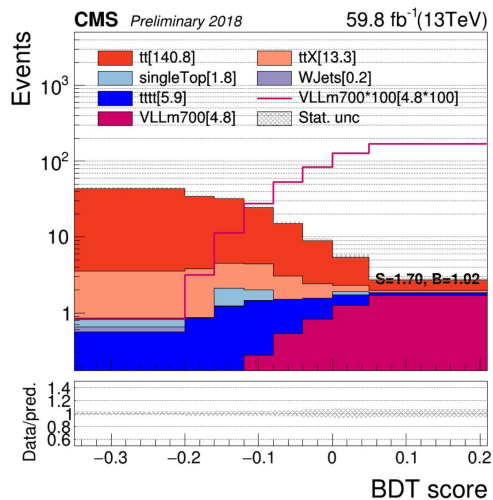
Background rejection versus Signal efficiency variables)



Combine impact result

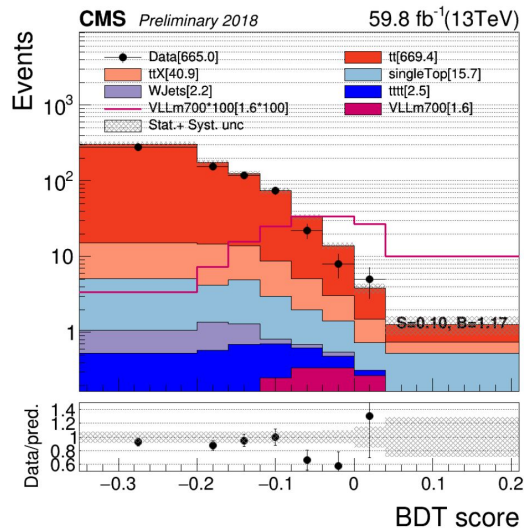


VLLm700 **best bin**(last bin: 0.05-0.2)

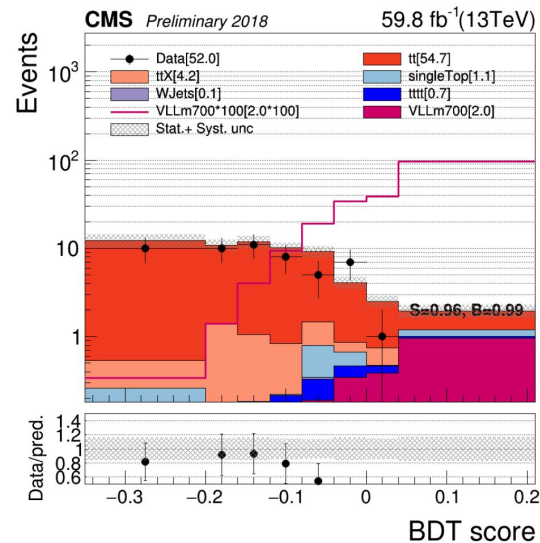


SR

limit: 1.71 significance: 1.49



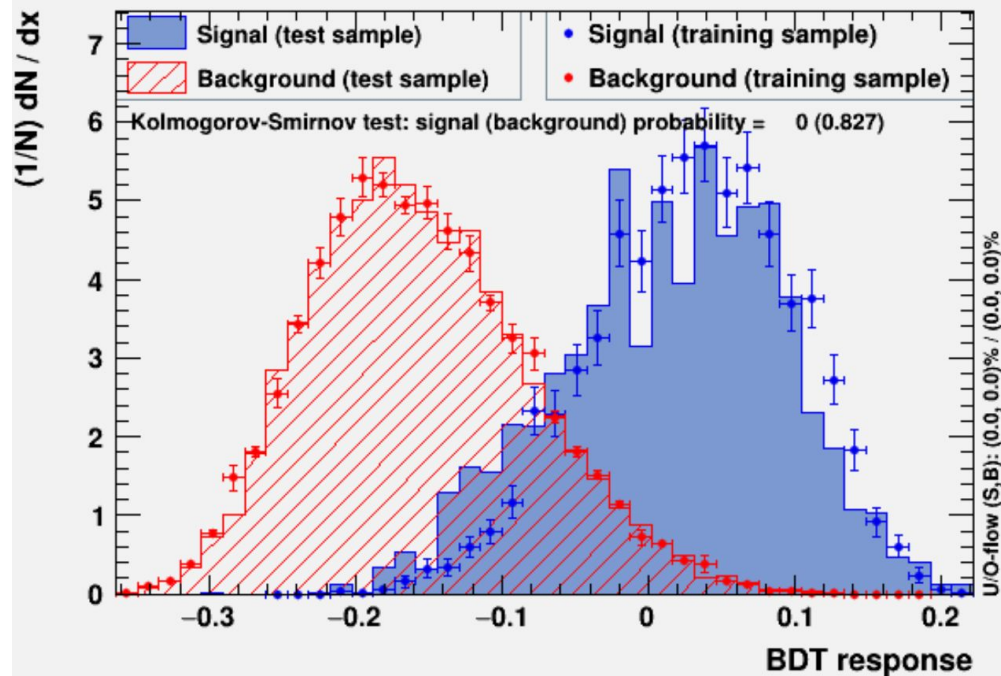
CR1



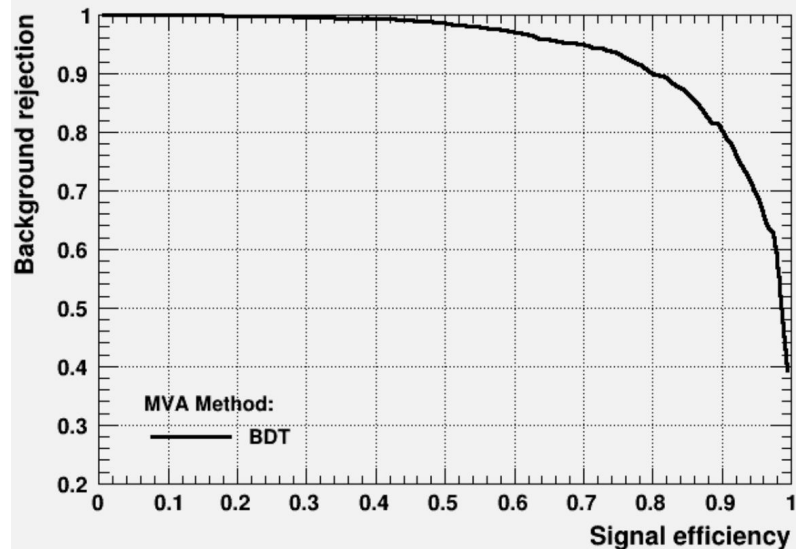
CR2

TMVA check

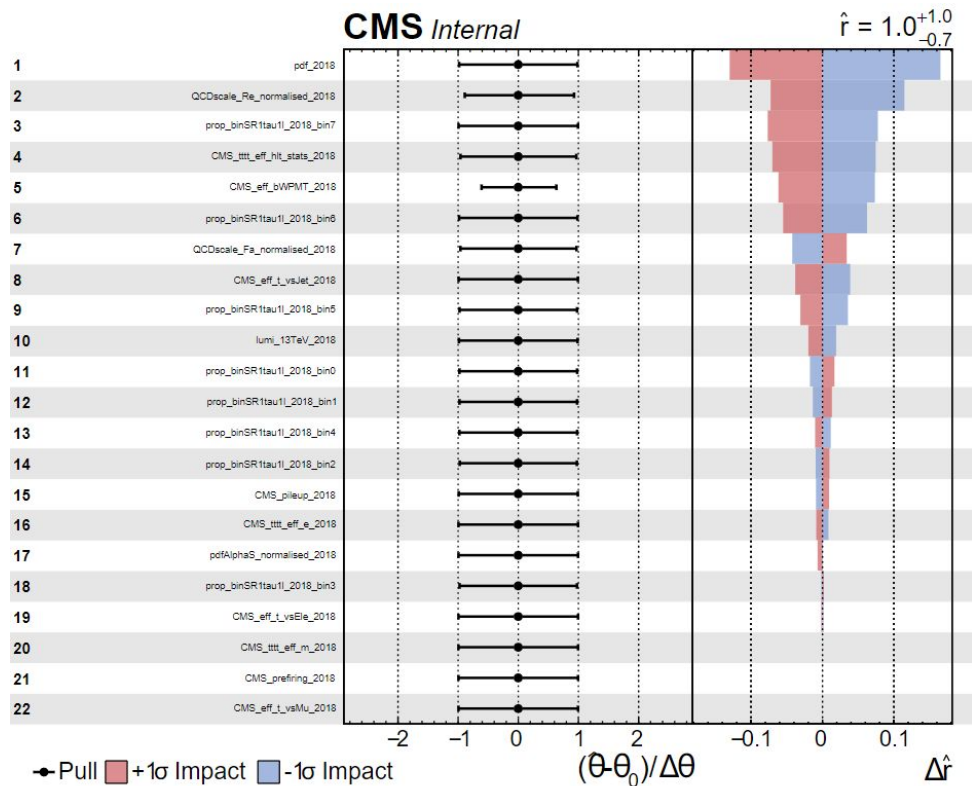
TMVA overtraining check for classifier: BDT(final25 inputs)



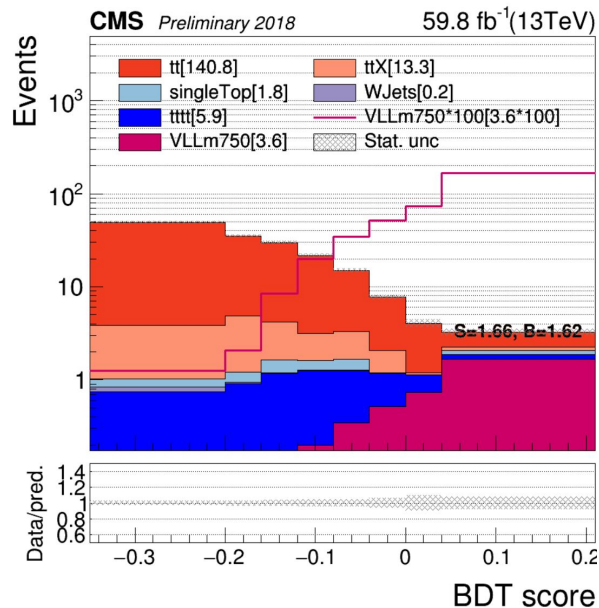
Background rejection versus Signal efficiency variables)



Combine impact result



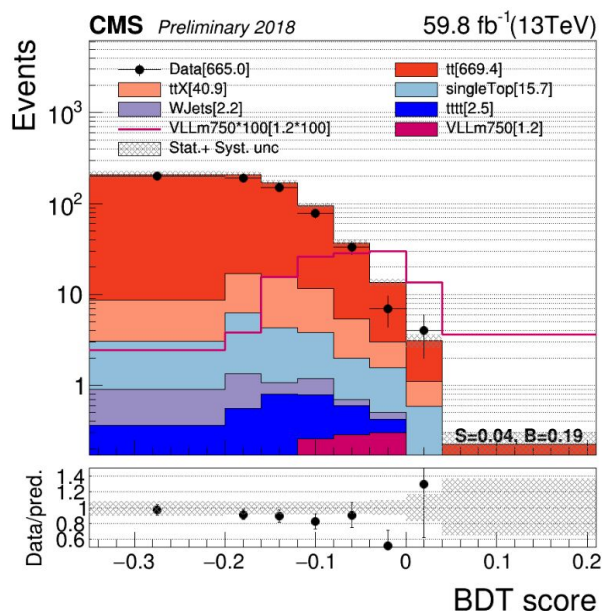
VLLm750 **best bin**(last bin: 0.04-0.2)



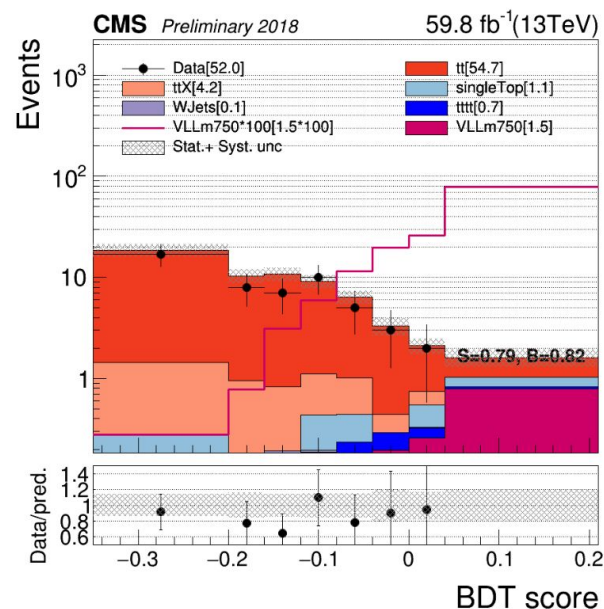
SR

limit: 2.16

significance: 1.18



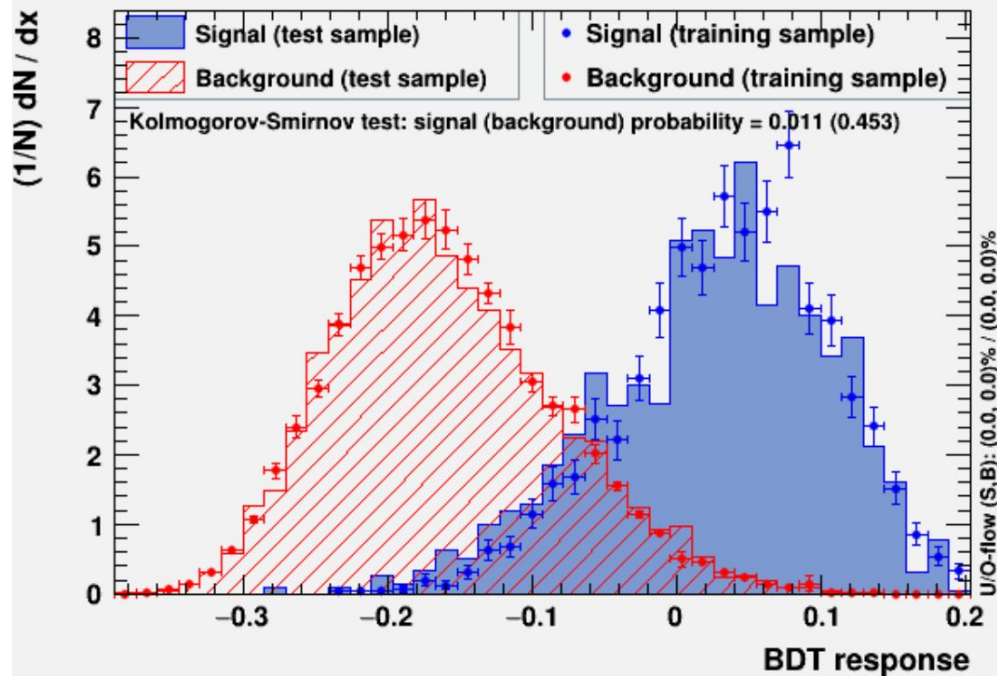
CR1



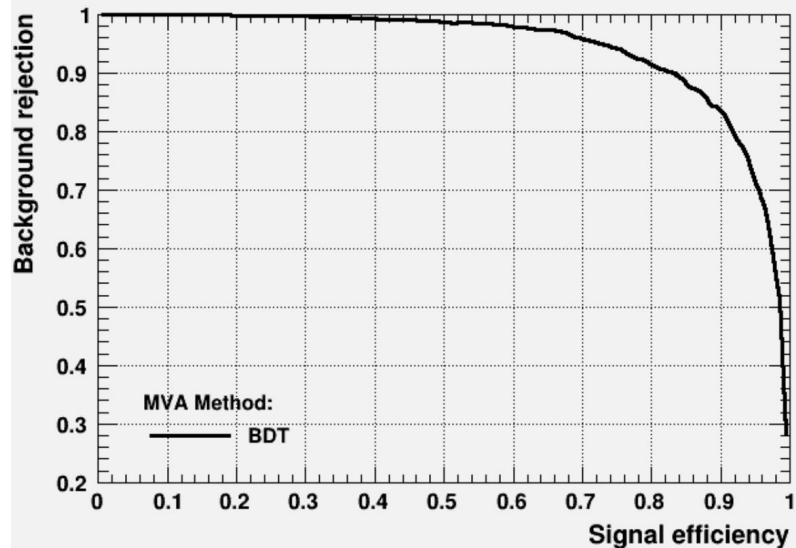
CR2

TMVA check

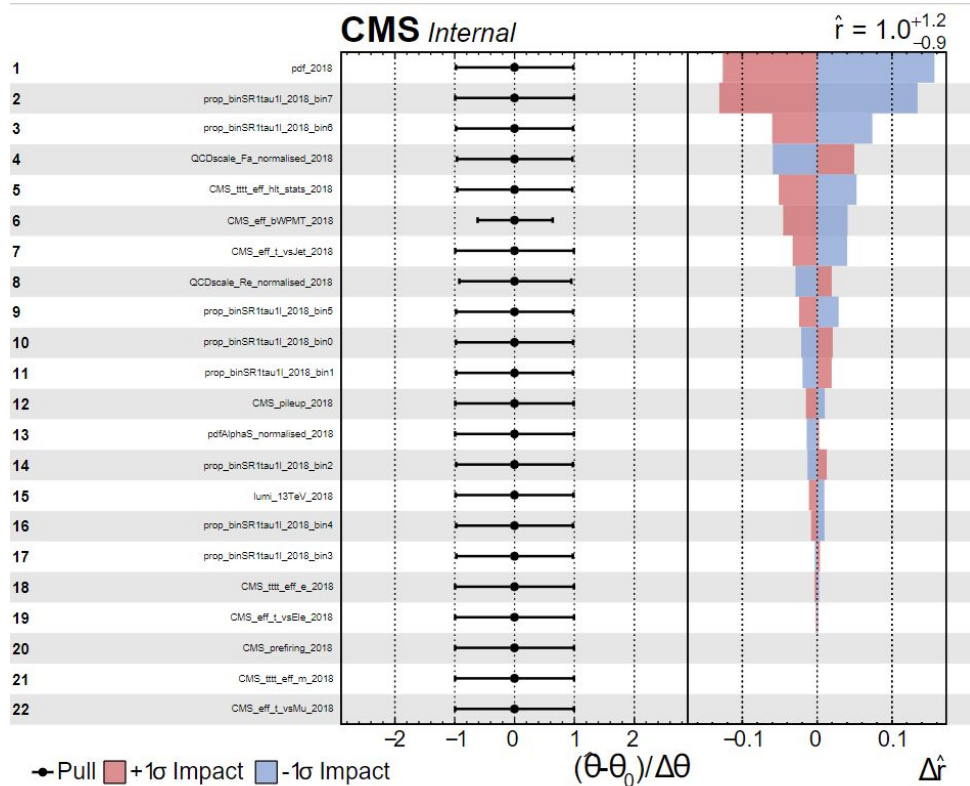
TMVA overtraining check for classifier: BDT(final25 inputs)



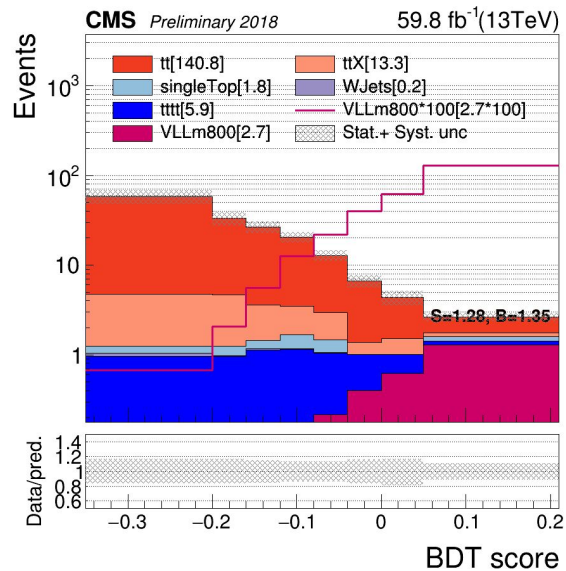
Background rejection versus Signal efficiency variables)



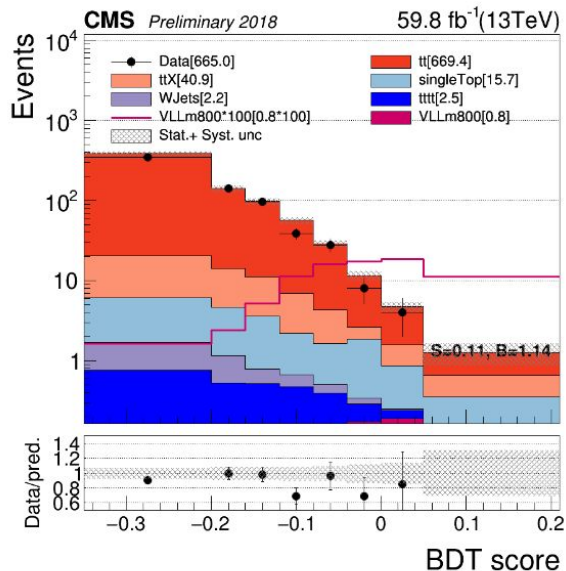
Combine impact result



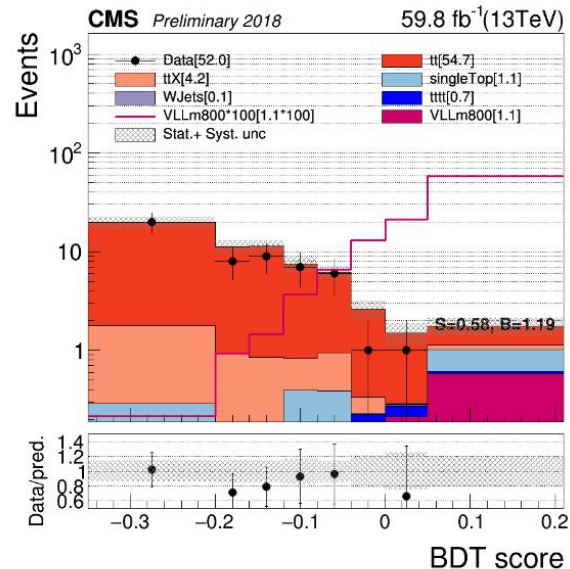
VLLm800 **best bin**(last bin: 0.05-0.2)



SR



CR1



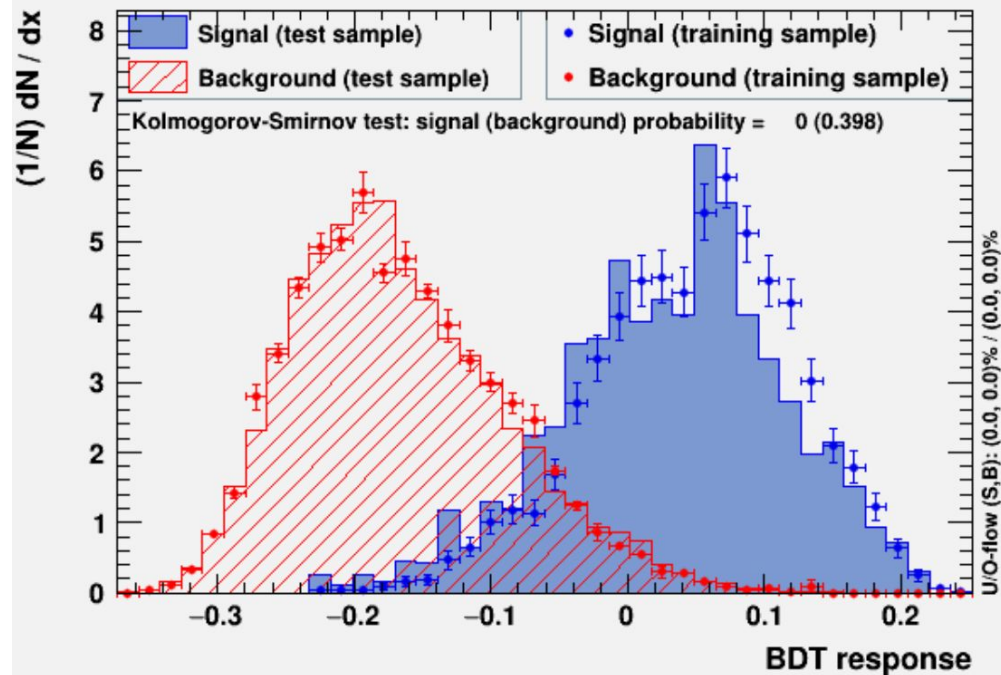
CR2

limit: 2.69

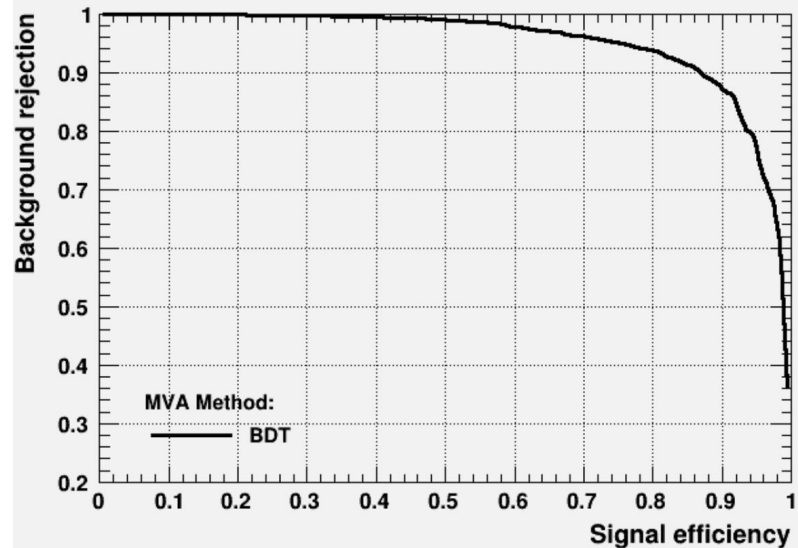
significance: 0.99

TMVA check

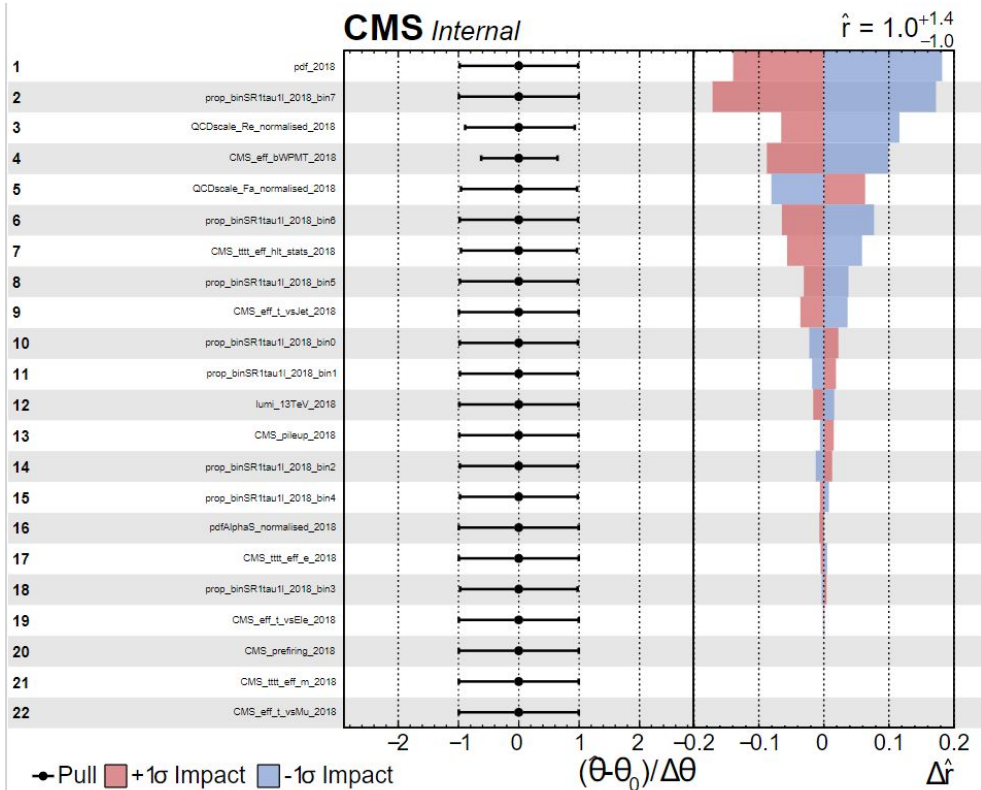
TMVA overtraining check for classifier: BDT(final25 inputs)



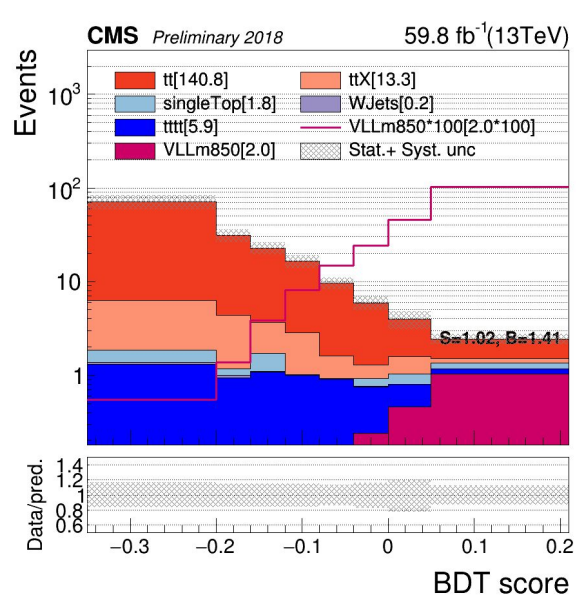
Background rejection versus Signal efficiency variables



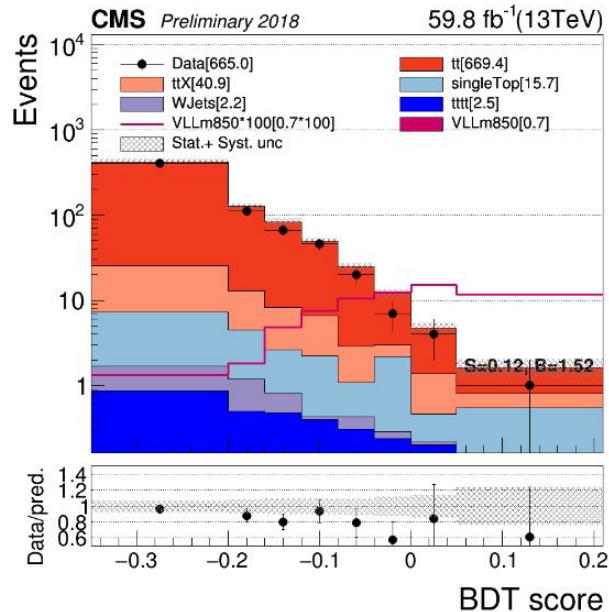
Combine impact result



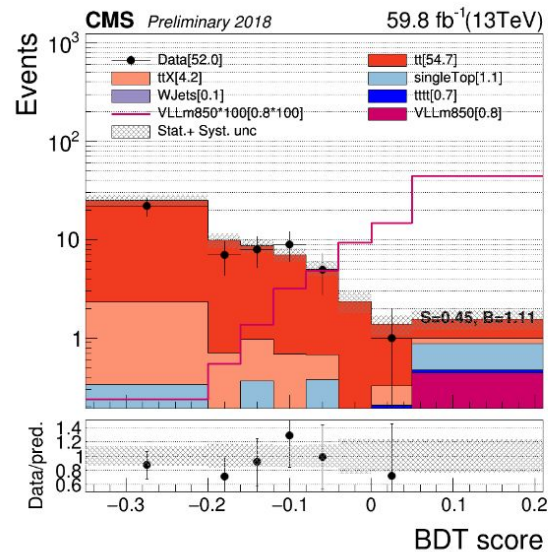
VLLm850 **best bin**(last bin: 0.05-0.2)



SR



CR1



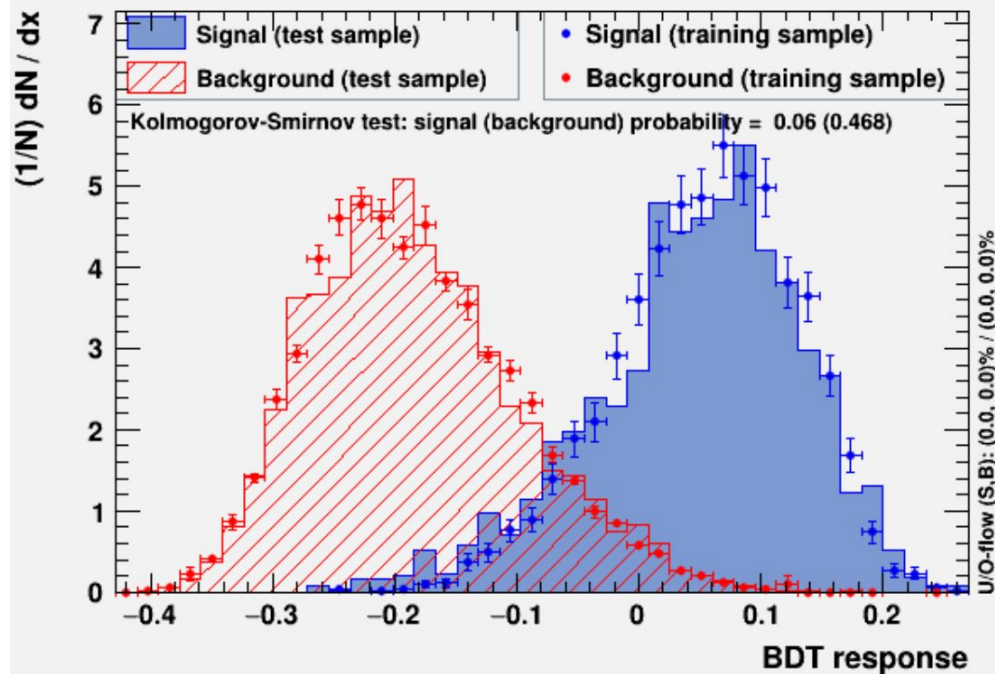
CR2

limit: 3.47

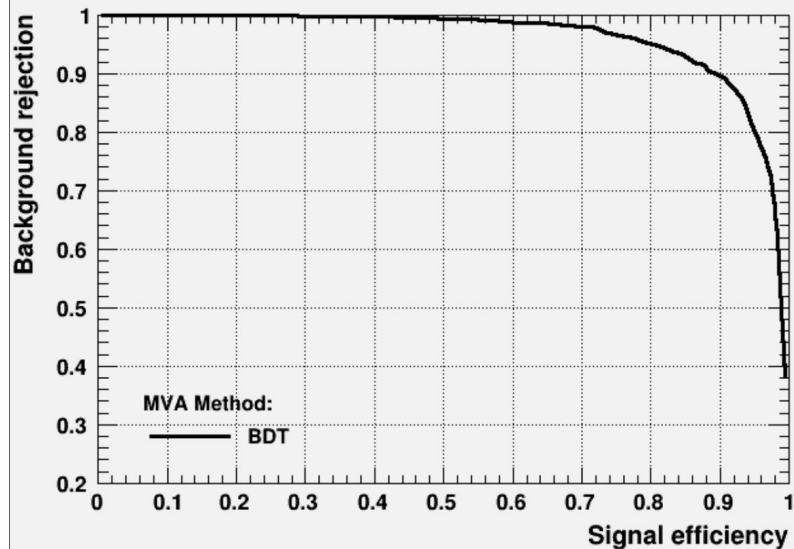
significance: 0.79

TMVA check

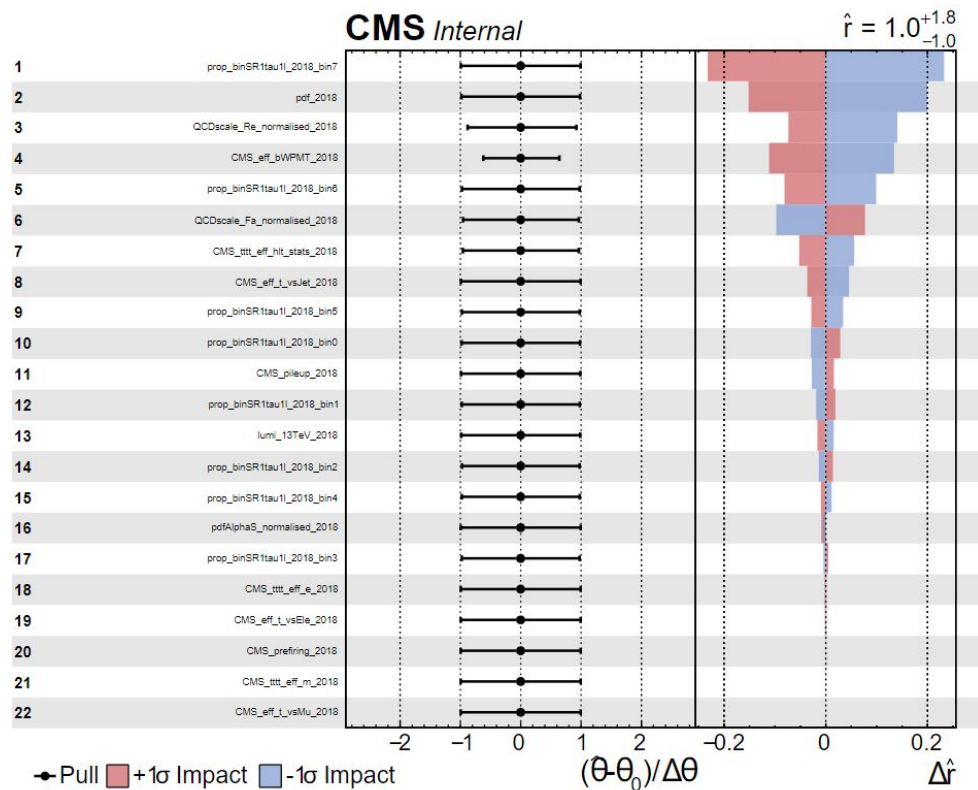
TMVA overtraining check for classifier: BDT(final25 inputs)



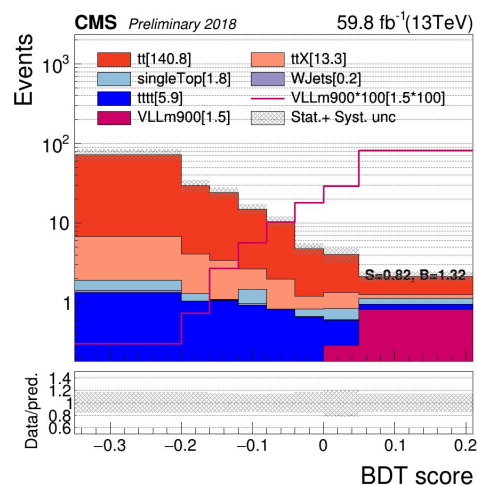
Background rejection versus Signal efficiency variables)



Combine impact result



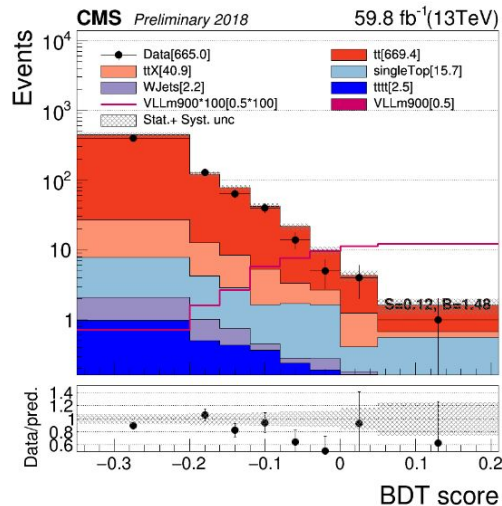
VLLm900 best bin(last bin: 0.05-0.2)



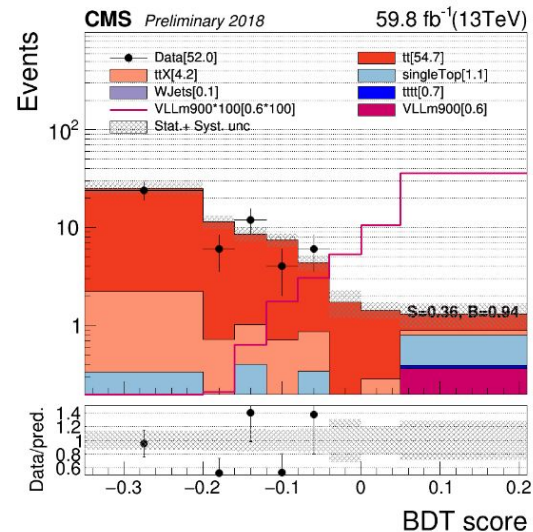
SR

limit: 4.38

significance: 0.65



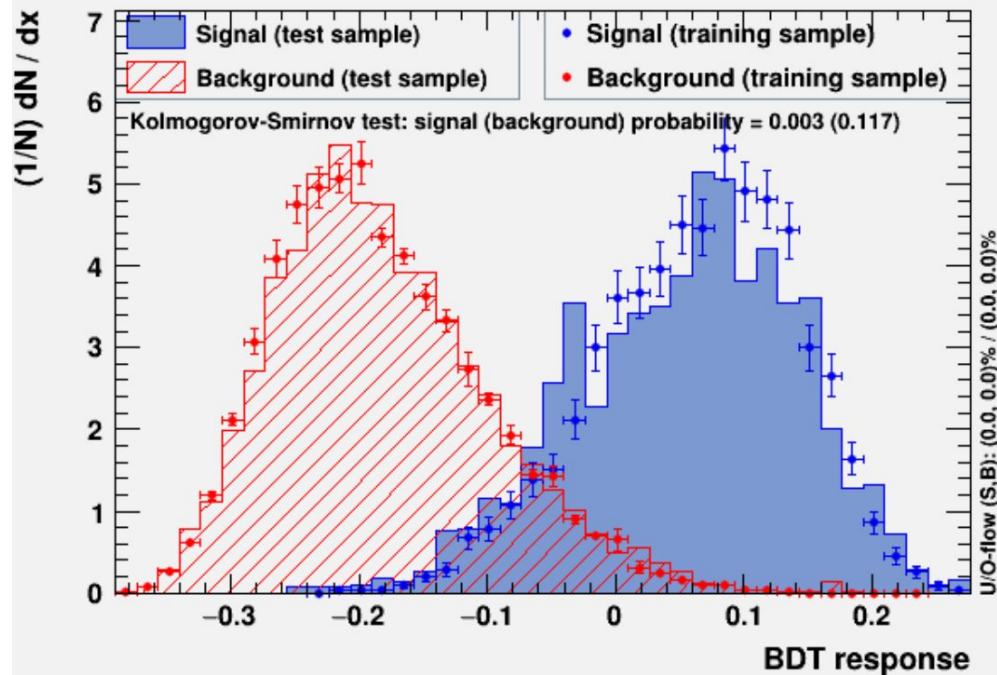
CR1



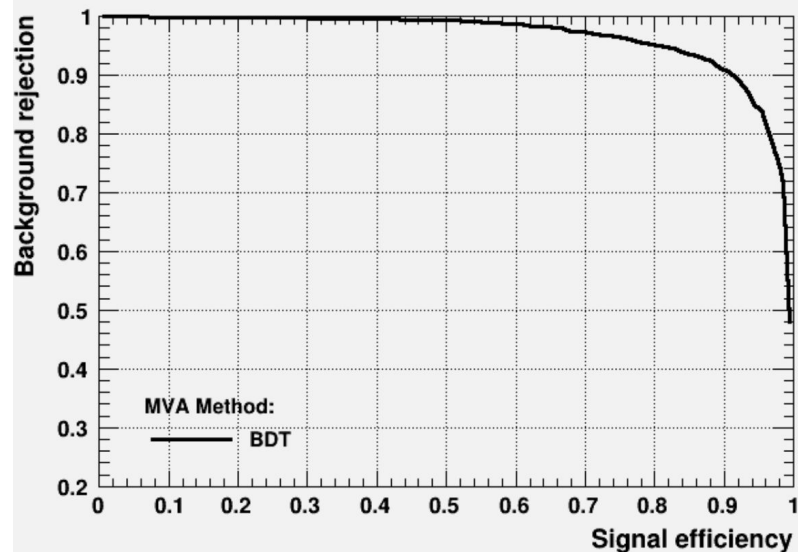
CR2

TMVA check

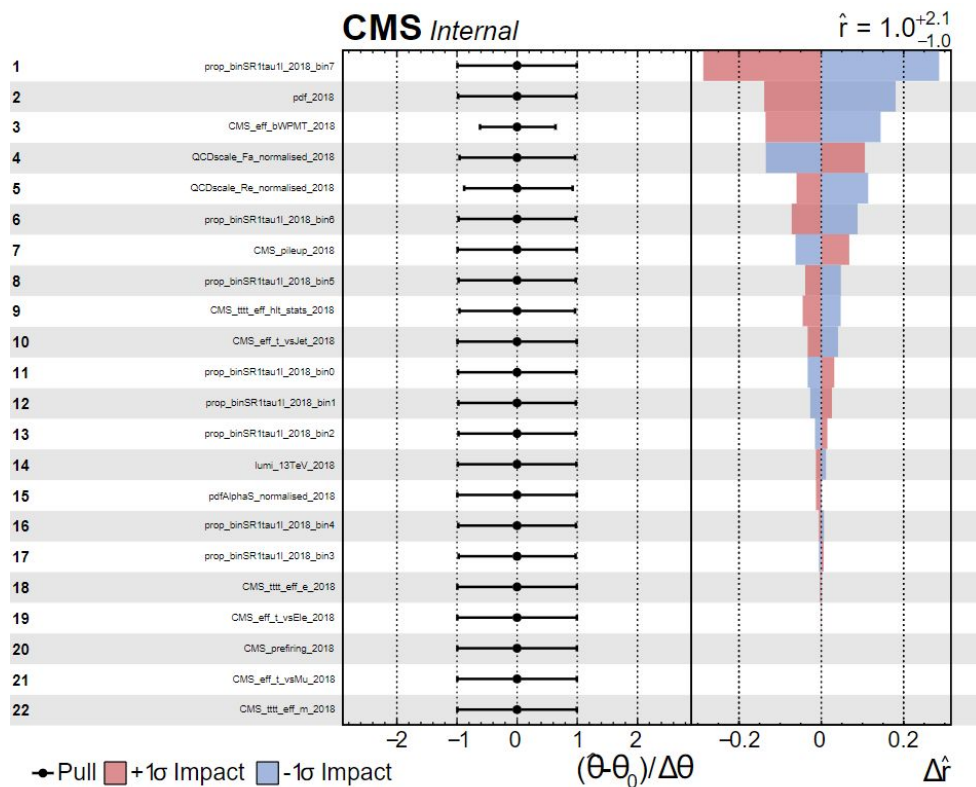
TMVA overtraining check for classifier: BDT(final25 inputs)



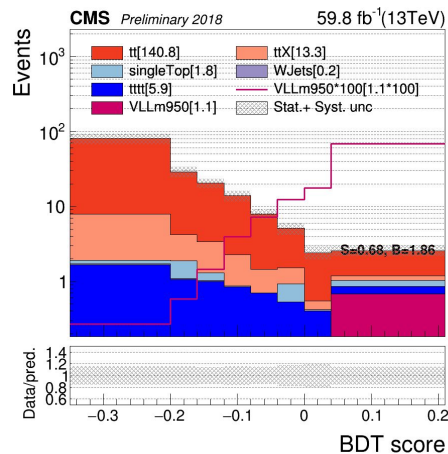
Background rejection versus Signal efficiency variables)



Combine impact result



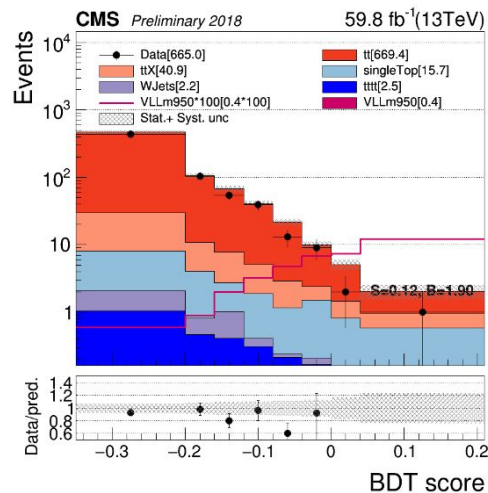
VLLm950 **best bin**(last bin: 0.04-0.2)



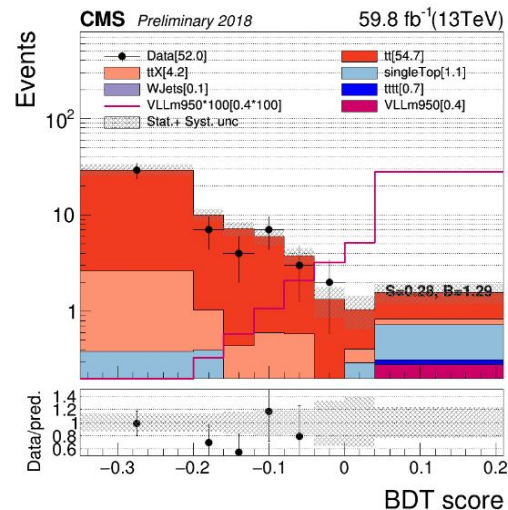
SR

limit: 5.84

significance: 0.47



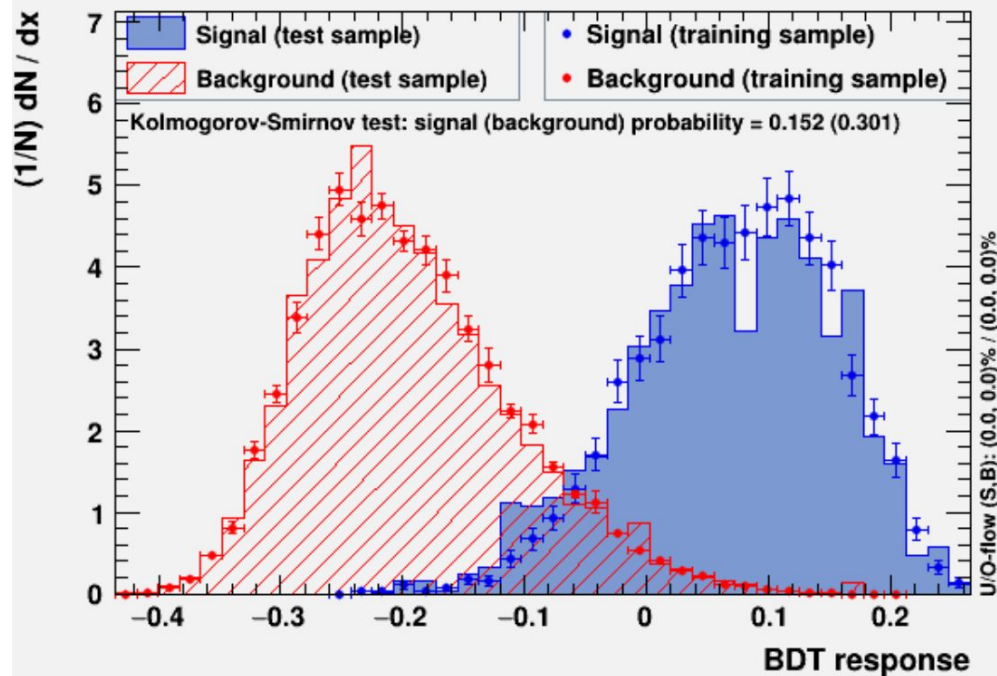
CR1



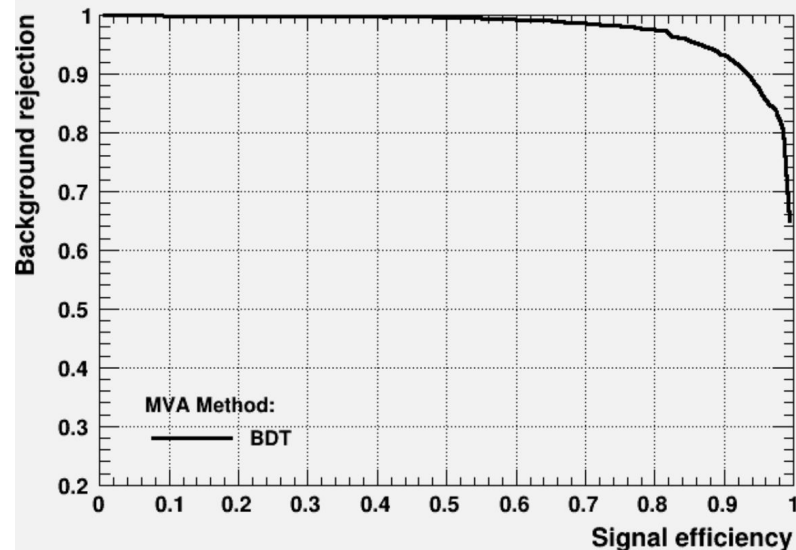
CR2

TMVA check

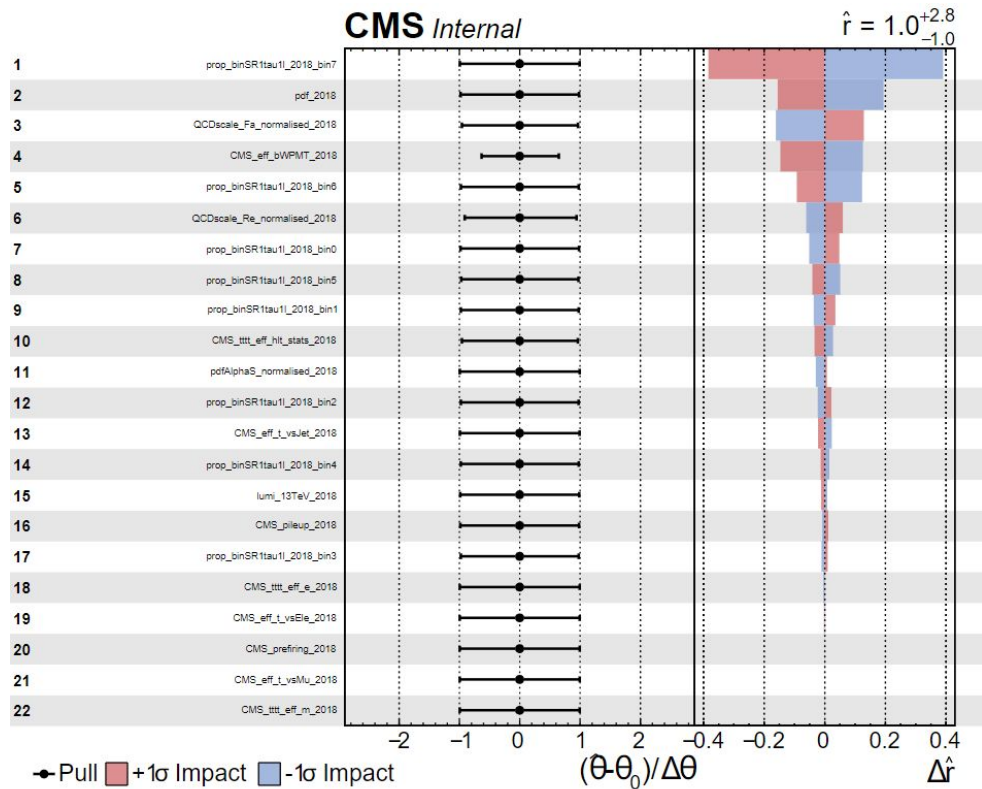
TMVA overtraining check for classifier: BDT(final25 inputs)



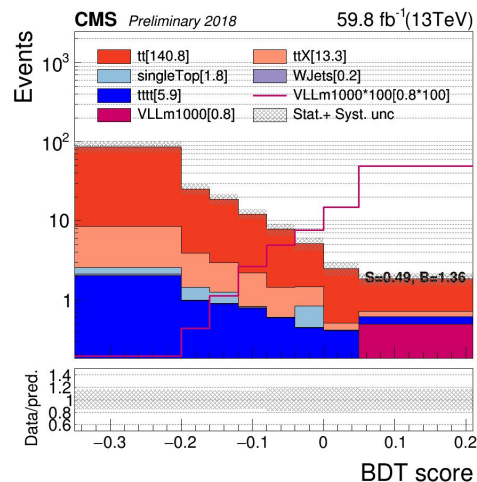
Background rejection versus Signal efficiency variables



Combine impact result



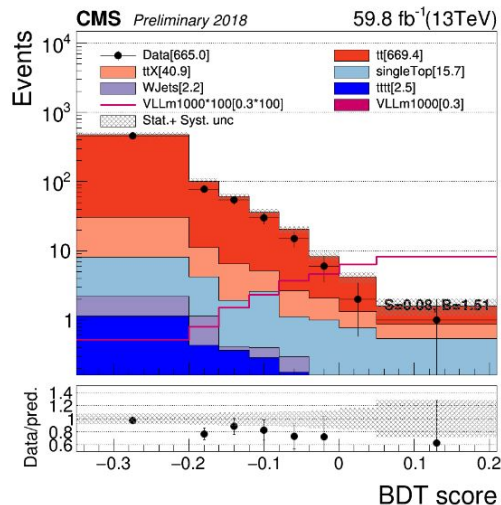
VLLm1000 best bin(last bin: 0.05-0.2)



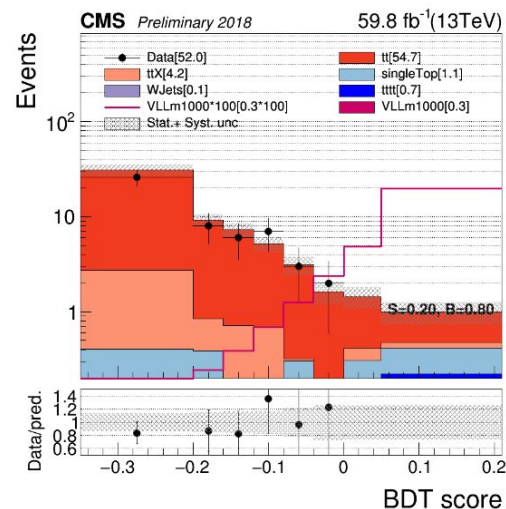
SR

limit: 7.59

significance: 0.40



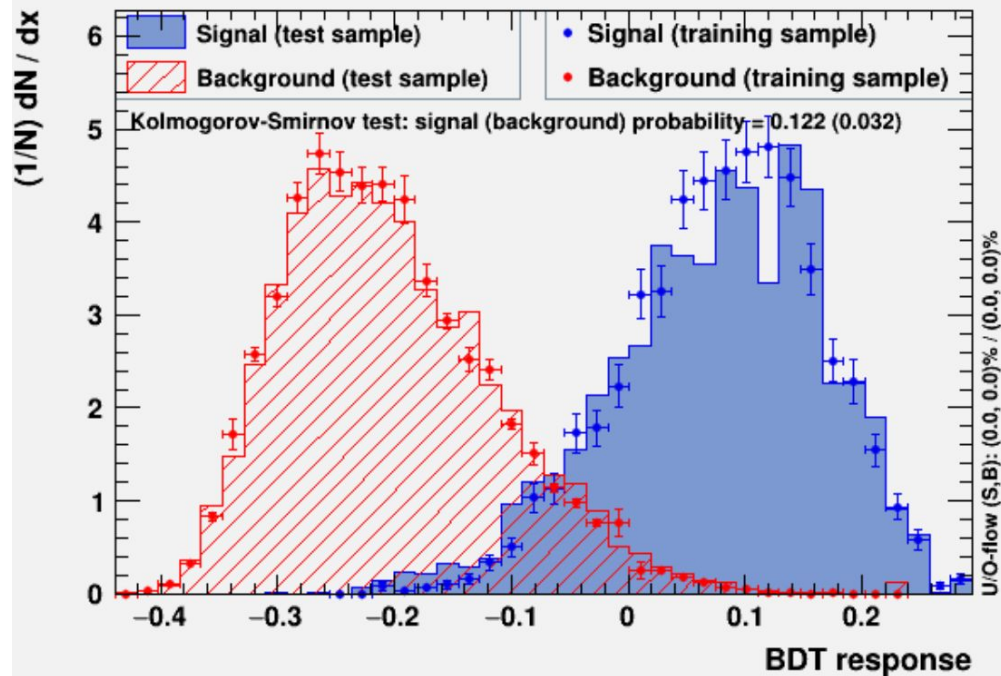
CR1



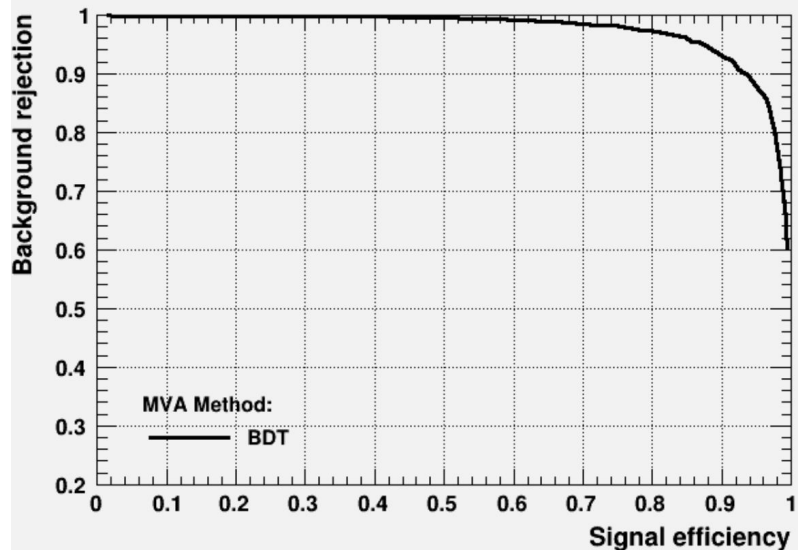
CR2

TMVA check

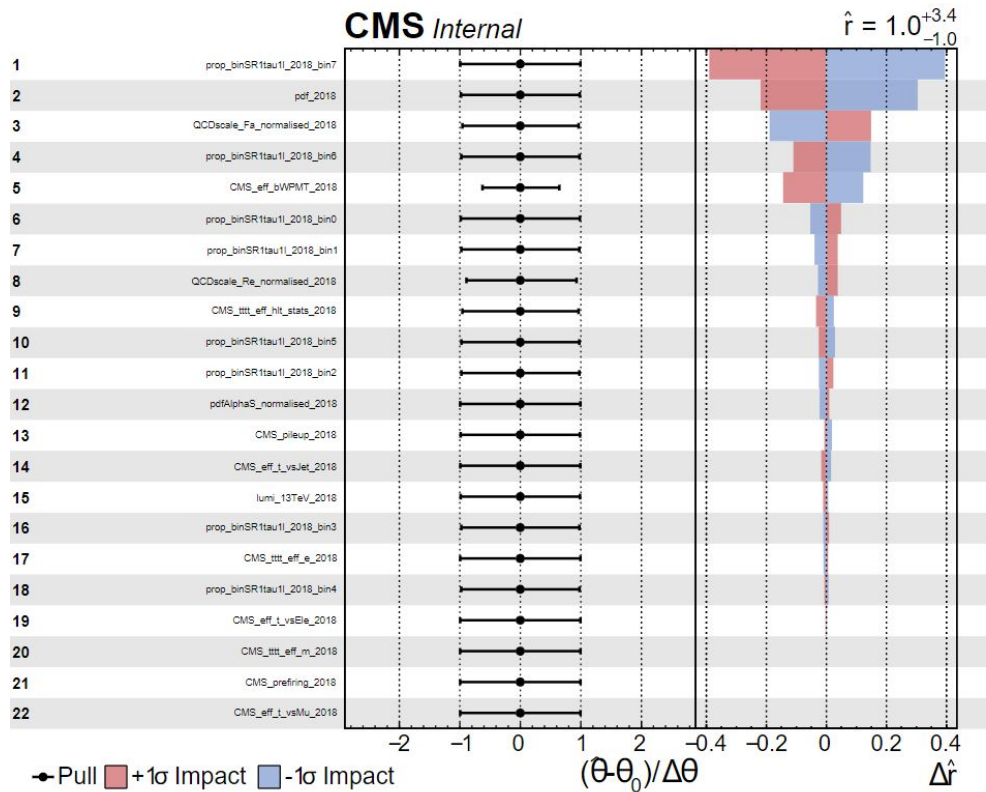
TMVA overtraining check for classifier: BDT(final25 inputs)

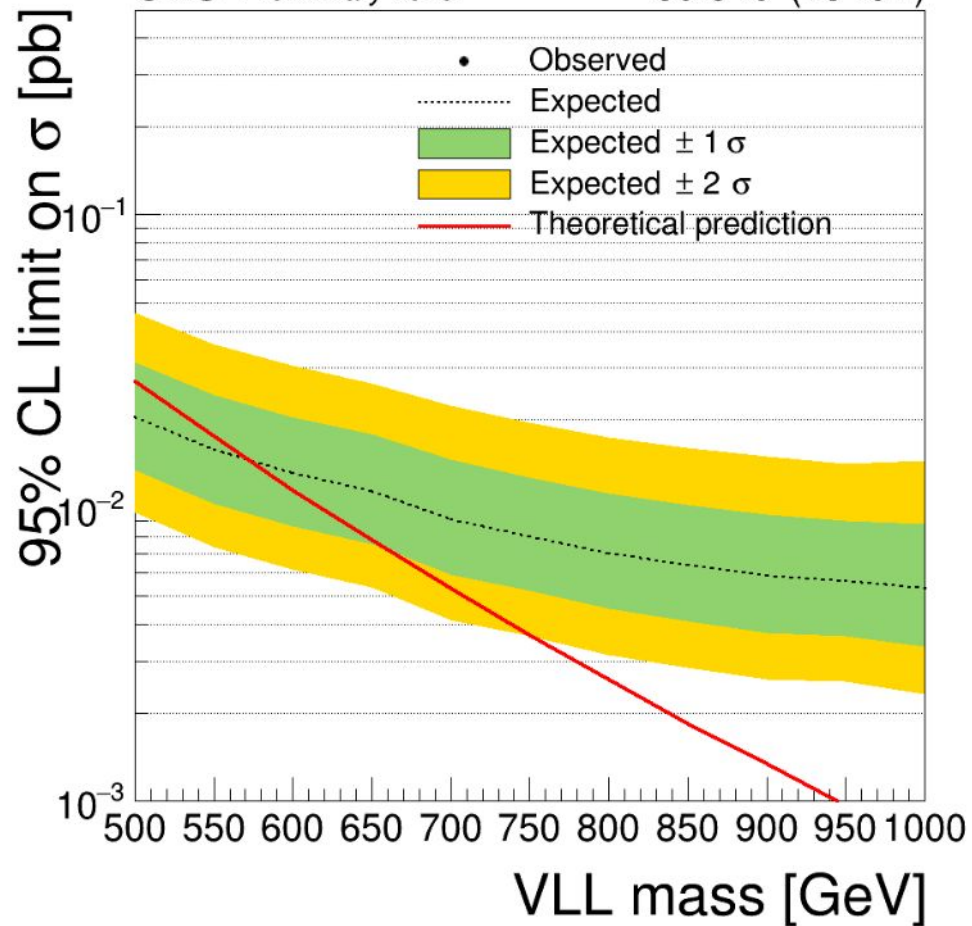


Background rejection versus Signal efficiency variables)



Combine impact result

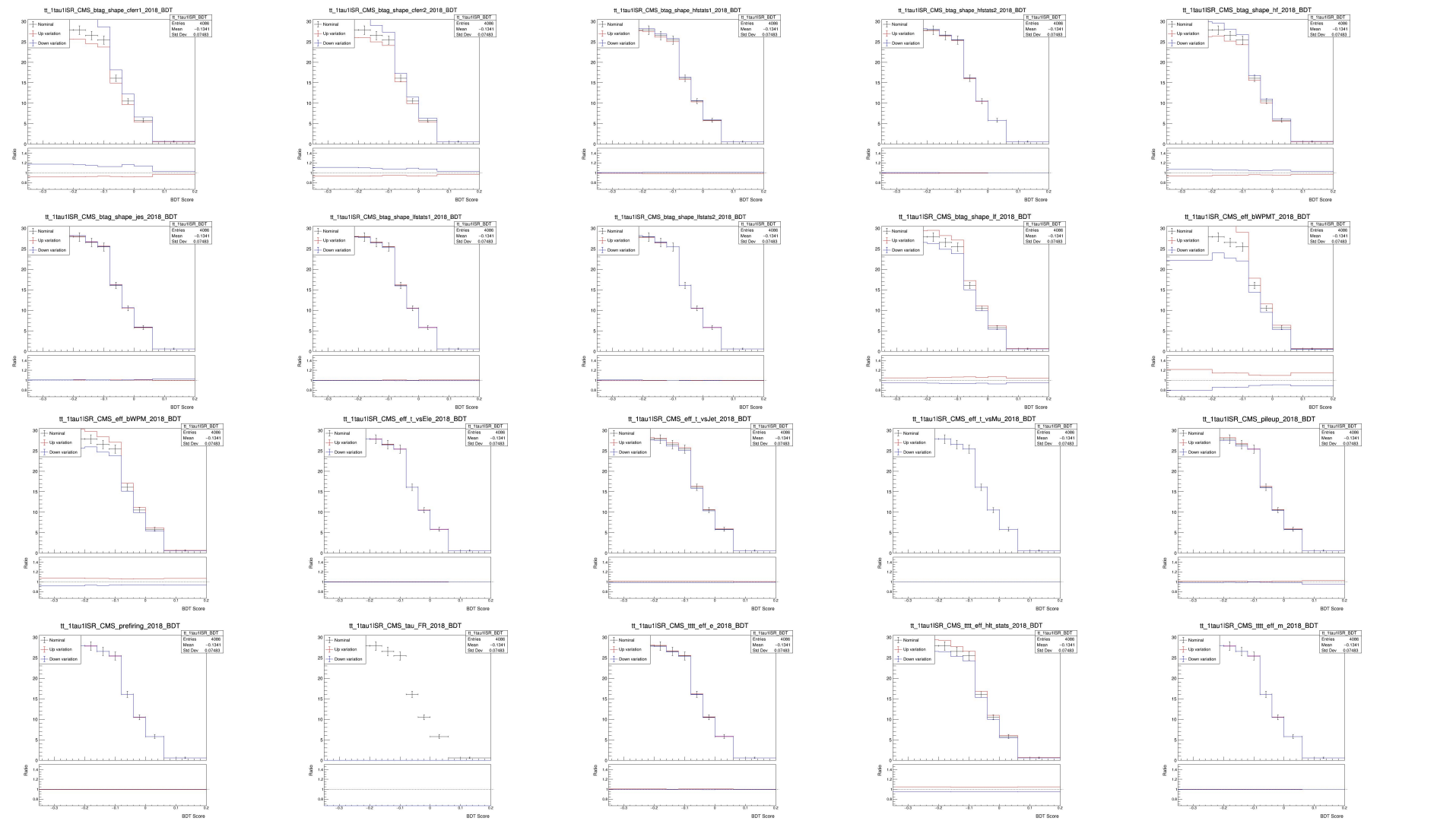




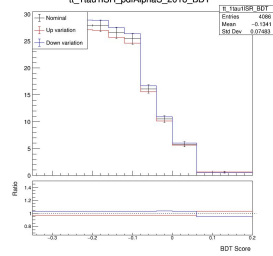
To do list:

- ❑ Samples list with VLL
- ❑ VLL sample validation: event entries...

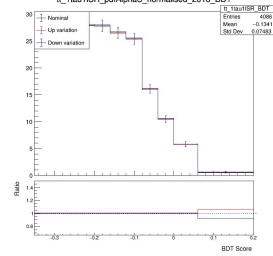
TT bar



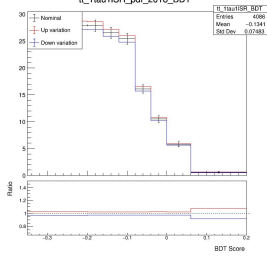
IL1tau1ISR_pdlAlphaS_2018_BDT



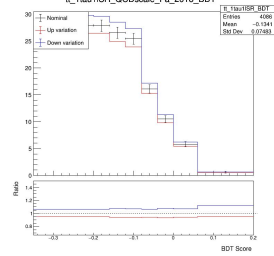
IL1tau1ISR_pdlAlphaS_normalised_2018_BDT



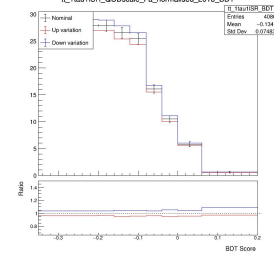
IL1tau1ISR_pdl_2018_BDT



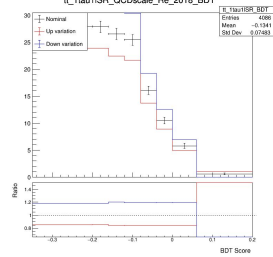
IL1tau1ISR_OCDScale_Fa_2018_BDT



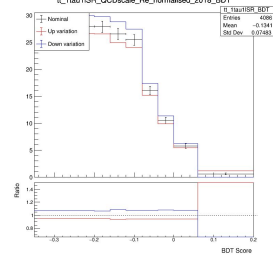
IL1tau1ISR_OCDScale_Fa_normalised_2018_BDT



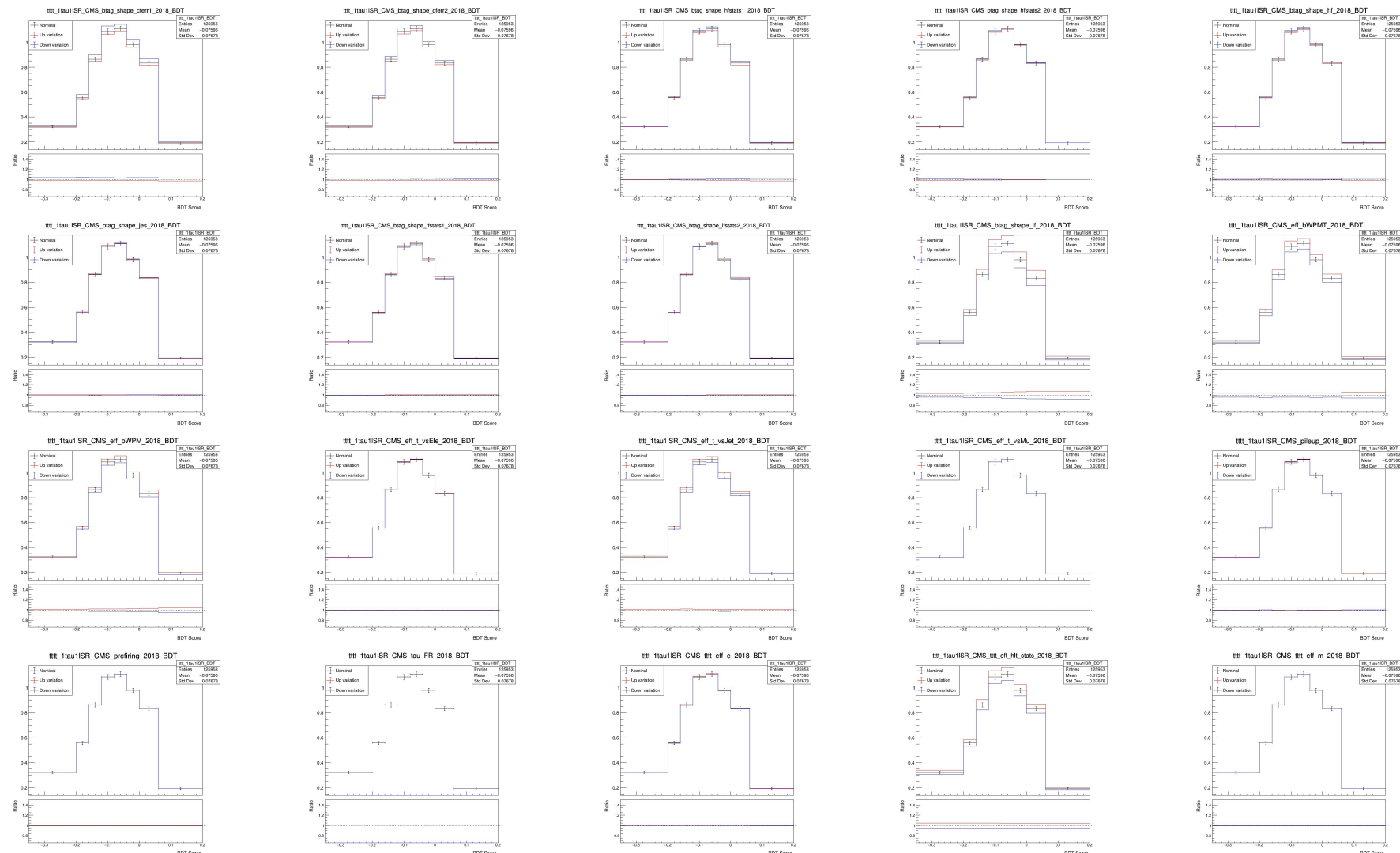
IL1tau1ISR_OCDScale_Re_2018_BDT



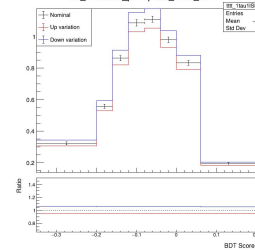
IL1tau1ISR_OCDScale_Re_normalised_2018_BDT



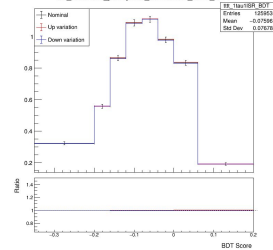
TTTT



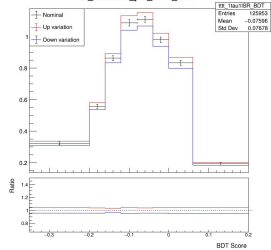
III_1tau1ISR_pdlAlphaS_2018_BDT



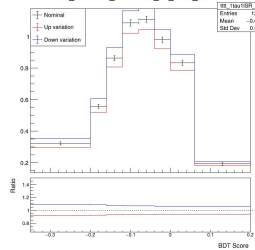
III_1tau1ISR_pdlAlphaS_normalised_2018_BDT



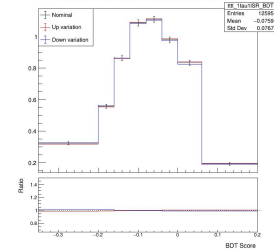
III_1tau1ISR_pdl_2018_BDT



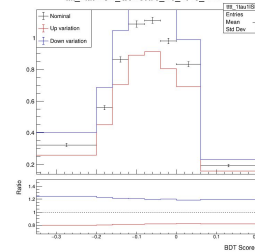
III_1tau1ISR_OCDscale_Fa_2018_BDT



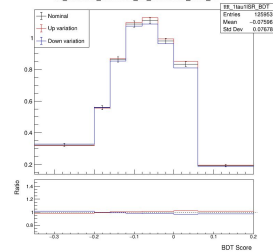
III_1tau1ISR_OCDscale_Fa_normalised_2018_BDT



III_1tau1ISR_OCDscale_Re_2018_BDT

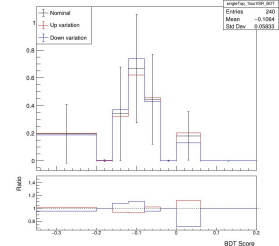


III_1tau1ISR_OCDscale_Re_normalised_2018_BDT

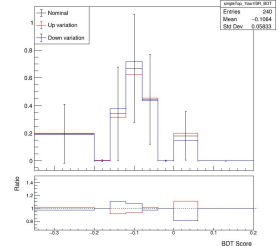


singleTop

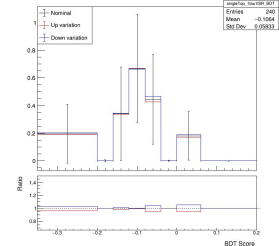
singleTop_1tau1SR_CMS_btag_shape_cflerr1_2018_BDT



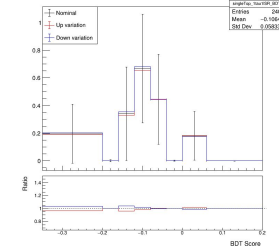
singleTop_1tau1ISR_CMS_btag_shape_cferm2_2018_BDT



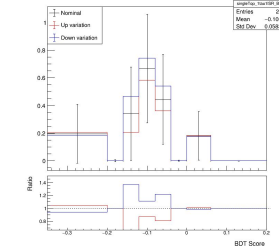
singleTop_1tau1SR_CMS_btag_shape_hfstats1_2018_BOT



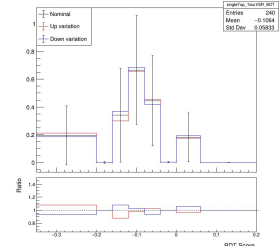
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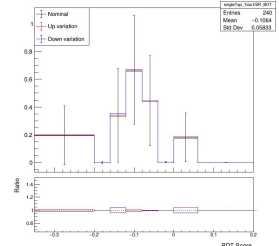
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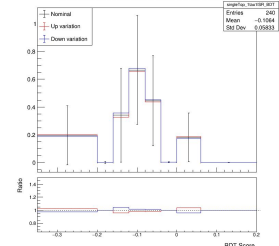
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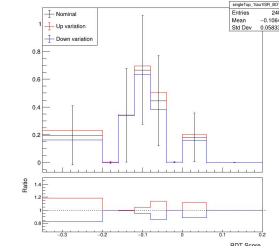
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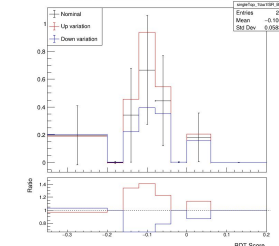
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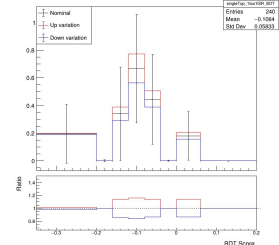
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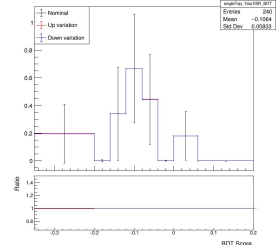
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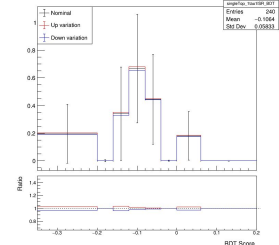
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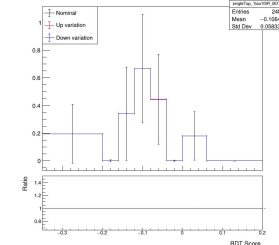
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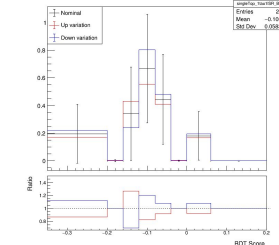
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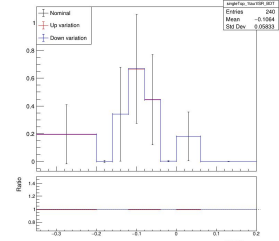
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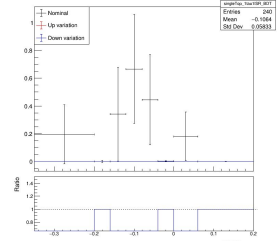
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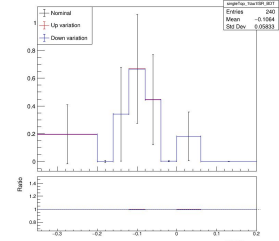
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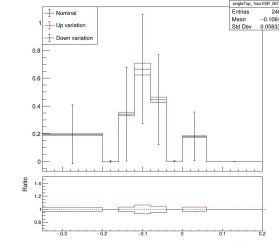
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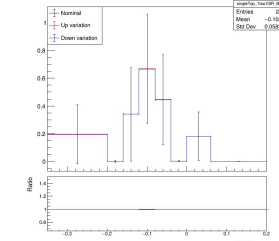
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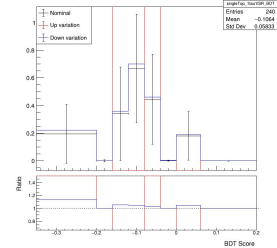
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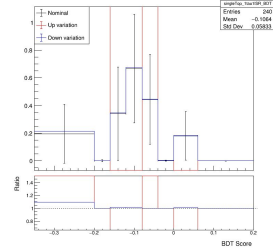
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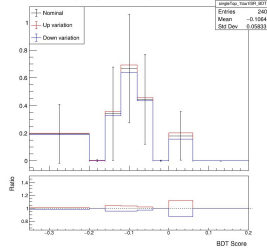
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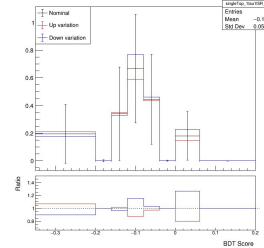
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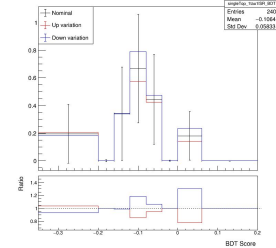
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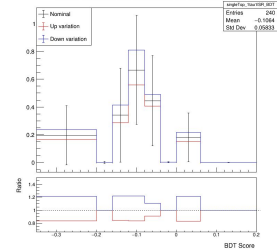
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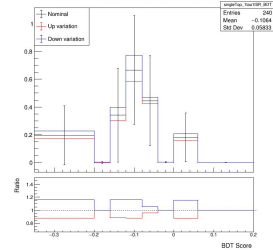
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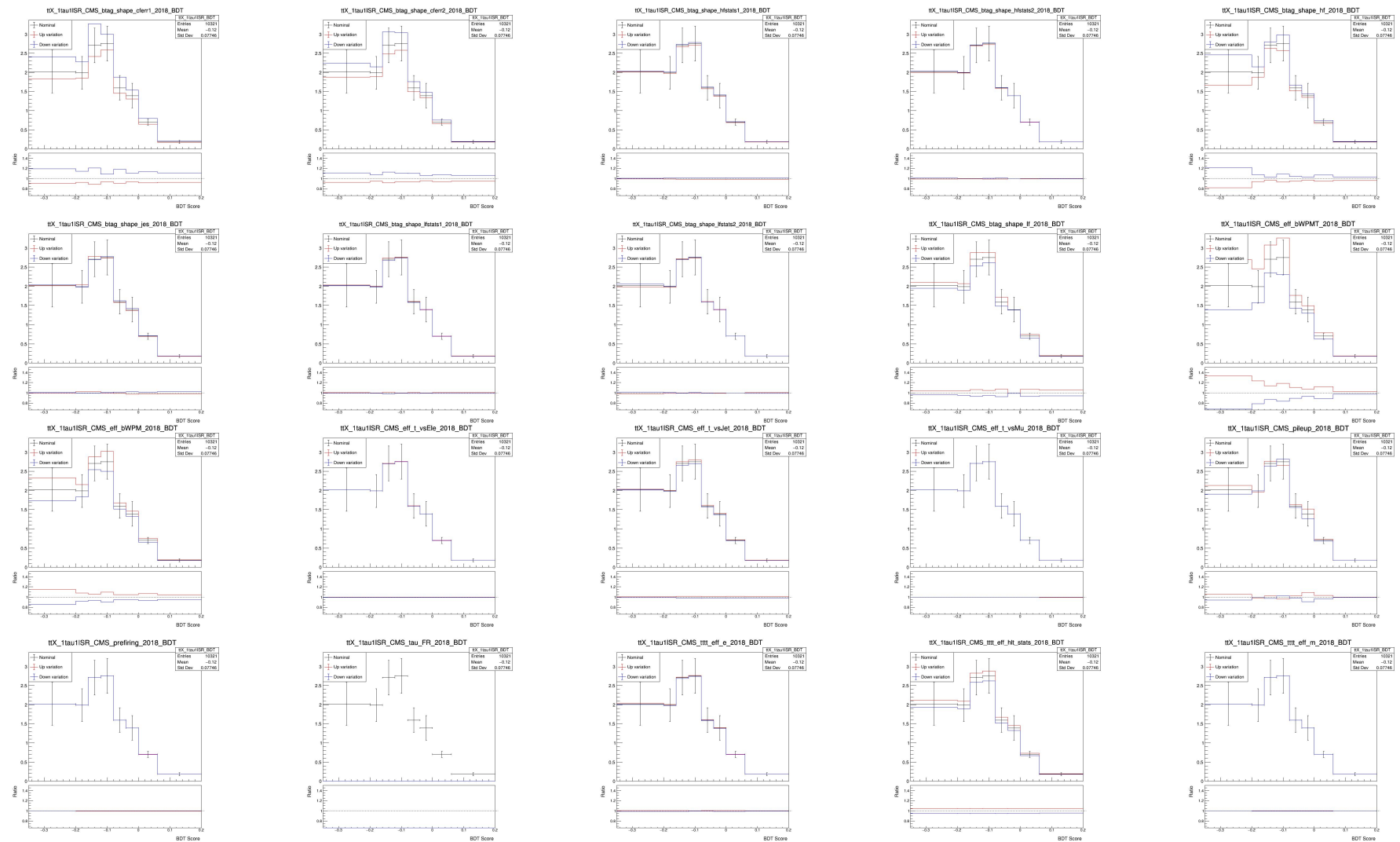
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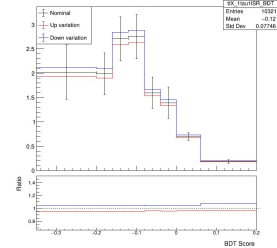
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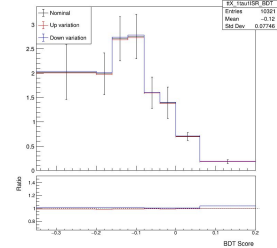
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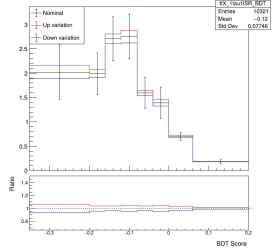
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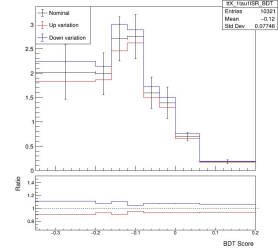
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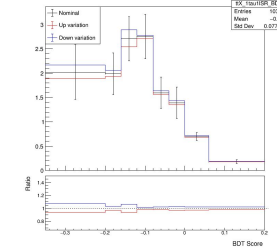
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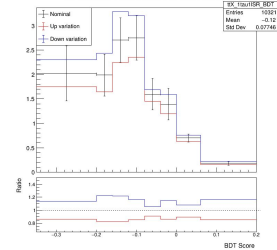
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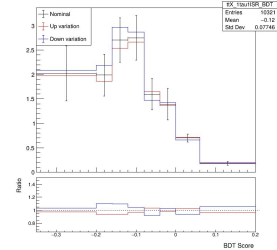
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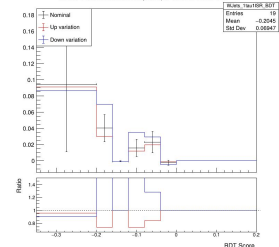


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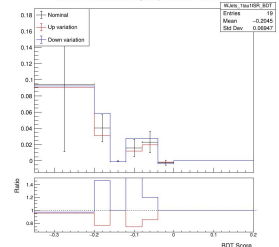


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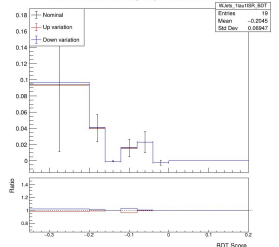
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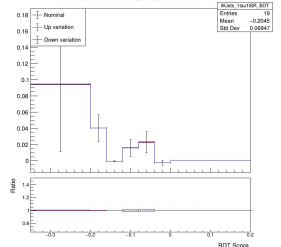
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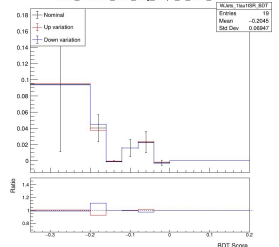
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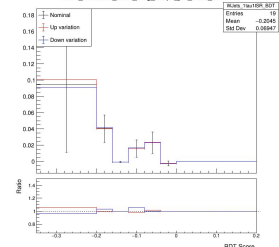
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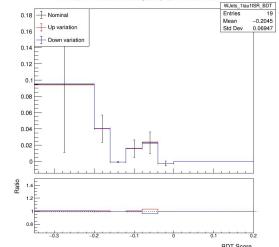
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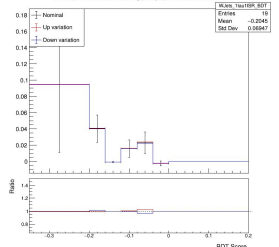
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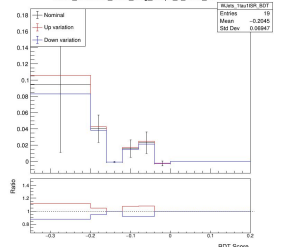
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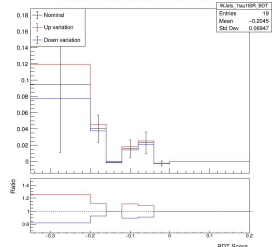
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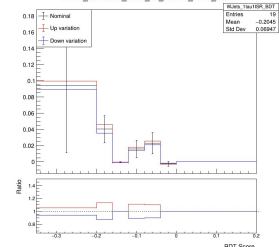
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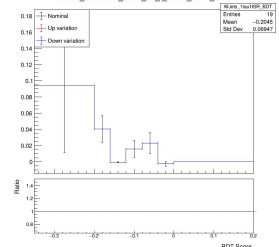
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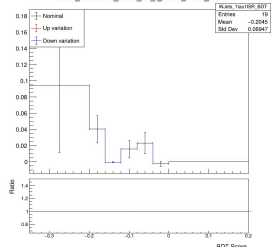
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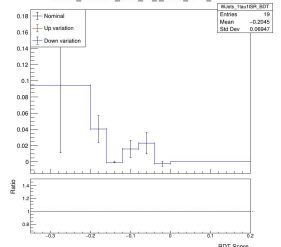
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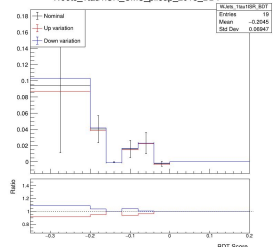
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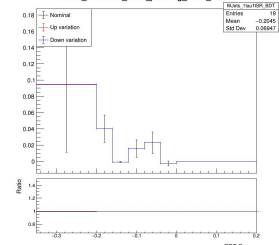
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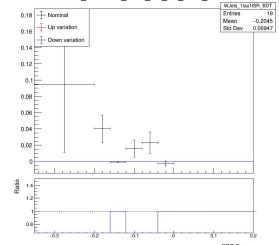
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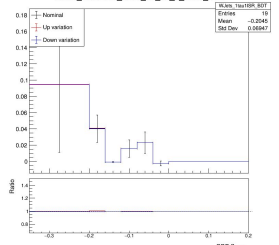
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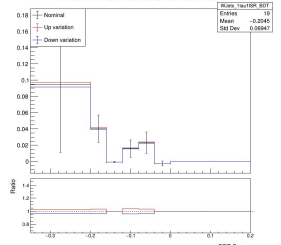
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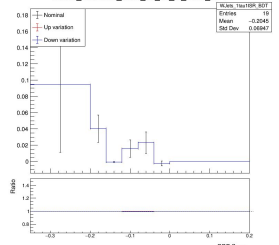
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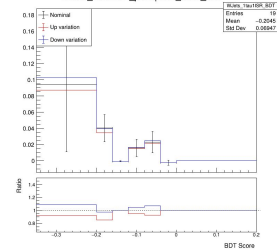
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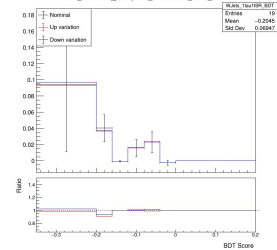
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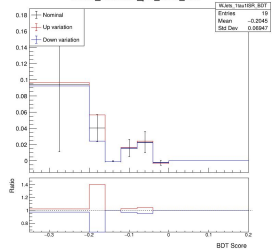
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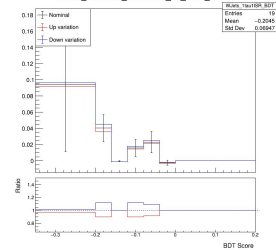
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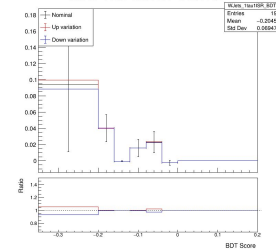
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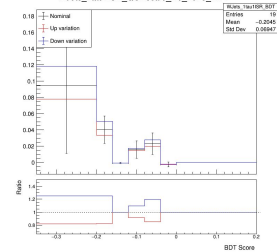
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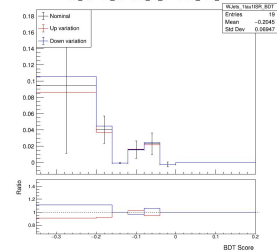
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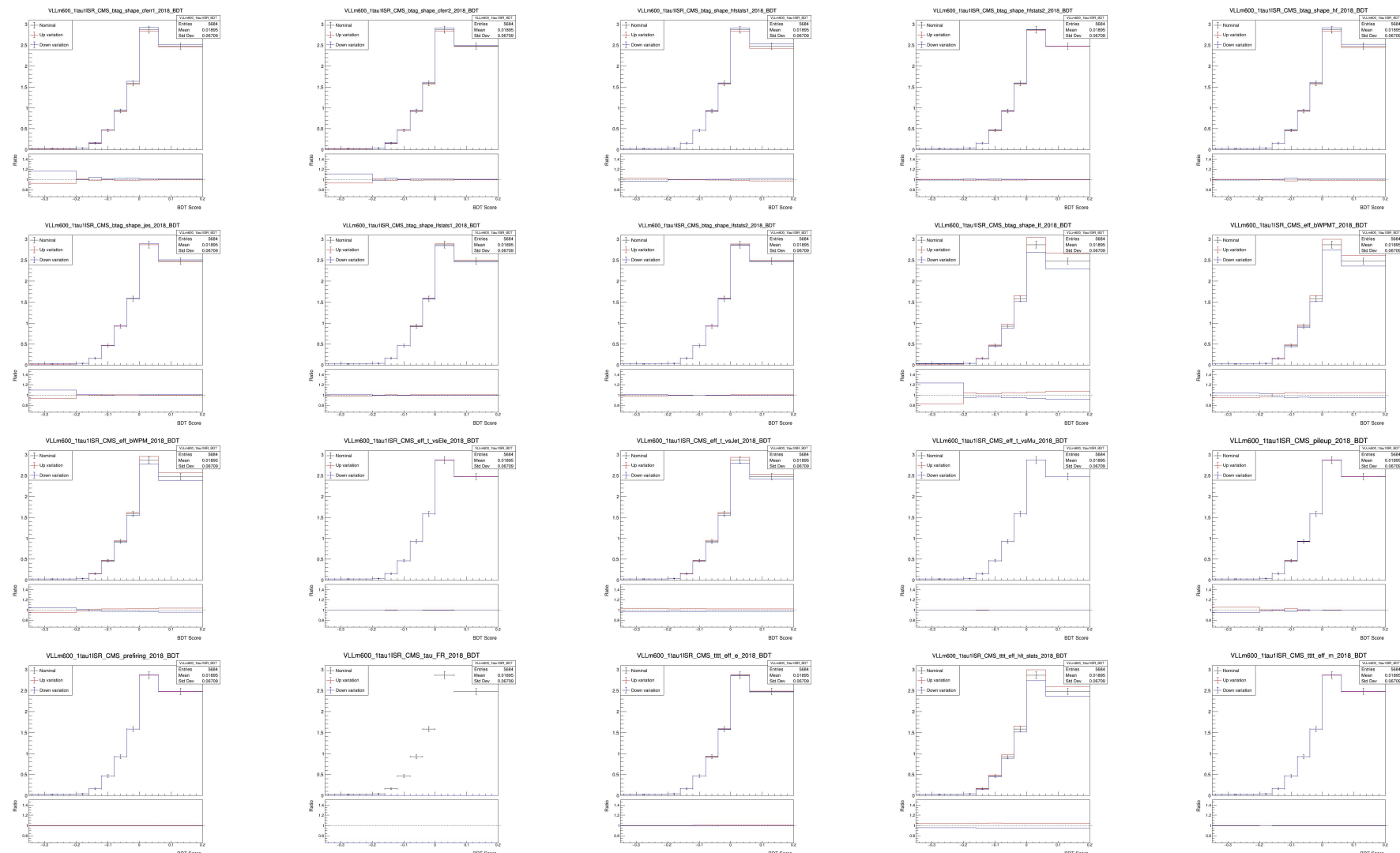
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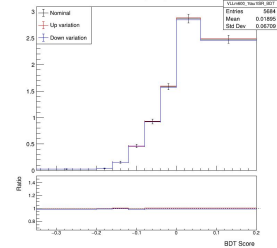
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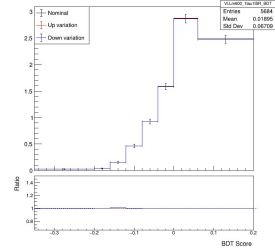
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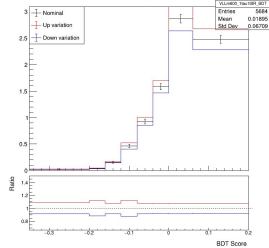
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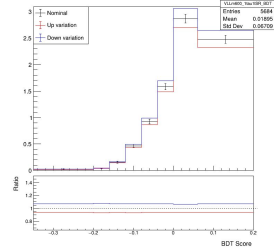
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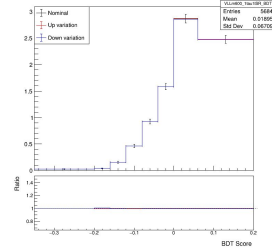
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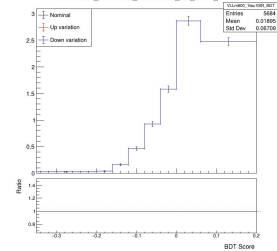
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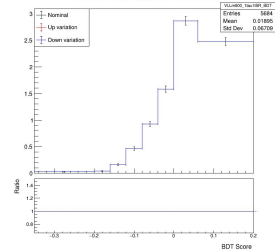
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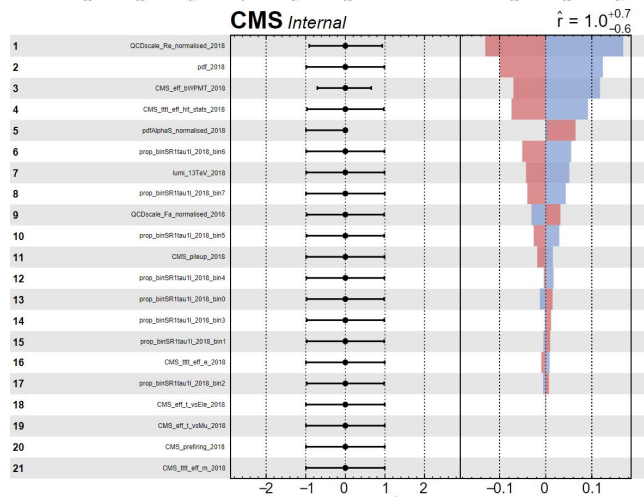
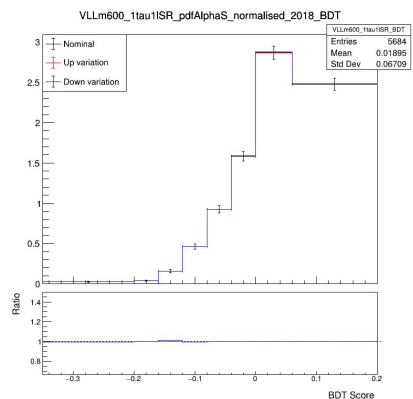
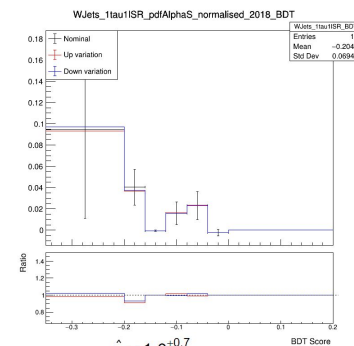
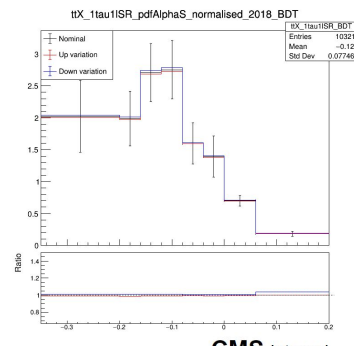
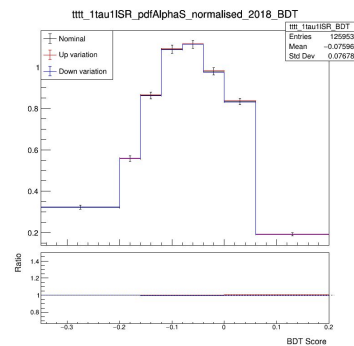
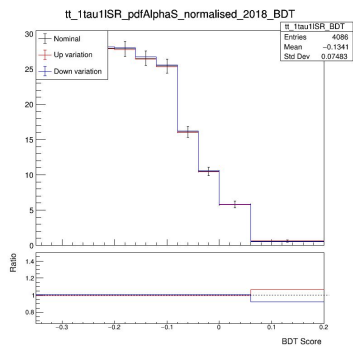
VLLm600_1tau1ISR_OCDscale_Re_2018_BDT



VLLm600_1tau1ISR_OCDscale_Re_normalised_2018_BDT



PDF_AlphaS_normalised



PDF

