

Silicon Tracker TDR 撰写工作

The CEPC Silicon Tracker consists of the Inner Silicon Tracker (ITK) and the Outer Silicon Tracker (OTK), positioned inside and outside the Time Projection Chamber (TPC), respectively. The ITK utilizes advanced CMOS sensor technology to achieve precise position measurements for accurate particle trajectory determination. In addition to position measurement, the OTK integrates AC-LGAD semiconductor detectors for precise time measurement of charged particles, significantly enhancing particle identification capabilities ...

5.1	Requirements	
5.2	Overview of ITK and OTK	
5.3	Inner silicon tracker (ITK)	
5.3.1	CMOS chip R&D	
5.3.1.1	HV-CMOS pixel R&D	
5.3.1.2	CMOS strip R&D	
5.3.2	ITK design	
5.3.3	Readout electronics	
5.3.4	Mechanical and cooling design	
5.3.5	Prospects and plan	
5.4	Outer silicon tracker (OTK) with TOF	
5.4.1	AC-LGAD sensor and ASIC R&D	
5.4.1.1	Sensor R&D	
5.4.1.2	ASIC R&D	
5.4.2	OTK design	
5.4.3	Readout electronics	
5.4.4	Mechanical and cooling design	
5.4.5	Prospects and plan	
5.5	Performance	

TRD撰写工作负责人

- 总负责：严琪
- 副总负责：李刚、李一鸣
- 其他参与人员：周扬、史欣、樊云云、赵梅、严雄波、.....

撰写计划方案和时间线

- 今天开始协商划分不同子任务。每周五组会讨论、拟定下个星期具体各子任务的划分。
- 今天分发章节latex模块。每个星期四下午5点前反馈（上传）上个星期的进度（努力完成该星期各拟定子任务的撰写）。我每周四晚上进行整理汇总。
- 周五做定期进度讨论；再拟定下个星期子任务的划分（合理分配人力在进度需要的地方）；重复持续约3个星期。
- 最后进行系统整体、阅读、讨论、修改。

下个星期的初步安排

- 5.1 Requirements
- 5.2 Overview of ITK and OTK
- 5.3 Inner silicon tracker (ITK)
 - 5.3.1 CMOS chip R&D
 - 5.3.1.1 HV-CMOS pixel R&D
 - 5.3.1.2 CMOS strip R&D
 - 5.3.2 ITK design
 - 5.3.3 Readout electronics
 - 5.3.4 Mechanical and cooling design
 - 5.3.5 Prospects and plan
- 5.4 Outer silicon tracker (OTK) with TOF
 - 5.4.1 AC-LGAD sensor and ASIC R&D
 - 5.4.1.1 Sensor R&D
 - 5.4.1.2 ASIC R&D
 - 5.4.2 OTK design
 - 5.4.3 Readout electronics
 - 5.4.4 Mechanical and cooling design
 - 5.4.5 Prospects and plan
- 5.5 Performance

- 严琪: Requirements, Overview of ITK and OTK, ITK 和 OTK 章节的 introduction, ITK design和OTK design
- 一鸣, 周扬: HV-CMOS pixel R&D。如果有额外的时间, Prospects and plan (HV-CMOS pixels)
- 史欣: CMOS strip R&D。如果有额外的时间, Prospects and plan (CMOS strips)
- 赵梅: AC-LGAD sensor R&D中sensor的发展和设计。如果有额外的时间, Prospects and plan
- 樊云云: AC-LGAD sensor R&D中的sensor的测试。如果有额外的时间, Prospects and plan
- 严雄波: AC-LGAD ASIC R&D。如果有额外的时间, Readout electronics
- 李刚: Performance