

Trigger Signal Test

Meeting @ 2.20

Question about BESIII clock:

When RUN is stopped, we lose BESIII clock. As RUN is started, we have BESIII clock

Can we check together with the FULL signal?

- Participants in the test
 - BESIII trigger team (Wen-xuan, Sheng)
 - CGEM team (Michela, Angelo)
- Test results
 - The CLOCK signal from trigger system will not be lost, regardless of whether the DAQ starts or stops collecting data.
 - The FULL signal from the CGEM Fanout module is currently unusable, possibly due to a mismatch in voltage standards between the MTI and the CGEMROC Fanout module, the signal voltage is changed when they connected.
 - Disconnected: signal high = 2.684V, signal low = 1.861V; Connected: signal high = signal low = 2.5V.

