Status at Mar 3

- Server GUFI:
 - Server DAQ is ready
 - Basic functions and acquisition have been tested
 - Acquired few runs with GUFI2 in the server with BESIII trigger
 - Acquired few runs with internal simulated trigger at high rate to test disks (see next slide)
 - Shared disks between GUFI and BESIII has been mounted
 - Prepared a procedure to define synchronization
 - Need to investigate the switch connection and configuration
- We need to verify status of the run when reset is recovered



Status at Mar 3

- Test writing to disk with internal test pulse
 - 5 kHz trigger rate, up to 2 hours, 640 hits per trigger to stress tests (twice the number of expected hits)
 - No significant packet loss (6-7 packets missing over 700M trigger)
- Looking at a 10 second run:
 - All the triggers are there
 - All the test pulses
 - Alberto is analysing the longer run (4.4 Gb binary)



Status at Mar 3

- CHECK signal:
 - Meeting between expert on both sides Friday
 - CHECK signal arrives 500 ns after the 256th trigger
 - 2 bits check signal in data
 - As requested, we would like to have a written document to keep track of the details
 - Present GEMROCs firmware send the check in the 257th trigger
 - In the past few days Angelo has finished update and run few checks, we need to flash the GEMROCs at some point and test this
 - Extended packet send logic to wait for the 500ns to send the 2 bits check in the 256th trigger in data