Contribution ID: 80 Type: not specified

## LO&CLK distribution RTM board development progress

Monday, 15 September 2025 15:30 (20 minutes)

We are developing an RTM board to generate and distribute Local and Clock signals for LLRF controller. 4 channels LO, 4 clock and 4 RF reference signals are desinged on the board for small scale applications. The PCB layout of the board has been finished, so the status of the board will be presented.

Primary authors: GAN, Nan (高能所); GAO, Wenbin (IHEP); MA, Xinpeng (IHEP); MU, Yajie (中国科学院

高能物理研究所)

Presenter: MA, Xinpeng (IHEP)
Session Classification: Session