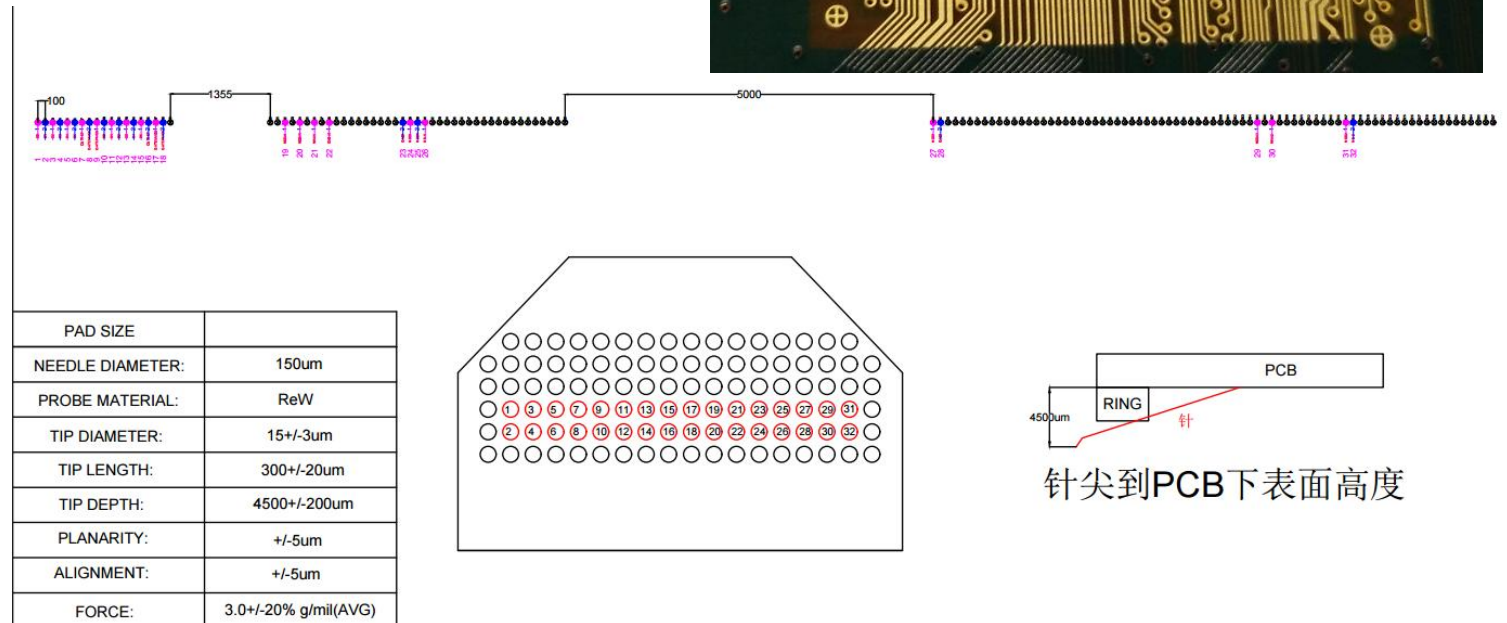
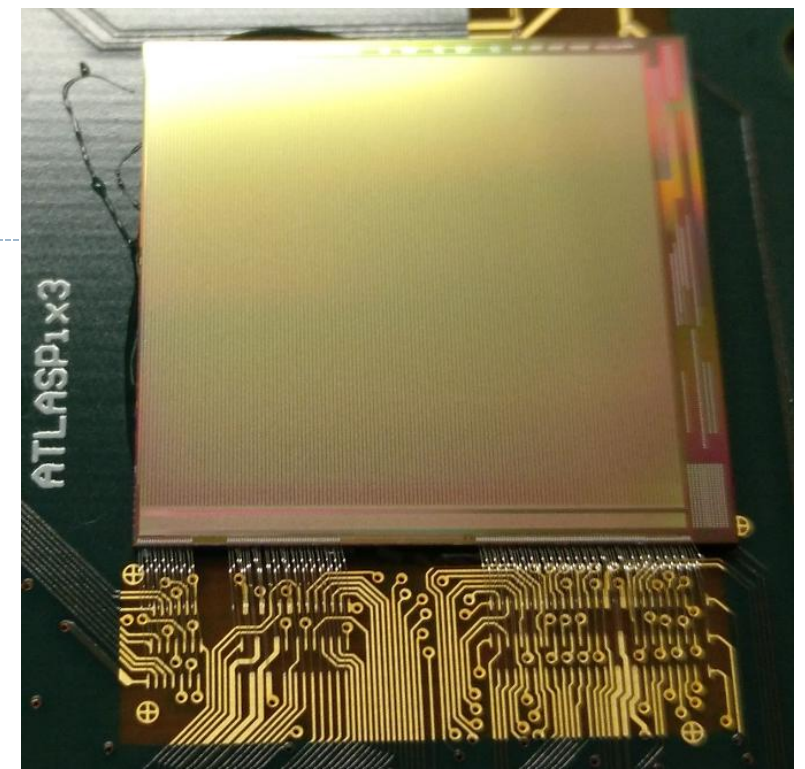




# ATLASPix Serial Powering -- First Try

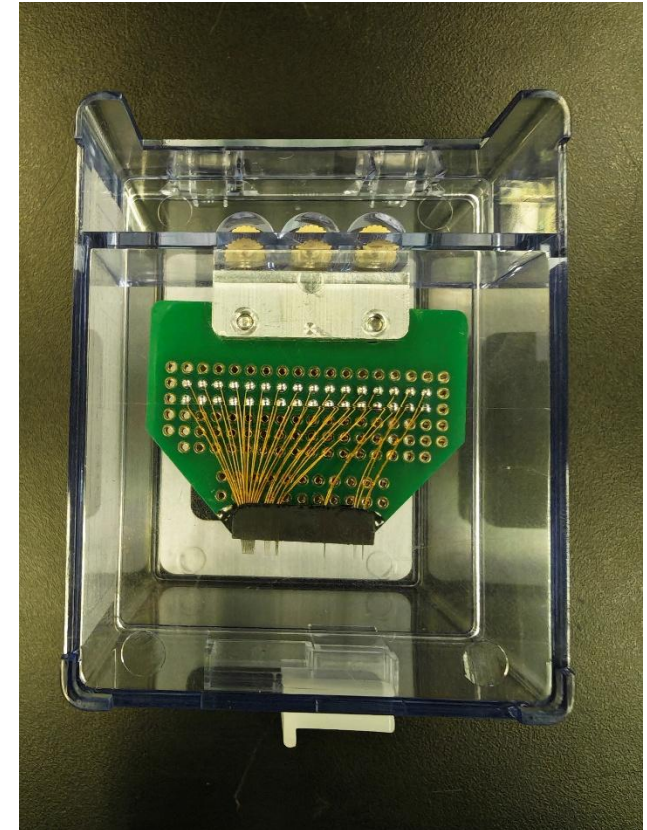
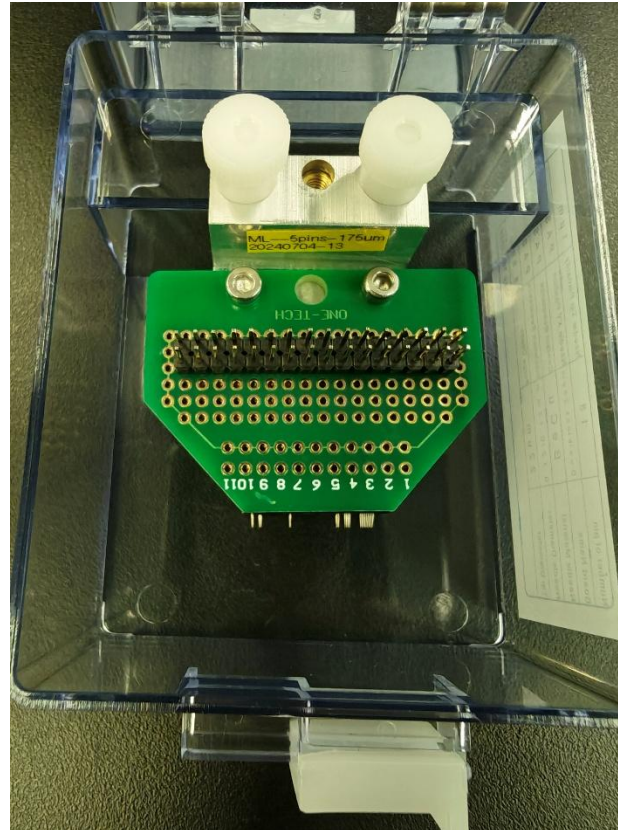
# Probe Card Design

- |              |   |       |           |
|--------------|---|-------|-----------|
| 1. b0        | } | Vdd!  | 19. vdda! |
| 2. b1        |   |       | 20. vdda1 |
| 3. b2        |   |       | 21. vina1 |
| 4. b3        |   |       | 22. gnda! |
| 5. b4        |   |       | 23. gnd!  |
| 6. b5        |   |       | 24. vdd!  |
| 7. outref    |   |       | 25. vdd1  |
| 8. gatenmos  |   |       | 26. vin1  |
| 9. gatepmos  |   |       | 27. vdd!  |
| 10. b0       | } | Vdda! | 28. gnd!  |
| 11. b1       |   |       | 29. vdda2 |
| 12. b2       |   |       | 30. vina2 |
| 13. b3       |   |       | 31. vdd2  |
| 14. b4       |   |       | 32. vin2  |
| 15. b5       |   |       |           |
| 16. outref   |   |       |           |
| 17. gatenmos |   |       |           |
| 18. gatepmos |   |       |           |



# Probe Card Production

- |              |       |           |
|--------------|-------|-----------|
| 1. b0        | Vdd!  | 19. vdda! |
| 2. b1        |       | 20. vdda1 |
| 3. b2        |       | 21. vina1 |
| 4. b3        |       | 22. gnda! |
| 5. b4        |       | 23. gnd!  |
| 6. b5        |       | 24. vdd!  |
| 7. outref    |       | 25. vdd1  |
| 8. gatenmos  |       | 26. vin1  |
| 9. gatepmos  |       | 27. vdd!  |
| 10. b0       | Vdda! | 28. gnd!  |
| 11. b1       |       | 29. vdda2 |
| 12. b2       |       | 30. vina2 |
| 13. b3       |       | 31. vdd2  |
| 14. b4       |       | 32. vin2  |
| 15. b5       |       |           |
| 16. outref   |       |           |
| 17. gatenmos |       |           |
| 18. gatepmos |       |           |



# Frist try

1. Probe Station RF arm
2. A simple version or not ???
3. Soldering wire on pins

