

# CEPC Inner Tracker: Publication (+plans)

	Title	Main Editors	Journal	Notes
1	Design and Development of MAPS-based CEPC Inner Tracker	Li, Yiming; Jiang, Xiaojie	NIM A	Review positive, Revision being prepared; On CEPC ITK general
2	An exploratory study of 55 nm HV-CMOS commercial technology for monolithic pixel sensors	Zhou, Yang; Li, Leyi; Li, Yiming	JINST 20 (2025) C10011	Published; On COFFEE2 design
3	Test of COFFEE2, the first 55 nm High Voltage CMOS sensor prototype	Xu, Zijun; Zeng, Cheng; Li, Yiming	JINST 21 (2026) C04078	Published On COFFEE2 tests
4	Irradiation study of a high voltage monolithic pixel sensor in 55 nm technology	Zhang, Hui; Shi, Tianyu	NIM A 1089 (2026) 171574	Published On COFFEE radiation study
5	Design and First Results of COFFEE3: A 55nm HVCMOS Pixel Sensor Prototype for High-Energy Physics Applications	Wei, Xiaomin; Zhou, Yang; Xu, Zijun; Li, Yiming	JINST	Accepted; on first COFFEE3 design & tests
6	Development of a 55 nm HV-CMOS Pixel Sensor Prototype with In-Pixel TDC for High-Hit-Density Tracking	Zhou, Yang; Li, Leyi	NIM A	Submitted; on detailed COFFEE3 left array design
7	基于55纳米制程高压CMOS的硅像素传感器芯片研究	Li, Yiming; Xu, Zijun; Zhou, Yang	原子能科学技术	Submission ~ Sep 2026
8	Crosstalk and Charge Collection at ATLASPix3.1	Velthuis, Jaap	tbc	Draft ready, to submit
9	HVCMOS-based serial powering study	Gao, Yanyan; Andreatza, Attilio; Ustuner, Fuat	tbc	Draft being planned