

# Outer Silicon Tracker Publications

	Title	Authors	Journal	Timeline
1	First prototype of a low-power timing chip for strip LGAD readout: LATRICO	C. Wang, X. Li, Y. Bai, H. Chen, J. Chen, Y. Deng, W. Huang, H. Jiang, H. Li, Z. Liang, L. Liu, X. Wang, Z. Wang, Q. Wu, Q. Yan, X. Yan, J. Ye, L. Zhang, G. Zhang, and Q. Zhu	JINST	2026.4.16
2	Design and Performance of Strip LGAD Sensor Prototypes	J. Chen, Y. Cui, G. Li, X. Shi, J. Teoh, Q. Yan, X. Yan, X. Zhang, Y. Zhang, M. Zhao, and Z. Zhao	TBD	This year
3	Development of a High-Precision Automated Testing System for Silicon Sensors	J. Chen, Y. Cui, G. Li, X. Shi, J. Teoh, Q. Yan, X. Yan, X. Zhang, Y. Zhang, and M. Zhao	TBD	This year
4	Performance of the Integrated LATRIC ASIC and LGAD Sensors	J. Chen, Y. Cui, G. Li, X. Shi, J. Teoh, C. Wang, Q. Yan, X. Yan, X. Zhang, Y. Zhang, M. Zhao, and Z. Zhao	TBD	This year
5	Development of a RISC-V System-on-Chip for High-Energy Physics (HERIS)	J. Chen, Y. Cui, S. Luo, X. Shi, J. Teoh, Q. Yan, X. Yan, L. Zhang, X. Zhang, and Y. Zhang	TBD	This year
6	First Multipurpose CO <sub>2</sub> Cooling System for the Silicon Tracker (MECOOL)	Z. Chang, X. Jing, G. Li, X. Qian, J. Teoh, Q. Yan, J. Yang, Y. Yue, and Z. Zhang	TBD	This year

