



- ❖ **JadePix**
 - TJ-180, Own MPW JadePix-5 terminated
- ❖ **TaichuPix**
 - TJ-180, MPW TaichuPix-3 completed
- ❖ TaichuPix+JadePix, with stitching and full wafer
 - TJ-180, Full Mask Halted due to multiple reasons
- ❖ VTX chip (to-be-named)
 - H-90, Full Mask It provides epi-layer wafer and supports stitching
- ❖ **COFFEE**
 - S-55, MPW, COFFEE-1, LL COFFEE 2+3, HV/LR
 - Planned to seek for high resistivity wafer opportunity when the design is mature enough
- ❖ **CHiR (COFFEE with High Resistivity)**
 - C-55, MPW First delivery starts in July
- ❖ JadePix & TaichuPix designs use small electrode thus can have smaller pixel size. The design benefits from epi-layer, which some foundries could not provide. We are also exploring other possibilities to have the same technology & foundry for both VTX & ITK