

LHC**CLHCP2026****Friday, 30 October 2026****Parallel 5 : Upgrade 2 - □□□□ (16:20 - 18:20)****-Conveners:**

time	[id] title	presenter
16:20	[767] A lightweight low-cost silicon micro-strip system for testbeam facility and cosmic-ray detection	□□
16:35	[768] UP bare stave and Cooling system design	□□□
16:50	[769] Performance study of MOSAIX chips for the ALICE ITS3 detector upgrade	□□□
17:05	[770] Design and Characterization of ER2 65 nm APTS Prototypes for the ALICE ITS3 Upgrade	QIAOMU TONG
17:20	[771] ATLAS HGTD module assembly at IHEP	□□□
17:35	[772] Progress of ATLAS HGTD LGAD Sensors: Production and Quality-Control Status	CHENGXIN LIAO
17:50	[773] ATLAS High-Granularity Timing Detector: results from hybrids and modules in test beam (online)	KHURAM TARIQ
18:05	[774] Quality Control of ATLAS HGTD Support Units	XIAOTIAN MA