

IEEE NPSS Real Time Conference 2009

Tuesday, 12 May 2009

FESP1 Front-End Signal Processing 1 (08:30 - 10:10)

time	[id]	title	presenter
08:30	[34]	FESP1-1, Ultra Fast Analog and Timing CMOS Digitizers	G. S. VARNER
08:55	[35]	FESP1-2, A Design for Large-Area Fast Photodectors with Transmission-Line Readout and Waveform Sampling	J.-F. C. GENAT
09:20	[36]	FESP1-3, Pico-Second Timing with Micro-Channel Plate Devices and Waveform Sampling Readout Electronics	F. TANG
09:45	[37]	FESP1-4, The Design and Initial Testing of the Beam Phase and Energy Test System for DTL in the Proton Accelerator of CSNS	L. ZHAO