



## IEEE NPSS Real Time Conference 2009

Tuesday, 12 May 2009

FESP2 Front-End Signal Processing II (11:00 - 12:40)

time	[id]	title	presenter
11:00	[67]	FESP2-1, FPGA Based Front End Instrumentation for Mariachi Experiment	S. S. JUNNARKAR
11:20	[68]	FESP2-2, A Serial Link Transmitter in MAPS	Q. SUN
11:40	[69]	FESP2-3, Readout Electronics and Data Acquisition System of MicroBooNE Experiment	H. CHEN
12:00	[70]	FESP2-4, A Readout Electronics System for GEM Detectors	Z. DENG
12:20	[71]	FESP2-5, Compact SiPM based Detector Module for Time-of-Flight PET/MR	M. RITZERT