



## IEEE NPSS Real Time Conference 2009

Monday, May 11, 2009

PS1A Poster Short Oral 1A (3:40 PM - 4:10 PM)

time	[id]	title	presenter
3:40 PM	[201]	CMSP-3, Emulating the GLink Chip-Set with FPGA Serial Transceivers in the ATLAS Level-1 Muon Trigger	R. GIORDANO
3:43 PM	[202]	CMSP-5, Receiver Assistant Congestion Control in High Speed and Lossy Networks	K. SHI
3:46 PM	[203]	CMSP-6, Characterizing Jitter Performance of Multi Gigabit FPGA-Embedded Serial Transceivers	R. GIORDANO
3:49 PM	[204]	CMSP-8, Handling Online Information in the LHCb Experiment	M. D. C. BARANDELA
3:52 PM	[205]	CMSP-10, A Unified Condition and Configuration Database for the End-Cap Muon Trigger System of the ATLAS Detector at the LHC	D. LELLOUCH
3:55 PM	[206]	CMSP-12, Comparing the Performance of EPICS Channel Access with a New Implementation Based on ICE (the Internet Communications Engine) *	S. XU
3:58 PM	[208]	CMSP-23, Precise Time Protocol Assessment on a Prototype System	A. LUCHETTA
4:01 PM	[209]	CMSP-17, A New Concept for Experiment Program Planning for the Wendelstein 7-X Experiment	J. SCHACHT
4:04 PM	[210]	CMSP-26, New Developments with the RatCAP PET Tomograph	M. L. PURSCHKE