

IEEE NPSS Real Time Conference 2009

Monday, 11 May 2009

PS1A Poster Short Oral 1A (15:40 - 16:10)

time	[id] title	presenter
15:40	[201] CMSP-3, Emulating the GLink Chip-Set with FPGA Serial Transceivers in the ATLAS Level-1 Muon Trigger	R. GIORDANO
15:43	[202] CMSP-5, Receiver Assistant Congestion Control in High Speed and Lossy Networks	K. SHI
15:46	[203] CMSP-6, Characterizing Jitter Performance of Multi Gigabit FPGA-Embedded Serial Transceivers	R. GIORDANO
15:49	[204] CMSP-8, Handling Online Information in the LHCb Experiment	M. D. C. BARANDELA
15:52	[205] CMSP-10, A Unified Condition and Configuration Database for the End-Cap Muon Trigger System of the ATLAS Detector at the LHC	D. LELLOUCH
15:55	[206] CMSP-12, Comparing the Performance of EPICS Channel Access with a New Implementation Based on ICE (the Internet Communications Engine) *	S. XU
15:58	[208] CMSP-23, Precise Time Protocol Assessment on a Prototype System	A. LUCHETTA
16:01	[209] CMSP-17, A New Concept for Experiment Program Planning for the Wendelstein 7-X Experiment	J. SCHACHT
16:04	[210] CMSP-26, New Developments with the RatCAP PET Tomograph	M. L. PURSCHKE