

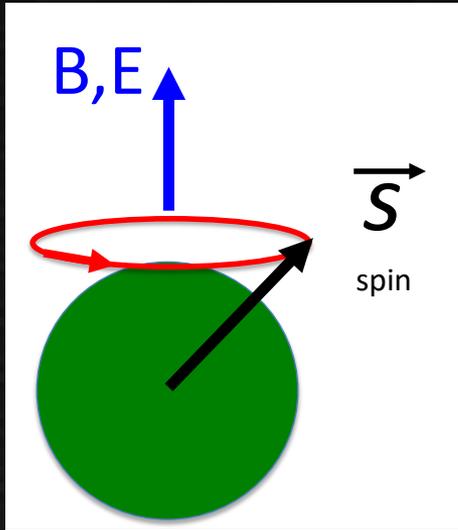
# Silicon Tracker for J-PARC muon $g-2$ /EDM experiment

Taikan Suehara  
(Kyushu University)

for the J-PARC  $g-2$ /EDM (E34) collaboration

# Muon g-2/EDM

Spin precession



$$\mathcal{H} = -\vec{\mu} \cdot \vec{B} - \vec{d} \cdot \vec{E}$$

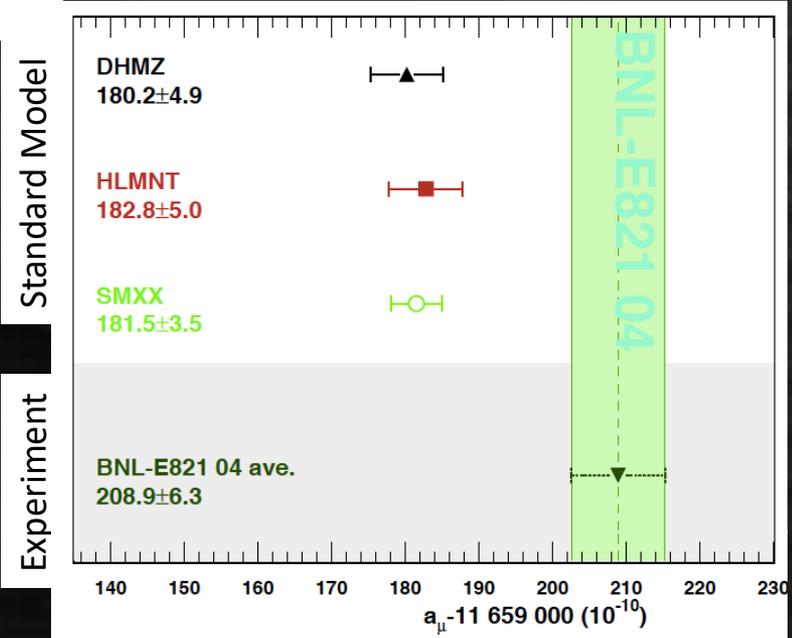
$$\vec{\mu} = g \left( \frac{q}{2m} \right) \vec{s}$$

$$g = 2(1 + a_\mu)$$

QED, QCD, weak  
(+BSM) correction

Magnetic dipole moment

BNL E821 exp. (2004)  
3.3  $\sigma$  deviation to SM calculation



$$\vec{d} = \eta \left( \frac{q}{2mc} \right) \vec{s}$$

Electric dipole moment

SM:  $\sim 2 \times 10^{-38}$  e cm, lepton T violation term

# Muon g-2: Fermilab vs J-PARC

In uniform magnetic field, muon spin rotates ahead of momentum due to  $g-2 \neq 0$

general form of spin precession vector:

$$\vec{\omega} = -\frac{e}{m} \left[ a_{\mu} \vec{B} - \left( a_{\mu} - \frac{1}{\gamma^2 - 1} \right) \frac{\vec{\beta} \times \vec{E}}{c} + \frac{\eta}{2} \left( \vec{\beta} \times \vec{B} + \frac{\vec{E}}{c} \right) \right]$$

BNL E821 approach  
 $\gamma=30$  ( $P=3$  GeV/c)

$$\vec{\omega} = -\frac{e}{m} \left[ a_{\mu} \vec{B} + \frac{\eta}{2} \left( \vec{\beta} \times \vec{B} + \frac{\vec{E}}{c} \right) \right]$$

FNAL E989

J-PARC approach  
 $E = 0$  at any  $\gamma$

$$\vec{\omega} = -\frac{e}{m} \left[ a_{\mu} \vec{B} + \frac{\eta}{2} (\vec{\beta} \times \vec{B}) \right]$$

J-PARC E34

$$\vec{\omega} = -\frac{e}{m} \left[ a_\mu \vec{B} + \frac{\eta}{2} \left( \vec{\beta} \times \vec{B} + \frac{\vec{E}}{c} \right) \right]$$

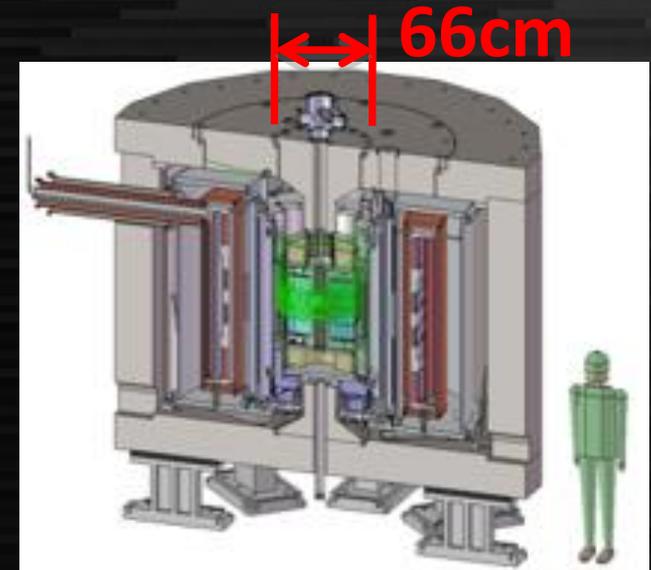
FNAL E989

$$\vec{\omega} = -\frac{e}{m} \left[ a_\mu \vec{B} + \frac{\eta}{2} (\vec{\beta} \times \vec{B}) \right]$$

J-PARC E34



**P= 3.1 GeV/c , B=1.45 T**



**P= 0.3 GeV/c , B=3.0 T**

J-PARC E34 will be a compact and independent experiment complementary to FNAL E989 (independent systematics)

# J-PARC muon $g-2$ /EDM (E34) Collaboration

144 members, 51 institutions from  
Canada, Czech, France, Korea, Japan, Russia, UK, US  
Official collaboration recently formed (Spokesperson: T. Mibe)



J-PARC Facility  
(KEK/JAEA)

LINAC

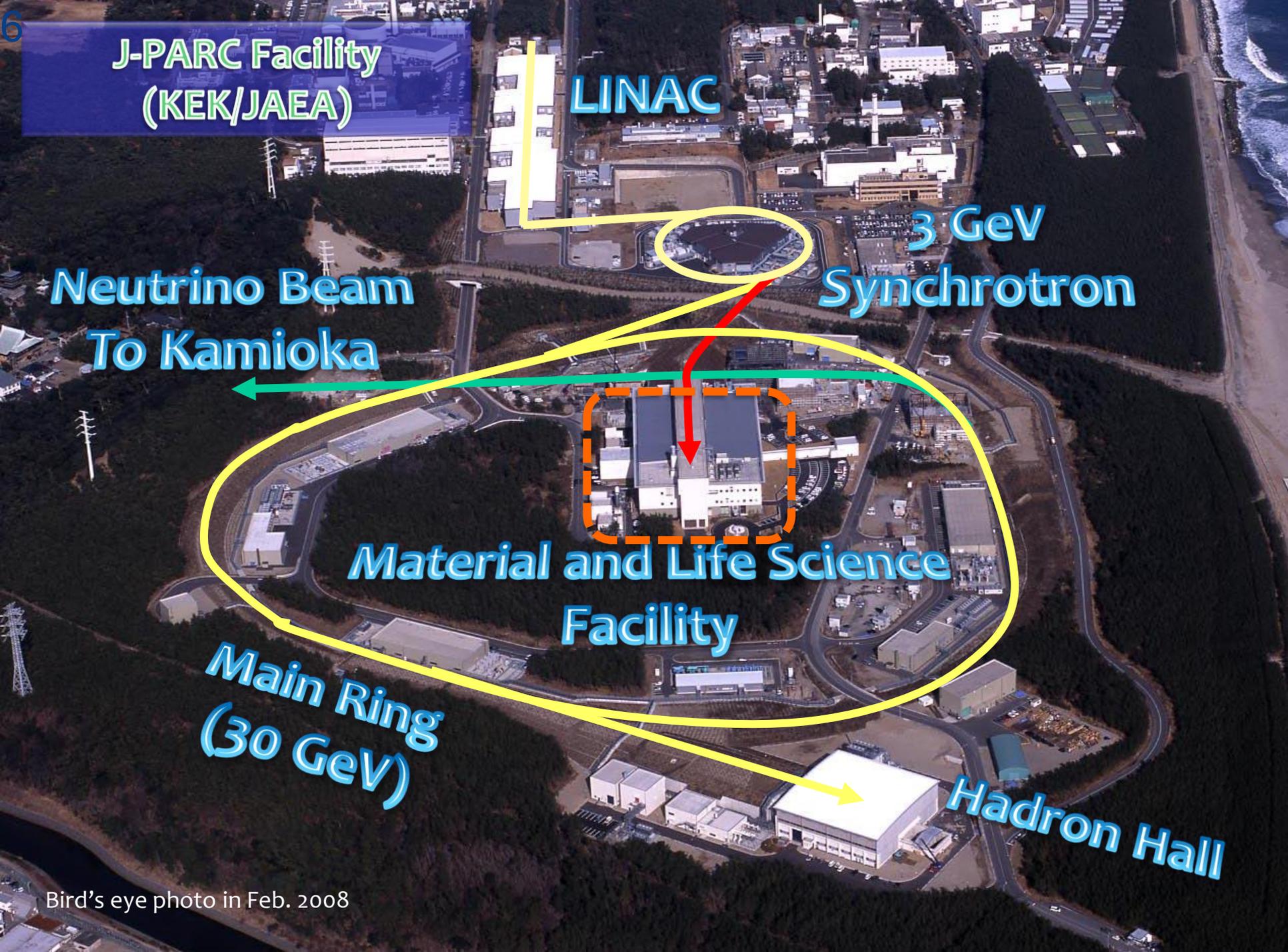
3 GeV  
Synchrotron

Neutrino Beam  
To Kamioka

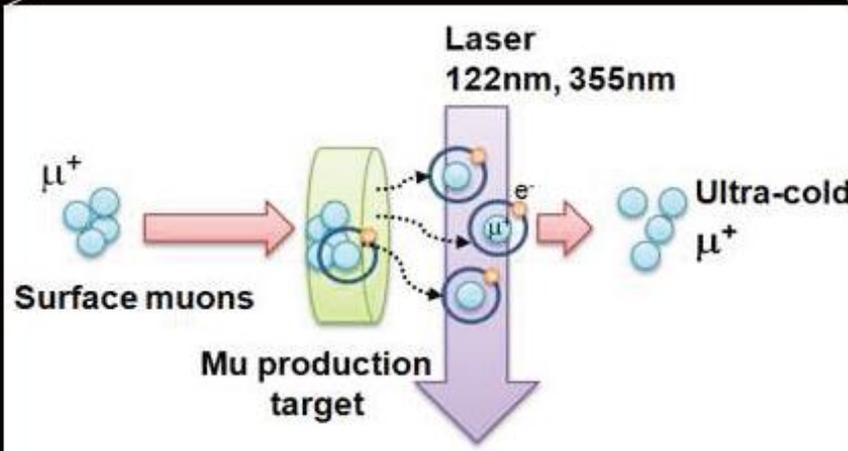
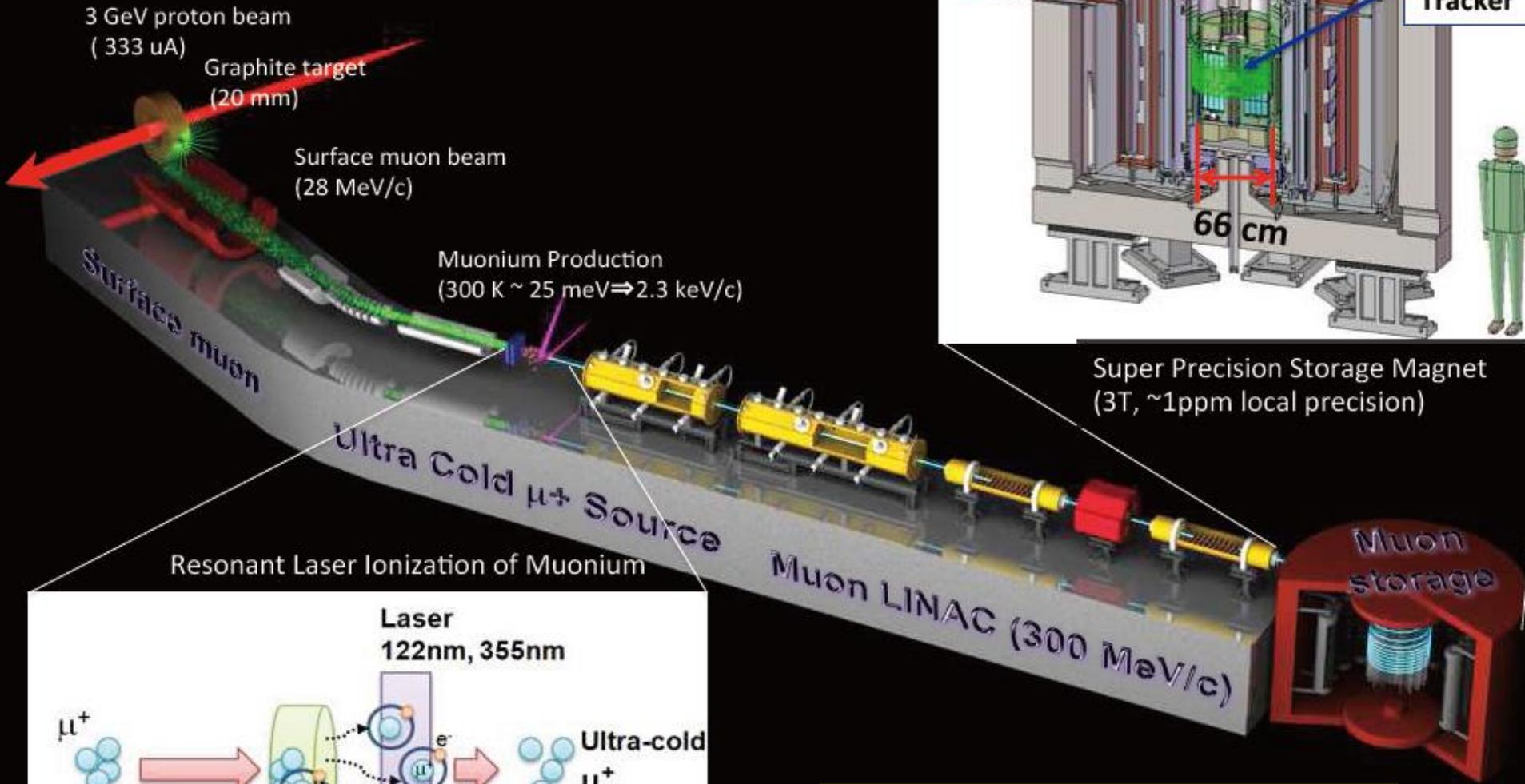
Material and Life Science  
Facility

Main Ring  
(30 GeV)

Hadron Hall

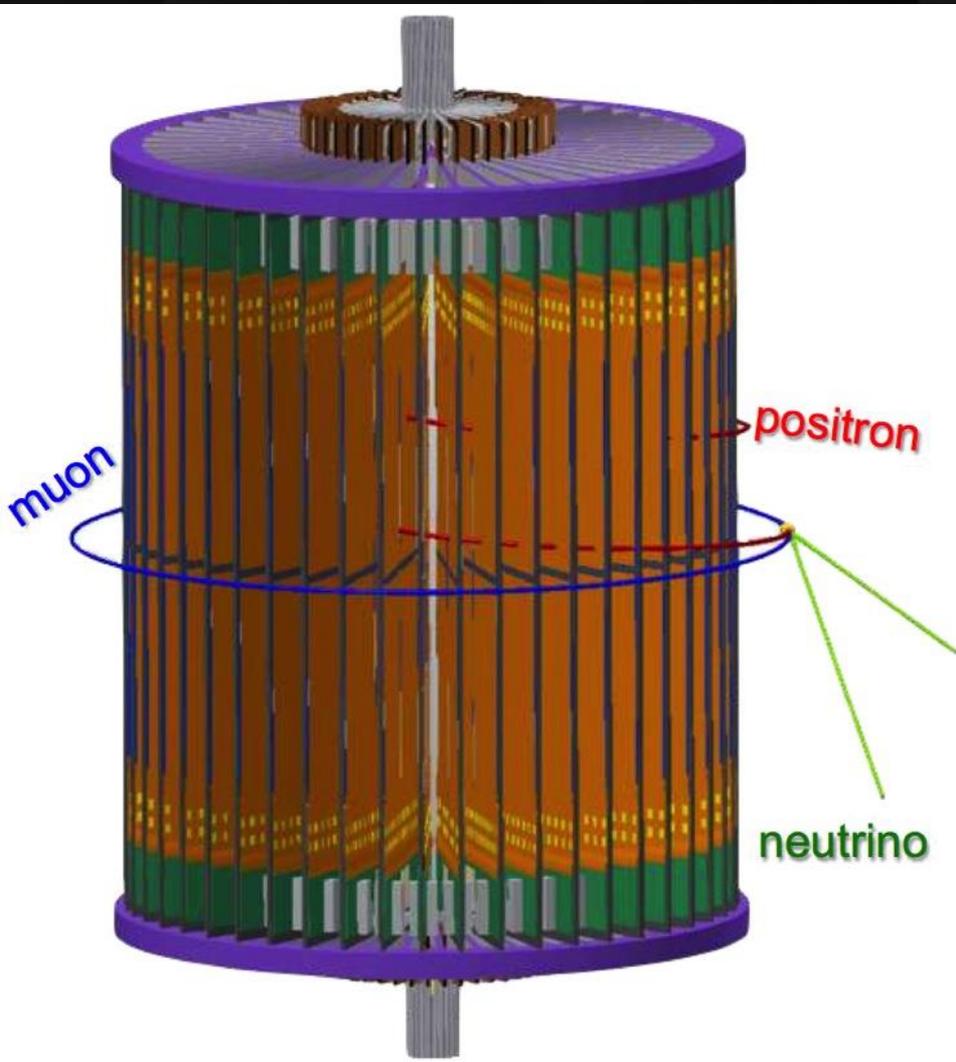


# Grand layout

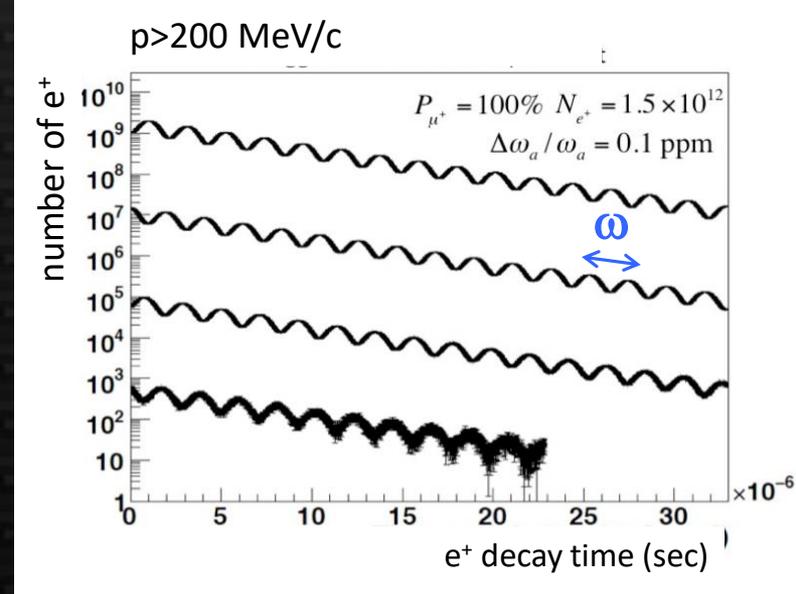


$$\Delta(g-2) = 0.1\text{ppm}$$
$$\text{EDM} \sim 10^{-21} \text{ e}\cdot\text{cm}$$

# Positron Tracker



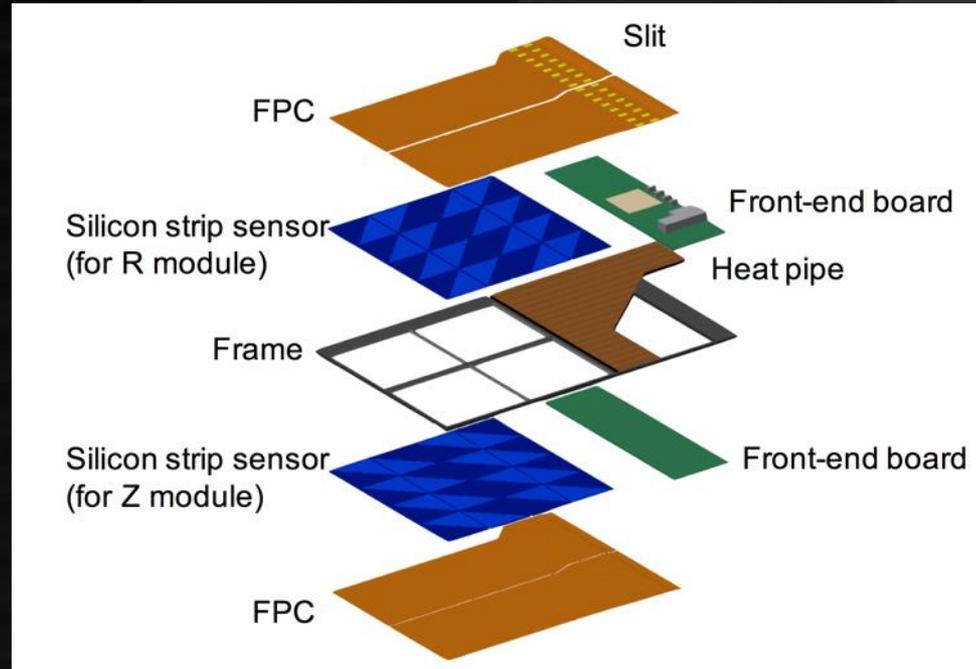
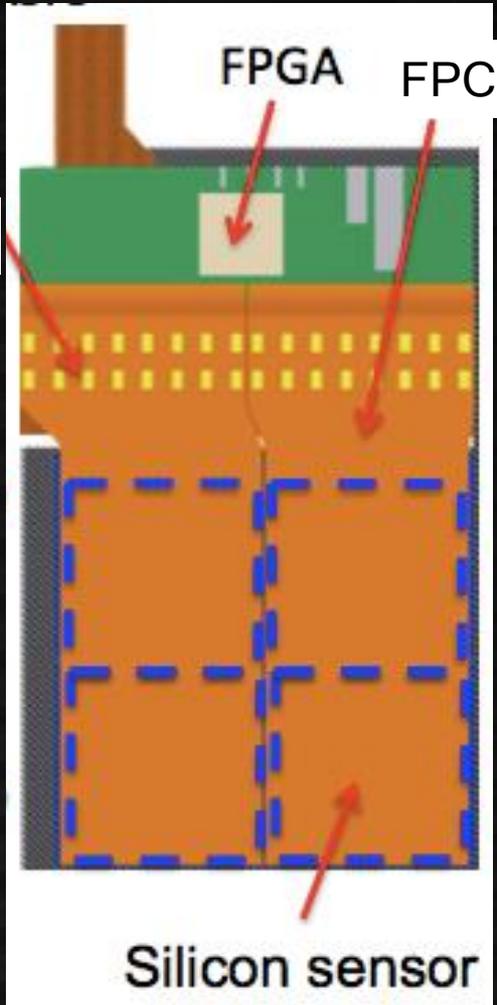
48 layers (vanes)



## Requirements:

- 0.1 ppm freq. measurement
- 40  $\mu\text{s}$  live time
- Event rate: 1400-10 kHz /strip
- Tracking of 100-300 MeV positrons
- 3 Tesla magnetic field
- 10  $\mu\text{rad}$  angular alignment (for EDM measurement)

# Vane structure



4 x 2 (R/Z) SSSD per half-vane  
32 x 2 ASICs (128 ch / ASIC)

Vacuum operation (liquid cooling of ASICs)  
~16 vanes (1/3 of current design)

Half-vane structure funded by JSPS (2015-19)

# Silicon Sensor



Produced by Hamamatsu

- Size: 97.28 x 97.28 mm
  - Thickness: 320  $\mu\text{m}$
  - Full depletion V:  $\sim 60\text{V}$
  - Strip pitch 190  $\mu\text{m}$
  - Strips: 512 x 2 (split in half)
  - Double metal for readout pads
- $\sim 200$  sensors produced



500  $\mu\text{m}$  (inactive gap)

WB Pads for Z sensors

WB Pads for R sensors

Double metal structure

Polysilicon resistance

AC pad

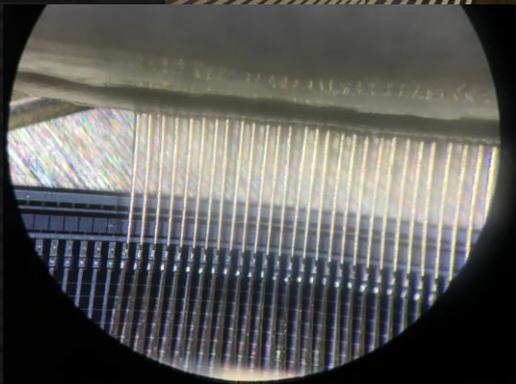
Alignment mark

# Characteristics of sensor

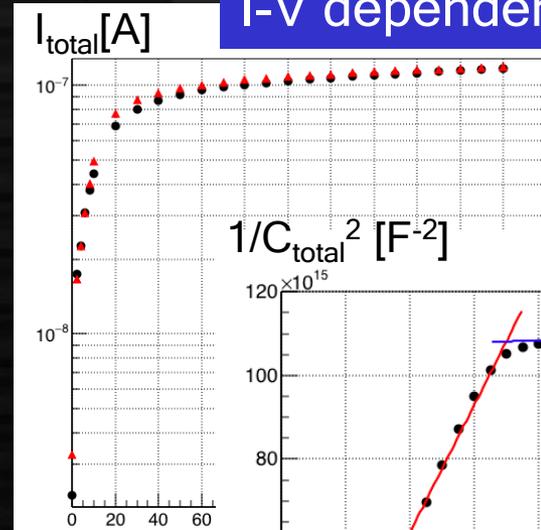
Probe station



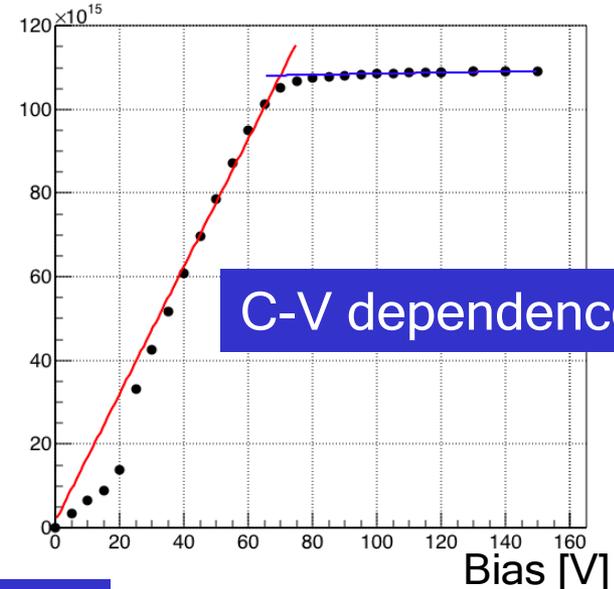
Probe card



I-V dependence



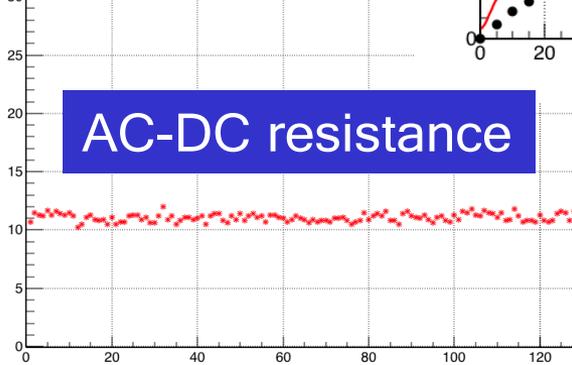
1/C<sub>total</sub><sup>2</sup> [F<sup>-2</sup>]



C-V dependence

R [Mohm]

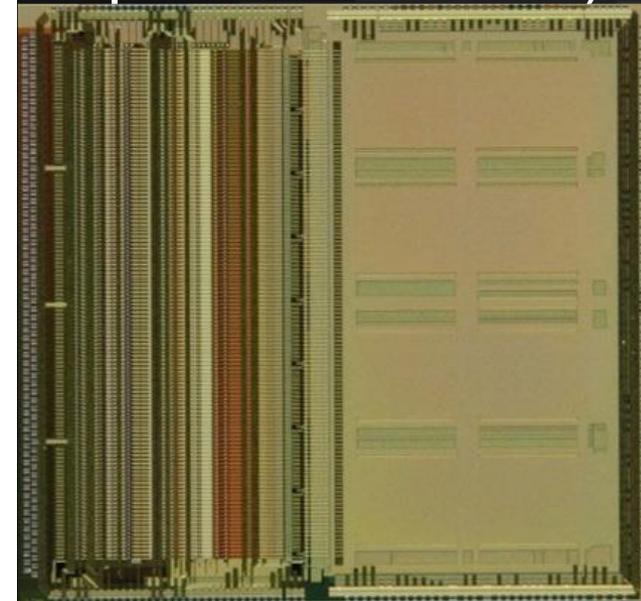
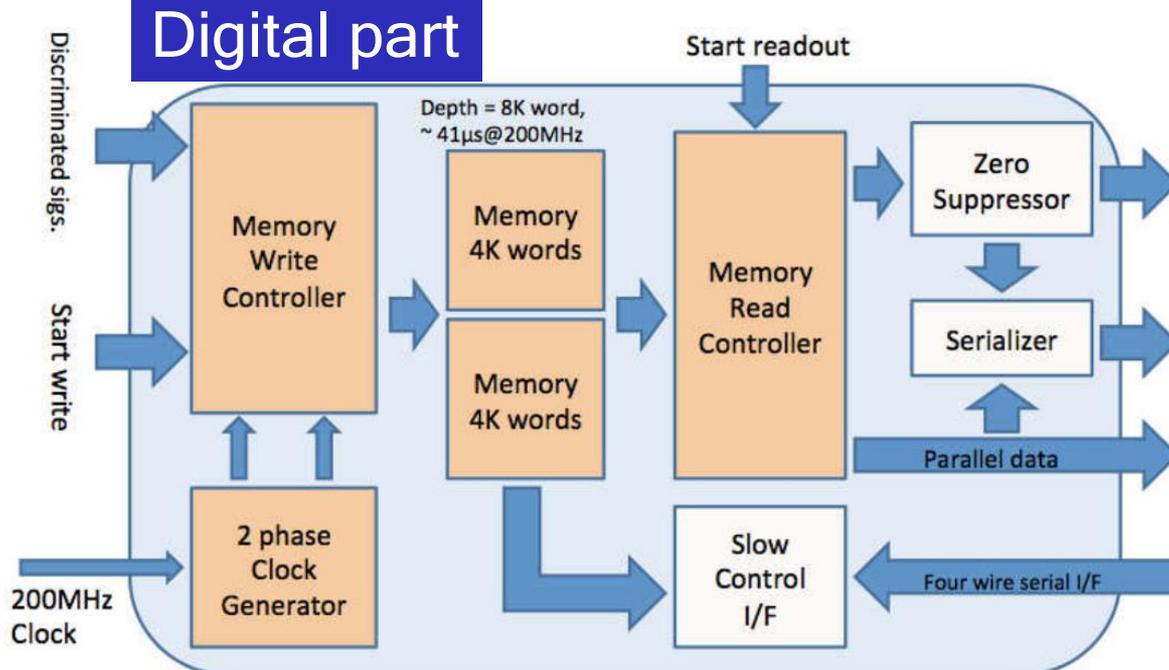
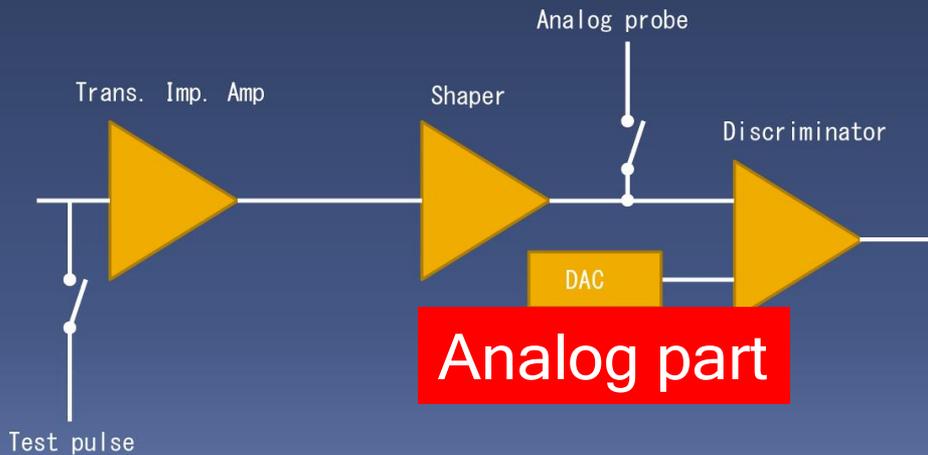
AC-DC resistance



Good quality:  
only a few bad  
channels  
per 200 sensors

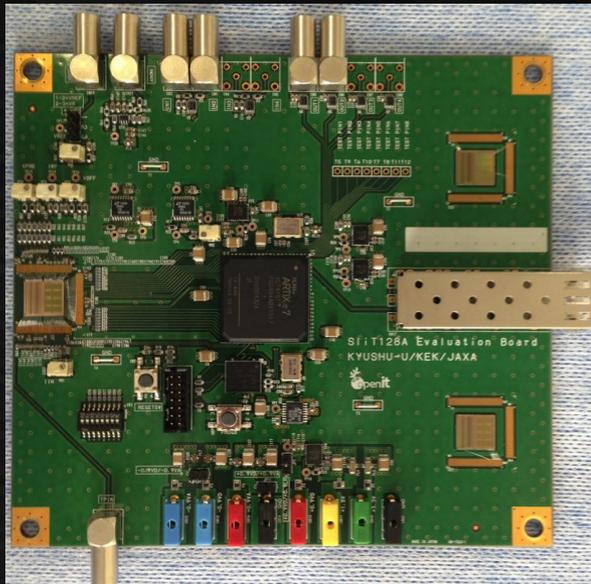
# Readout ASIC: SiT128A (2015)

128 ch A/D mixed ASIC  
200 MHz binary digitizer  
for 5 ns timing & ToT  
8 K words (41  $\mu$ s active time)  
Silterra 0.18  $\mu$ m process  
Modification ongoing (TEG  
chip will come soon)



SiT128A (9 x 10 mm)

# SiT128A evaluation board



Two types of evaluation boards developed

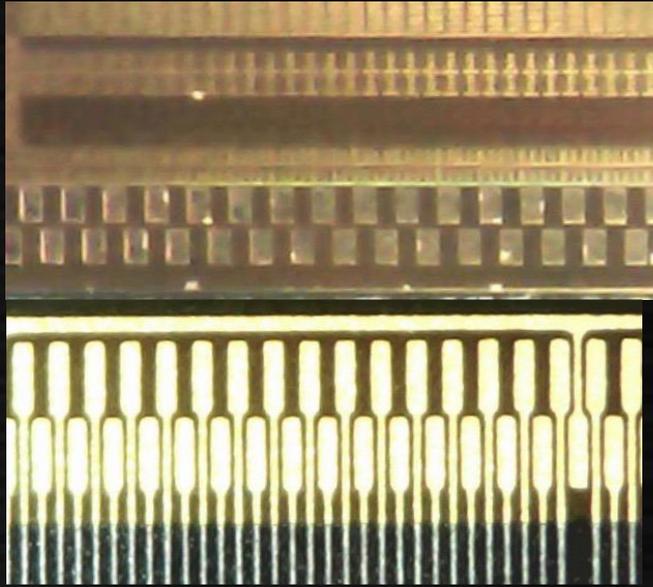
- Single SiT128A test board (2015)
  - Optimization of wire-bonding
  - Evaluation of SiT128A performance
- Multi SiT128A board (2016)
  - For test of sensor with real signal
  - Preparation of making real vanes

## Specifications

- 100/100  $\mu\text{m}$  (L/S) wire-bonding pads
- No capacitors under ASIC (for stable WB)
- Artix7 FPGA
- SiTCP readout with optical connector
- Voltage supply
  - 3.3V, 2.4V, 1.5V, 1.2V, 1.0V (FPGA)
  - 2.4V, +/- 0.9V (ASIC)



# FPC development



SLiT128A input pads:  
125  $\mu\text{m}$  pitch  
houndstooth pattern

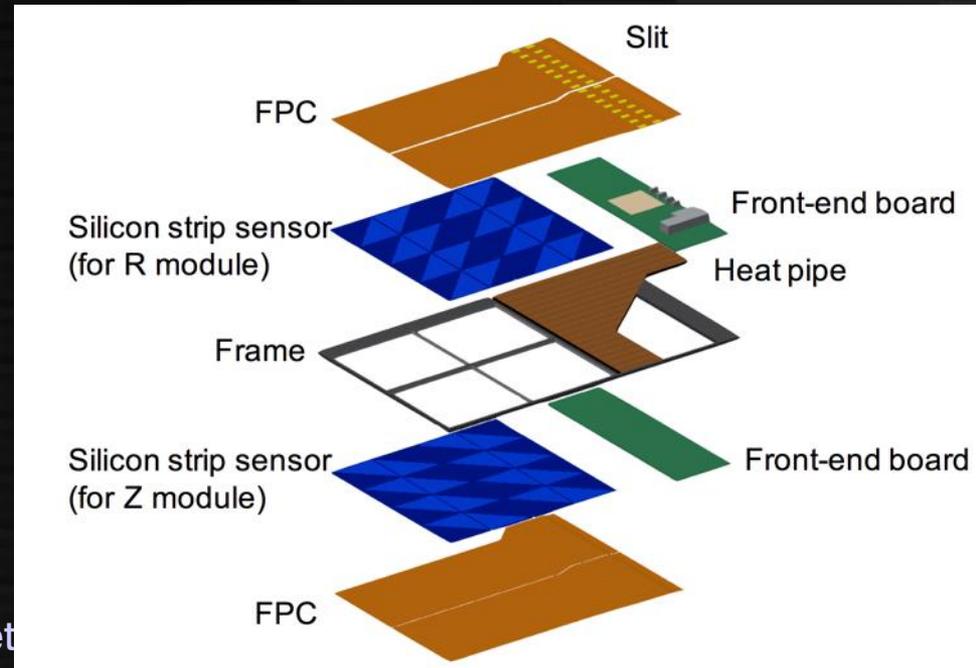
FPC (polyimide)  
pitch adapter  
Minimum spacing: 20  $\mu\text{m}$



Pitch adapter for SLiT128A multi

For making “real” vanes, we need large FPCs with fine pitch. Investigation on possible specification has started, aiming at 40-50  $\mu\text{m}$  line pitch

Taikan Suehara et

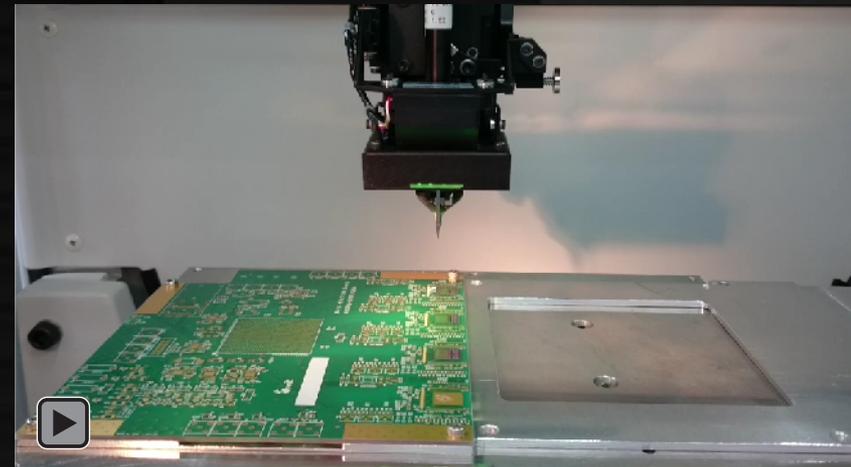
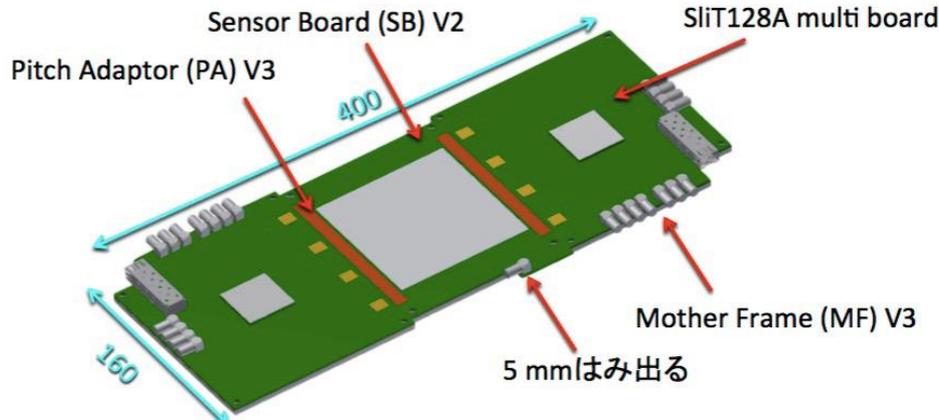


# Assembly of the test system

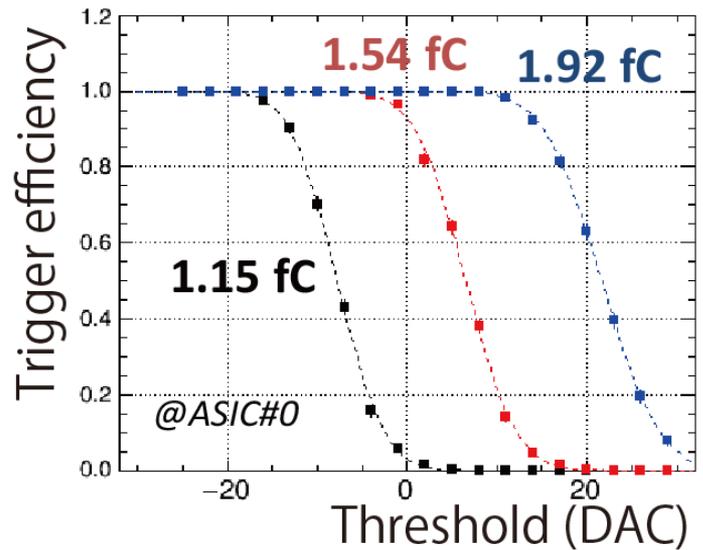
## Setup for WB



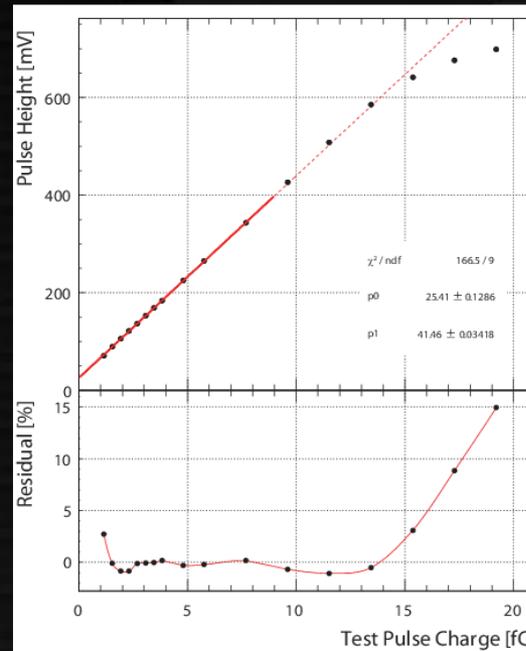
1. Fix sensor board and SliT128A multi board on mother frame on Al plate
2. Glue pitch adapter, ASIC and sensor
3. Wire bonding of sensor and PA, PA and ASIC, ASIC and multi board



# Performance evaluation

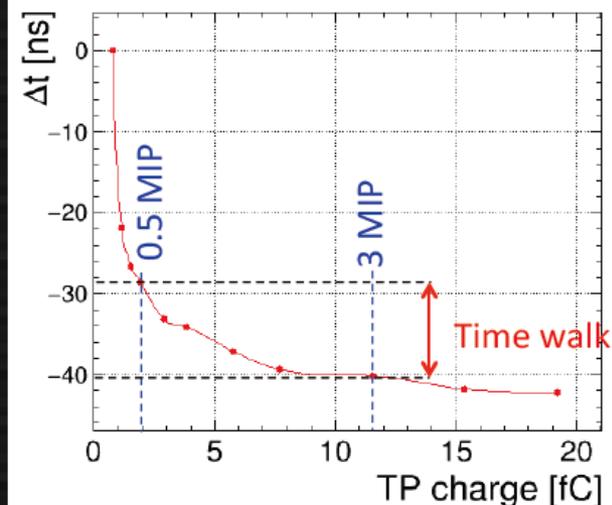


Noise measurement with test pulse by S-curve method: ENC =  $\sim 800e$  (S/N  $\sim 29$  on MIP)



Dynamic range:  
Linearity < 5 %  
up to 4 MIPs

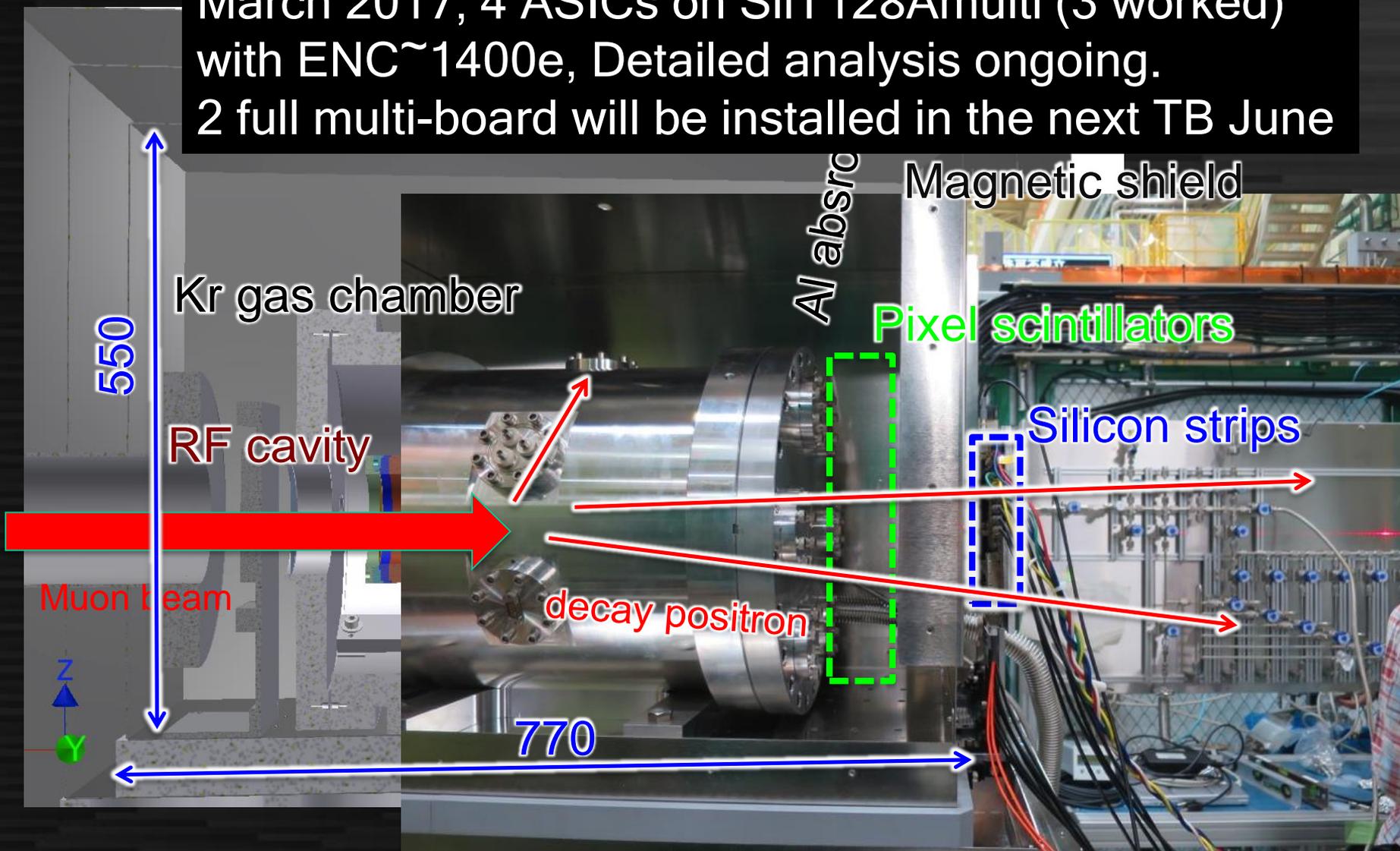
Time walk with 0.2 MIP ( $\sim 5000e$ ) threshold



Time walk of 11.5 ns (0.5-3 MIP) observed: try to improve in the next version (< 5 ns preferred)

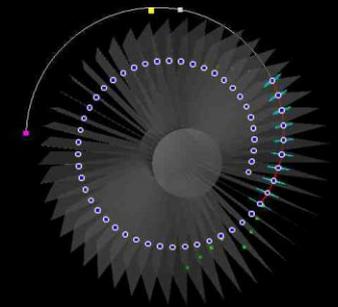
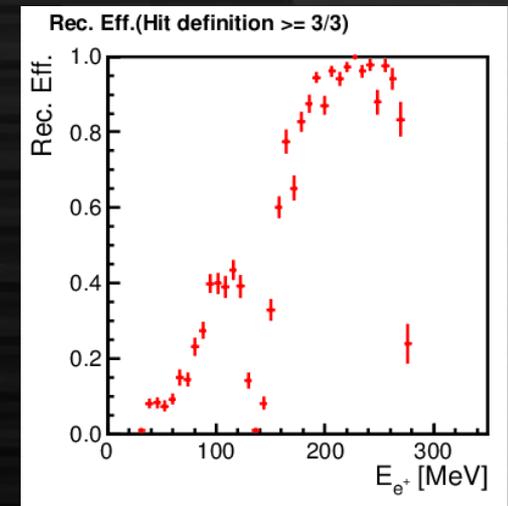
# Beam test on Mu-HFS (MuSEUM) exp.

March 2017, 4 ASICs on SiT128A multi (3 worked) with ENC ~ 1400e, Detailed analysis ongoing.  
2 full multi-board will be installed in the next TB June



# Other developments

- DAQ
  - Based on DAQ middleware (KEK)
  - Synchronization on multiple FPGA under work
- Tracking software
  - Modular framework under study
  - Hough transform for track finding
  - Kalman filter (GenFit) for tracking
- Alignment
  - Laser alignment (with freq-comm technique) under study
- Timing synchronization with GPS
- Thermal study with novel heat pipe



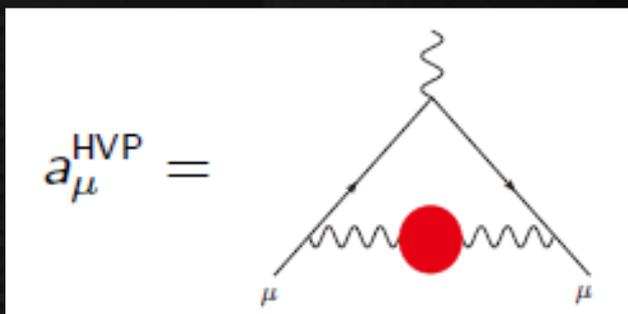
# Summary and Prospects

- J-PARC E34: muon  $g-2$ /EDM measurement with novel method complementary to BNL/FNAL exp.
  - Target date:  $\sim 2019$
- Silicon tracker is the main detector component
- $>200$  SSSD produced at HPK, with excellent quality
- SliT128A ASIC has been developed in KEK, meeting basic quality criteria, upgrade ongoing
- First detector prototype fabricated with a PCB with 4 ASICs connected with automatic wire-bonding
  - First test OK, preparing 2 layers/16 ASICs for MuSEUM run in June
- First real vane will be ready in  $\sim 1$  year



# Muon $g - 2$ : current status

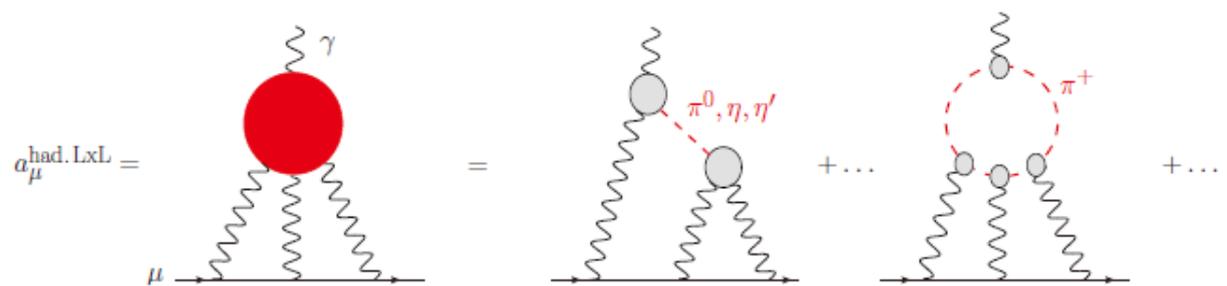
Contribution	$a_\mu \times 10^{11}$	Reference
QED (leptons)	116 584 718.853 $\pm$ 0.036	Aoyama et al. '12
Electroweak	153.6 $\pm$ 1.0	Gnendiger et al. '13
HVP: LO	6889.1 $\pm$ 35.2	Jegerlehner '15
NLO	-99.2 $\pm$ 1.0	Jegerlehner '15
NNLO	12.4 $\pm$ 0.1	Kurz et al. '14
HLbL	116 $\pm$ 40	Jegerlehner, AN '09
NLO	3 $\pm$ 2	Colangelo et al. '14
Theory (SM)	116 591 794 $\pm$ 53	
Experiment	116 592 089 $\pm$ 63	Bennett et al. '06
Experiment - Theory	295 $\pm$ 82	3.6 $\sigma$



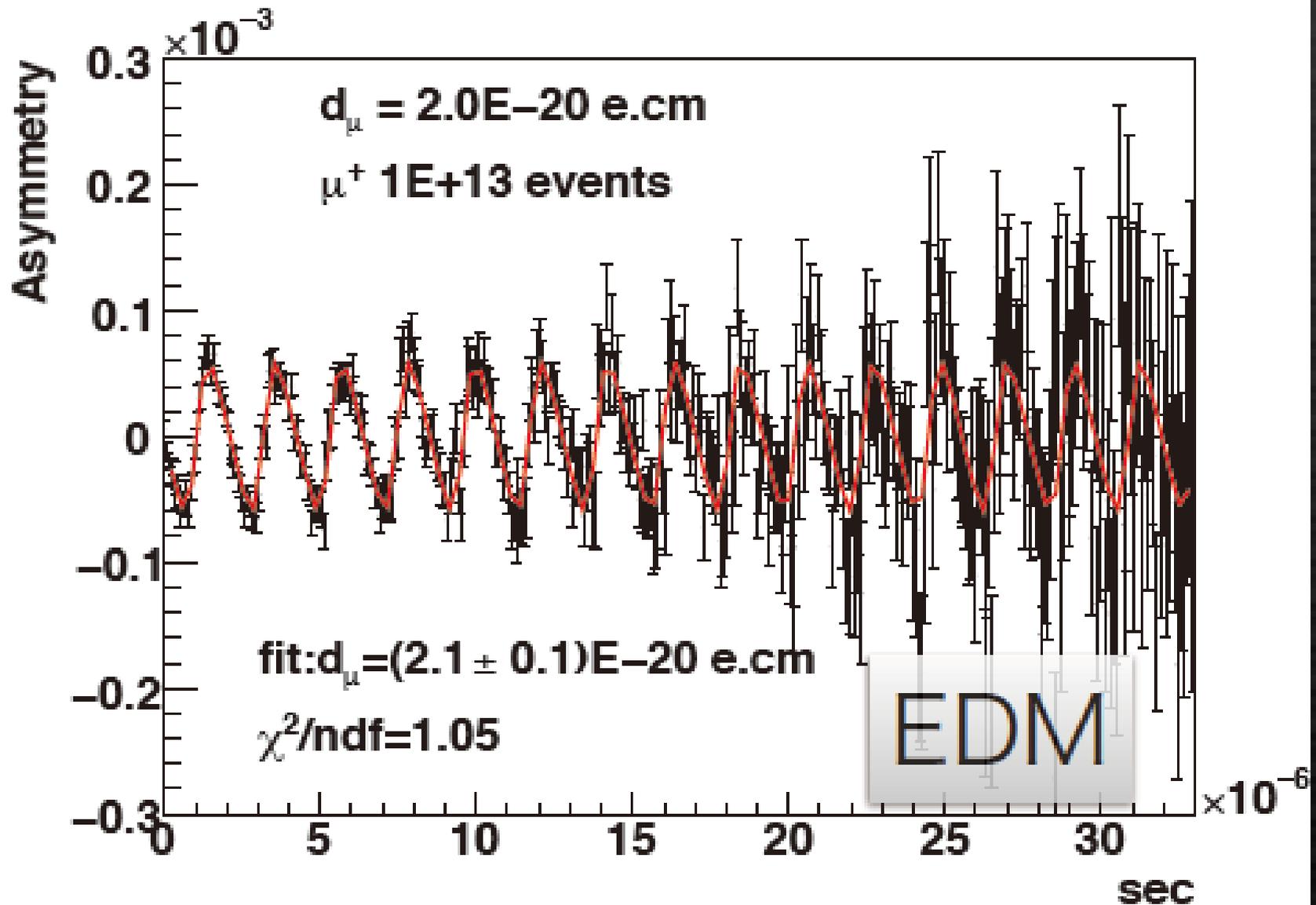
$$a_\mu^{\text{HVP}} = \frac{1}{3} \left( \frac{\alpha}{\pi} \right)^2 \int_0^\infty \frac{ds}{s} K(s) R(s),$$

$$R(s) = \frac{\sigma(e^+e^- \rightarrow \gamma^* \rightarrow \text{hadrons})}{\sigma(e^+e^- \rightarrow \gamma^* \rightarrow \mu^+\mu^-)}$$

Hadronic light-by-light scattering in muon  $g - 2$  from strong interactions (QCD):



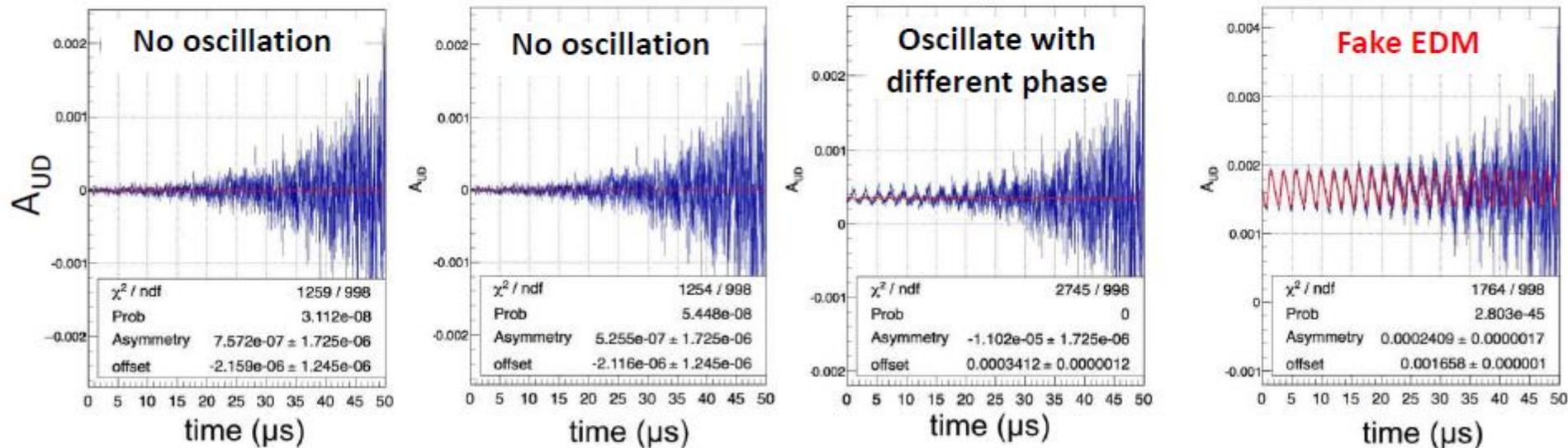
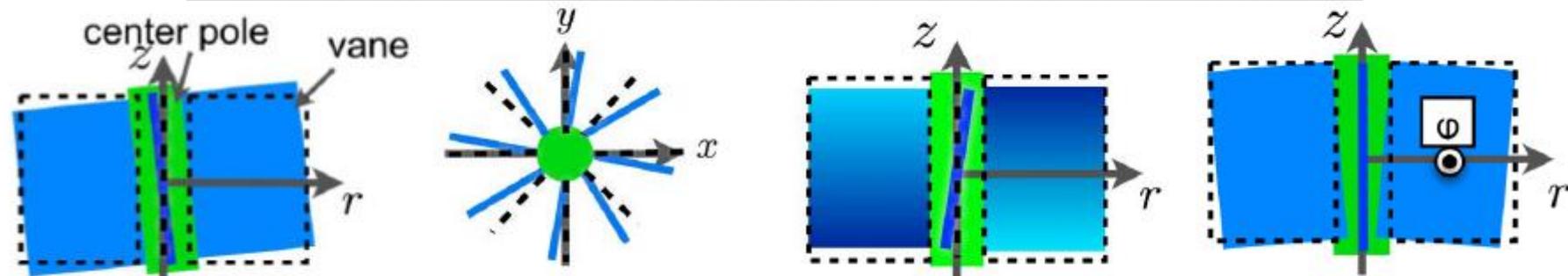
# EDM signature



# Fake EDM Signal by Misalignment

- EDM is measured from up-down asymmetry “ $A_{UD}$ ”.

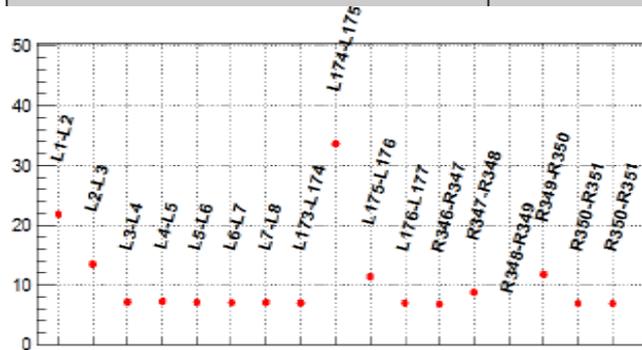
## Simulation with 1 mrad misalignment and null EDM signal



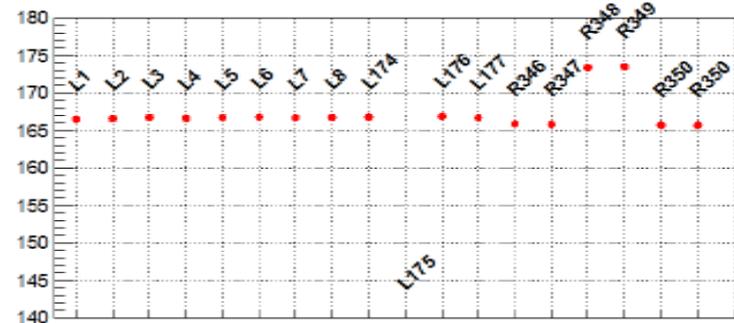
- The alignment must be controlled with  $10 \mu\text{rad}$  accuracy to measure EDM with  $10^{-21} e \cdot \text{cm}$  (final goal).

# Silicon sensor DC characteristics

測定項目	測定結果	理論値
I-V	plateau were observed	-
C total	3050 pF	3100 pF
Full depletion voltage	~ 80 V	~ 80 V
C interstrip	7.1 pF	3.0 pF + $\alpha$
Detector Capacitance	17 pF	9 pF + $\alpha$
C coupling	167 pF	164 pF
R Polysilicon	~12 M $\Omega$	5~15 M $\Omega$



C interstrip



C coupling

# Facilities at Kyushu University

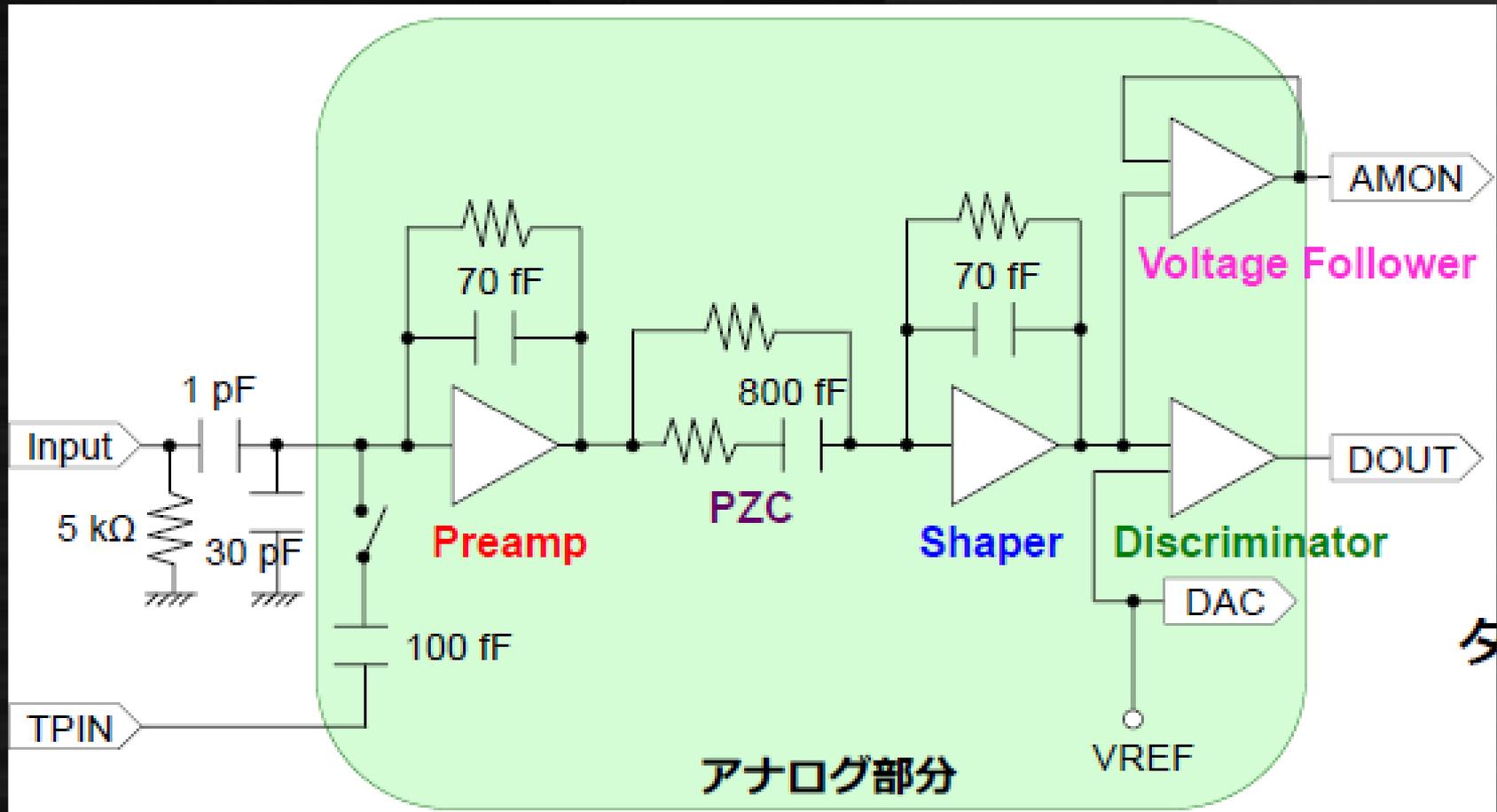
Automatic Bonder

Probe station

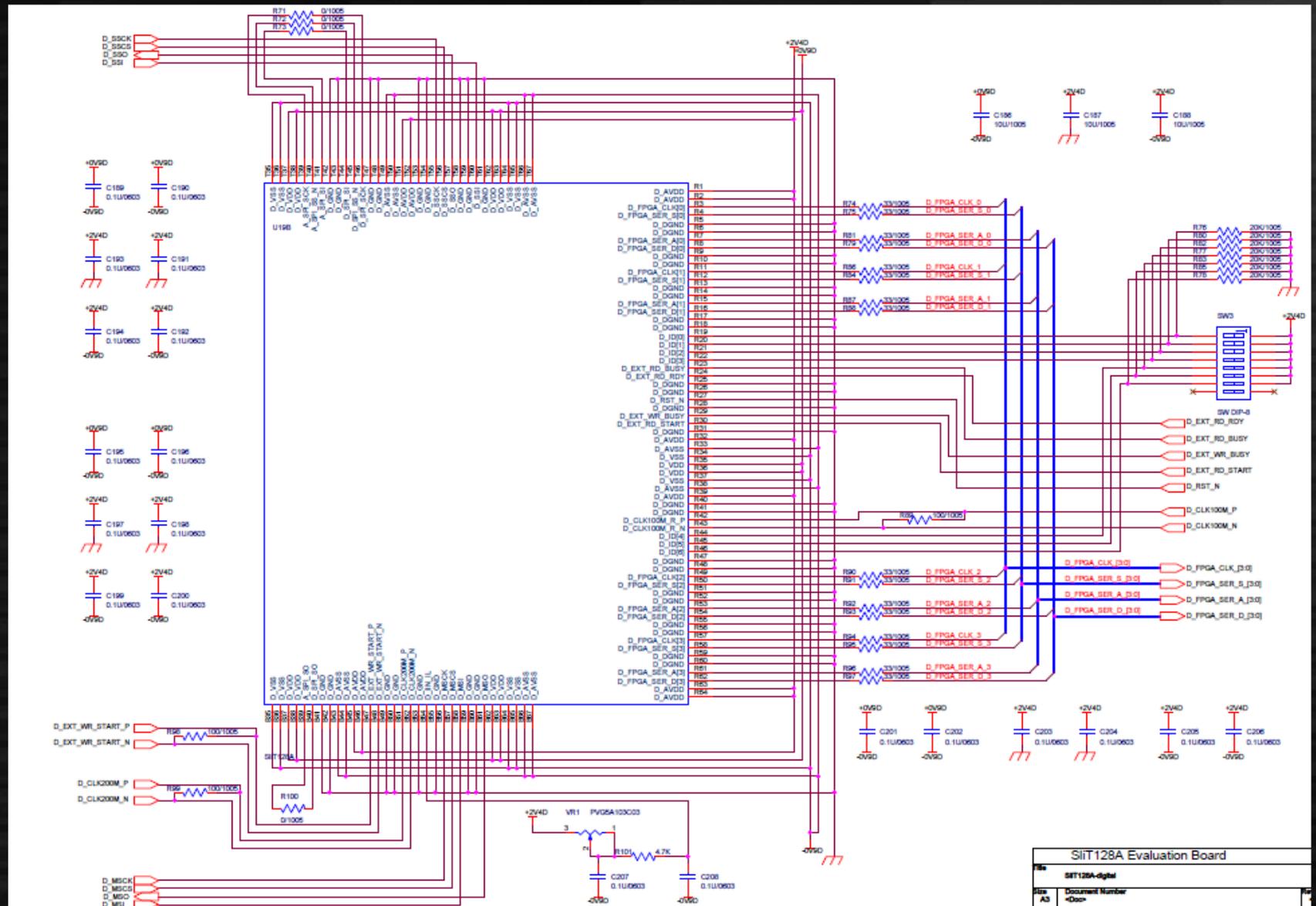
Manual Bonder

Bond tester

# SiT128A: analog part

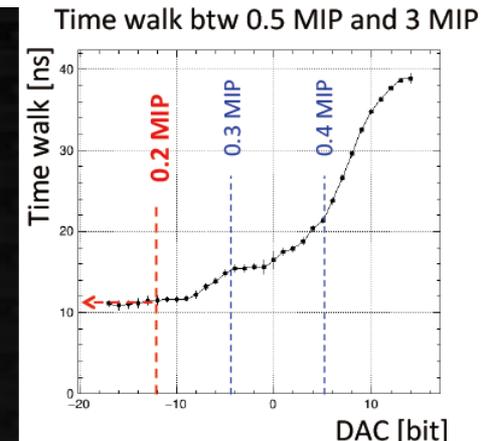
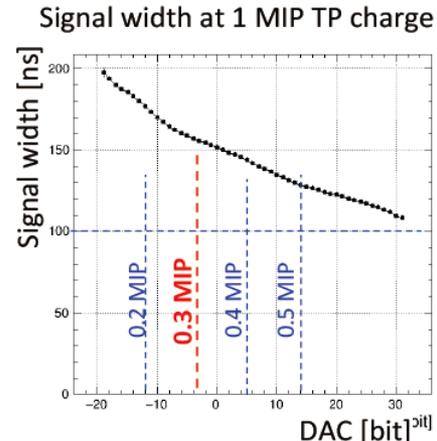
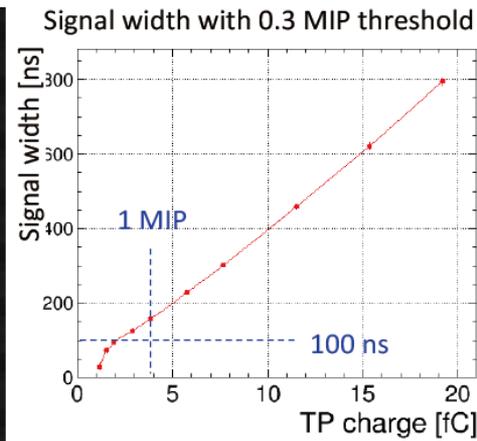


# SIIT128A: digital I/F

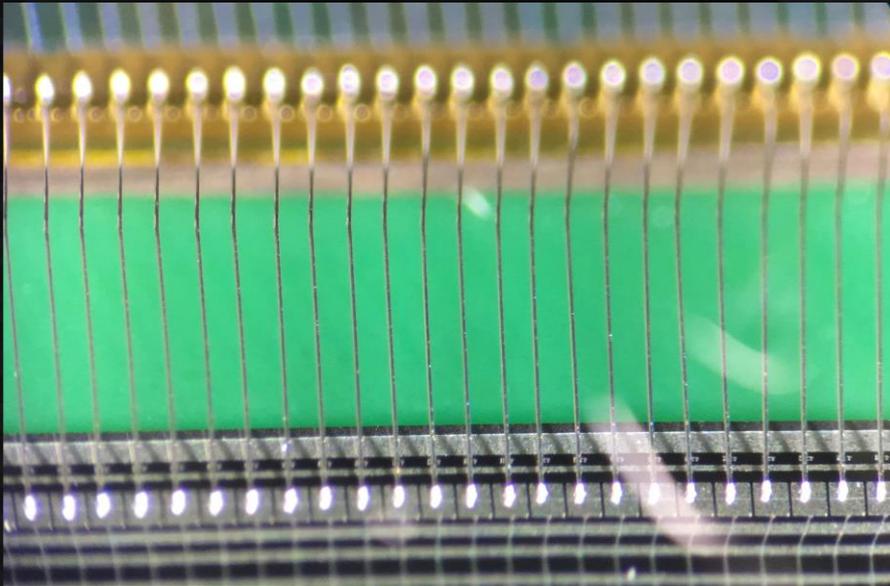


# SiT128A without sensor

Parameter	Requirement	SiT128A TEG Simulation	SiT128A TEG Result	SiT128A Result
Dynamic range	~ 3 MIP	~4 MIP	~3 MIP	<b>4 MIP</b>
ENC	< 1600 e	1210 e	1070 e	<b>430 e</b>
Pulse width (1 MIP)	< 100 ns	53.5 ns	96.0 ns	<b>155 ns</b>
Time walk (0.5 - 3 MIP threshold)	< 5 ns	6.5 ns	14.6 ns	<b>11.5 ns</b>
Power consumption	5 mW/ch	-	-	<b>0.44 W/chip</b>



# Wire bonding



25  $\mu\text{m}$  aluminum wire  
Bonding force: 8-9 g

