

The Research on High-speed Readout of Front-end Large-scale Data

Introduces a method of high-speed readout of front-end large-scale data based on the new JESD204B commercial ADC to FPGA transmission protocol. Design a front-end acquisition source board, to achieve the board to the back-end data transmission system. The JESD204B protocol is simulated by FPGA, which provides the principle support for front-end ASIC with high-speed data readout.

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