

# Characterization of the prototype CMOS pixel sensor JadePix-1 for the CEPC vertex detector



Liejian Chen<sup>1,2</sup>, Jia Tao<sup>1,2</sup>, Hongbo Zhu<sup>1,2</sup>, Ying Zhang<sup>1,2</sup>, Xiacong Ai<sup>1,3</sup>, Yi Liu<sup>3</sup>, Chenfei Yang<sup>4</sup>, Ryuta Kiuchi<sup>1,2</sup>, Xin Shi<sup>1,2</sup>, Ke Wang<sup>1,2</sup>, Na Wang<sup>1,2</sup>, Zhenan Liu<sup>1,2</sup>, Qun Ouyang<sup>1,2</sup>, Xinchou Lou<sup>1,2</sup>



<sup>1</sup>Institute of High Energy Physics, CAS

<sup>2</sup>State Key Laboratory of Particle Detection and Electronics

<sup>3</sup>Deutsches Elektronen-Synchrotron DESY

<sup>4</sup>University of Science and Technology of China

## Abstract

The proposed Circular Electron Positron Collider (CEPC) will allow measurement of the Higgs properties with precision beyond the (HL-)LHC. To meet the stringent physics requirements, its vertex detector will have to be constructed with the state-of-the-art pixel detector technologies that promise high spatial resolution, low power consumption and low material budget. We have conducted R&D based on the emerging CMOS pixel sensor technology and developed the first prototype JadePix-1. In this poster, we describe the sensor structures that are primarily designed to evaluate the impacts of diode geometry on charge collection. We present the detailed test results obtained with radioactive sources and electron beams, and the sensor performance before and after neutron irradiation up to  $10^{15}$  1 MeV  $n_{eq}/cm^2$ .

## Introduction

The CEPC is proposed to measure the Higgs boson properties as precise as possible, which will impose stringent requirements on the CEPC detector performance. The innermost vertex detector will play a critical role in heavy-flavor tagging. Therefore, the CEPC vertex detector must be facilitated with silicon pixel sensors with high spatial resolution ( $\sigma_{sp} \sim 3 \mu m$ ), fast readout (20  $\mu s$ ), low material budget (0.15 %  $X/X_0$  per layer) and radiation tolerance (TID 1Mrad/y, NIEL  $2 \times 10^{12}$  1 MeV  $n_{eq}/cm^2/y$ ). Monolithic active pixel sensors (MAPS), fabricated with standard CMOS processes, have already demonstrated its excellent performance in several high energy physics experiments. We have conducted R&D and developed the first prototype JadePix-1. As illustrated in Figure 1, the readout electronics is integrated with the sensitive volume on the same silicon substrate.

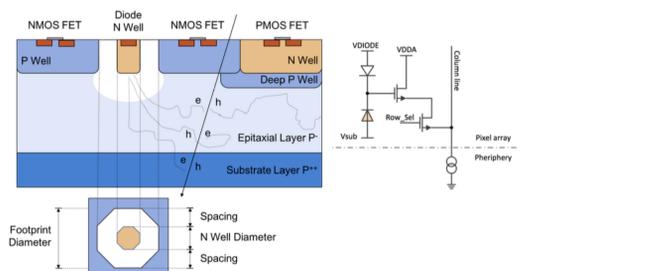


Figure 1. Cross sectional view of a CMOS pixel sensor and schematic of its in-pixel readout circuitry. Charges generated by the passing through charged particle are collected by the diode N-well mostly by diffusion.

The collecting diode consists of the collecting N-well and the surrounding P-well. Footprint defined as the total area formed by the surrounding P-well. As listed in Table 1, several geometry parameters are implemented to evaluate the collected signal charge over the equivalent input capacitance ratio (Q/C), which is critical for reducing the analogue power consumption.

Sector	Diode Surface	Footprint
A1	4 $\mu m^2$	30 $\mu m^2$
A2	8 $\mu m^2$	30 $\mu m^2$
A3	15 $\mu m^2$	30 $\mu m^2$
A4	4 $\mu m^2$	20 $\mu m^2$
A5	4 $\mu m^2$	15 $\mu m^2$

Table 1. Diode geometries implemented in JadePix-1.

## DAQ System

The data acquisition (DAQ) system consists of "Three-Part" structures, as shown in Figure 2. The analog signal generated in sensor is first amplified on the daughter board, and then converted to digital signal on the mother board. Data processed on the KC705 evaluation board are transmitted to PC via PCIe. A modern multi-thread C++ software with Qt based online GUI take data automatically.

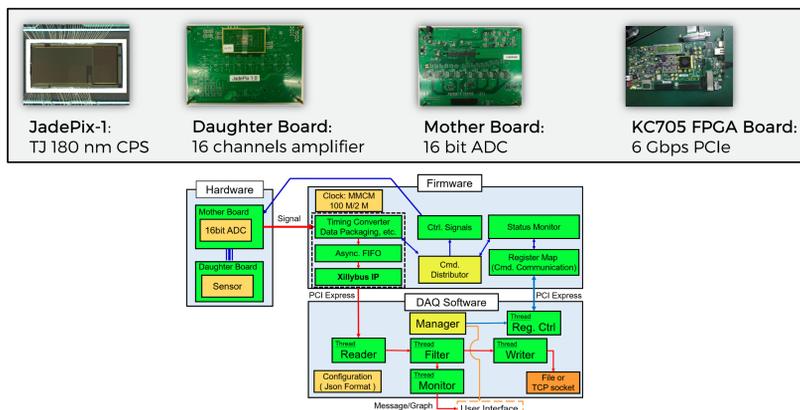


Figure 2. Components and the architecture of the DAQ system.

## Offline Analysis

The Correlated Double Sampling (CDS) method is used to suppress noise and extract the signal, as illustrated in Figure 3a, 3b and 3c. Noise can be measured with or without radioactive source and results are shown in Figure 3d, 3e and 3f.

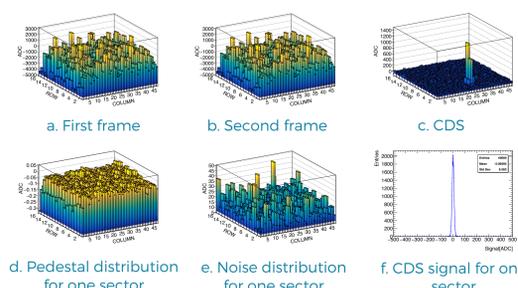


Figure 3. Correlated Double Sampling method and the obtained results.

## Results

### Tests with low-energy X-rays

<sup>55</sup>Fe is used to calibrate the pixel gain based on the assumptions that the charges with X-ray hitting on diode is complete conversion while the charges with X-ray hitting other place disperse slowly towards diode on thermal. The phenomenon is verified with AllPix<sup>2</sup> and TCAD simulation, as illustrated in Figure 4.

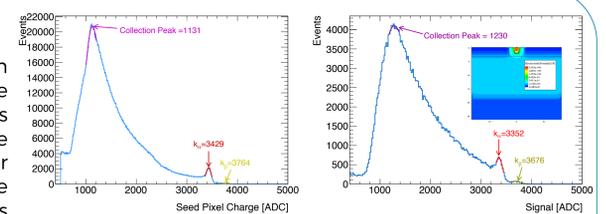


Figure 4. Example of seed signal distribution from experiment and simulation

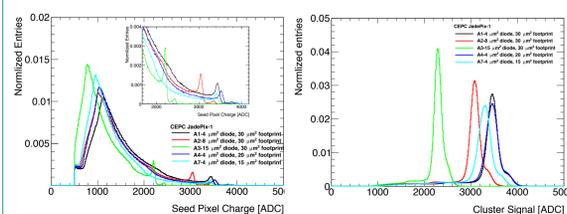


Figure 5. JadePix-1 response to X-rays from <sup>55</sup>Fe source for pixels with 33  $\mu m$  pitch

Two calibration peaks can always be recognized from the spectra of the seed pixels for different diode geometries. Charge spreading into neighboring pixels can be studied by grouping the specific pixels (cluster). Seed and cluster charge distributions are shown in Figure 5. The cluster charge collection efficiencies are shown in Table 2.

### Sensor performance after irradiation

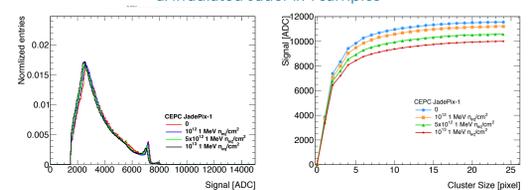


Figure 6. JadePix-1 response to <sup>55</sup>Fe source before and after neutron irradiation

JadePix-1 samples were sent to pulsed neutron reactors for neutron irradiation to  $10^{12}$ ,  $5 \times 10^{12}$ , and  $10^{15}$  1 MeV  $n_{eq}/cm^2$  fluences. The irradiated sensors are also characterized with <sup>55</sup>Fe source. Even for small diode array, A1, the cluster charge decrease less than 15%, shown in Figure 6.

### Tests with electron beams

JadePix-1 has been characterized with electron test beam at DESY in Hamburg, Germany. Preliminary results show that spatial resolutions better than 5  $\mu m$  and 3.5  $\mu m$  can be achieved for pixel sizes of  $33 \times 33 \mu m^2$  and  $16 \times 16 \mu m^2$ . And there is no significant degradation after irradiation. The most probable values estimated from the Landau-like shape of seed signal distributions are used to calculate signal to noise, shown in Table 2.

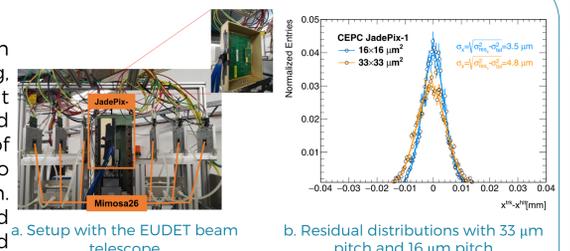


Figure 7. JadePix-1 with the DESY test beam

Sector	CVF [ $\mu V/e$ ]	CCE (Radioactive source)	S/N (Test Beams)
A1	33.4	94.39%	64
A2	29.0	95.23%	64
A3	20.8	97.09%	52

Table 2. Summary the JadePix-1 characterizations

## Conclusion

We have developed the first prototype JadePix-1 according to the requirements of the CEPC vertex detector. The sensors have been characterized by radioactive resources and the electron test beam by using our designed DAQ system. The performance evaluation of irradiated sensors are critically conducted in the next step.

## Acknowledgements

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