

Mini-workshop for CEPC MOST silicon project

Thursday, 23 November 2017

Sensor design - Afternoon session: Main Building A419 (09:30 - 10:30)

-Conveners: ying zhang; Wei WEI Wei; Weiguo Lu; Liang Zhang; Wu Gao; 赵梅

time	[id] title	presenter
09:30	[18] Status of CMOS pixel sensor design for CEPC vertex detector R&D	Ms ZHANG, ying
09:50	[20] Research Program of Front-End Microelectronics and Proposal for MAPS Readout	Prof. GAO, Wu
10:10	[19] Some considerations on the CMOS sensors for CEPC MOST-2	Mr WEI WEI, Wei