

GERO: 一款 TPC 通用读出 ASIC

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Abstract: The paper presents a prototype ASIC (GERO-NSP): which is aimed to provide generic readout solution for the Time Projection Chambers (TPC). The 16-channel GERO would provide high sampling frequency, configurable storage depth, and digitalization of data to meet various needs in different experiments. GERO was fabricated in 0.18um CMOS process. The circuit design and test results of the first prototype will be discussed as well as the improvements made on the new version.

Primary author: Ms 赵, 馨远 (清华大学)

Co-author: Mr LIU, Feng (Dept. of Tsinghua University)

Presenter: Ms 赵, 馨远 (清华大学)

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