

# Status in this week

## CMOS pixel sensor

- Starting the update toward “B” sector
- Main changes is the number of rows (48 for “A” and 96 for “B” )
- Some of functions use their algorithm assuming “48”, and those should be modified. ( In future, it maybe better to make the firmware operation with both of “A” & “B” )
- Asking the purchase of “mother/daughter” board

## CEPC Higgs->ZZ\* analysis

- Confirmation works with Yongfeng
- found different cut conditions, FastJet handling problem etc.

( I will prepare 2<sup>nd</sup> quarter review slide later)